Vishay Semiconductors

Hyperfast Rectifier, 30 A FRED Pt[®] G5



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LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS								
I _{F(AV)}	30 A							
V _R	600 V							
V _F at I _F at 125 °C	1.6 V							
t _{rr} (typ.)	20							
I _{FSM}	280							
T _J max.	175 °C							
Package	TO-247AD 2L							
Circuit configuration	Single							

FEATURES

- Hyperfast and optimized Q_{rr}
- Best in class forward voltage drop and switching losses trade off
- Optimized for high speed operation
- 175 °C maximum operating junction temperature
- Polyimide passivation
- AEC-Q101 qualified meets JESD 201 class tin whisker 2 test
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

Featuring a unique combination of low conduction and switching losses, this rectifier is the right choice for soft switched and resonant converters, as well as medium frequency hard switching converters. This device is specifically designed to improve efficiency of high speed LLC output rectification stages of EV / HEV on-board battery chargers

MECHANICAL DATA

Case: TO-247AD 2L

Molding compound meets UL 94 V-0 flammability rating **Terminal:** matte tin plated leads, solderable per J-STD-002

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS					
Repetitive peak reverse voltage	V _{RRM}		600	V					
Average rectified forward current	I _{F(AV)}	T _C = 107 °C, D = 0.50	30						
Non-repetitive peak surge current	I _{FSM}	T_{C} = 25 °C, t_{p} = 10 ms, sine wave	280	А					
Repetitive peak forward current	I _{FRM}	T _C = 107 °C, D = 0.50, f = 20 kHz	60						
Operating junction and storage temperature	T _J , T _{Stg}		-55 to +175	С°					

ELECTRICAL SPECIFICATIONS ($T_J = 25 \text{ °C}$ unless otherwise specified)									
PARAMETER SYMBOL TEST CONDITIONS MIN. TYP. MAX. U									
Breakdown voltage, blocking voltage	V_{BR}, V_{R}	I _R = 100 μA	600	-	-				
Forward voltage	VF	I _F = 30 A	-	2.1	2.5	V			
	۷F	I _F = 30 A, T _J = 125 °C	-	1.6	-				
Poverse leekage ourrent	I _R	V _R = V _R rated	-	-	20				
Reverse leakage current		$T_J = 125 \text{ °C}, V_R = V_R \text{ rated}$	-	-	500	μA			
Junction capacitance	CT	V _R = 200 V	-	36	-	pF			
Series inductance	L _S	Measured to lead 5 mm from package body	-	8	-	nH			

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 1
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DYNAMIC RECOVERY CHARACTERISTICS (T_J = 25 °C unless otherwise specified)										
PARAMETER	SYMBOL	TEST CO	ONDITIONS	MIN.	TYP.	MAX.	UNITS			
		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 10$	00 A/µs, V _R = 30 V	-	20	-				
Reverse recovery time	t _{rr}	T _J = 25 °C		-	35	-	ns			
		T _J = 125 °C		-	46	-				
Peak recovery current	I	T _J = 25 °C	I _F = 20 A dI _F /dt = 1000 A/µs V _R = 400 V	-	10	-	A			
	I _{RRM}	T _J = 125 °C		-	18	-				
Daviene in a company all and	Q _{rr}	T _J = 25 °C		-	115	-	nC			
Reverse recovery charge		T _J = 125 °C	-	-	560	-				
Boyeroo receivery time	t _{rr}	T _J = 25 °C		-	39	-	ns			
Reverse recovery time		T _J = 125 °C		-	49	-	115			
Deals receiver a surrent	I _{RRM}	T _J = 25 °C	l _F = 30 A dl _F /dt = 1000 A/µs	-	10.5	-	А			
Peak recovery current		T _J = 125 °C	$V_{\rm B} = 400 \text{ V}$	-	20.5	-	A			
Davience was a view of a view	0	T _J = 25 °C		-	185	-	20			
Reverse recovery charge	Q _{rr}	T _J = 125 °C		-	650	-	nC			

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Thermal resistance, junction-to-case	R _{thJC}		-	-	1.1	°C/W			
Weight			-	5.5	-	g			
Mounting torque			6 (5)	-	12 (10)	kgf · cm (lbf · in)			
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	175	°C			
Marking device		Case style: TO-247AD 2L	E5PW3006LH						

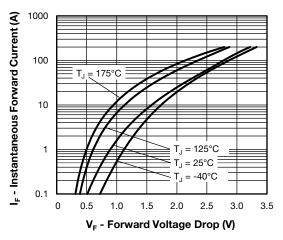


Fig. 1 - Typical Forward Voltage Drop Characteristics

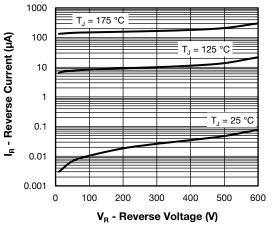
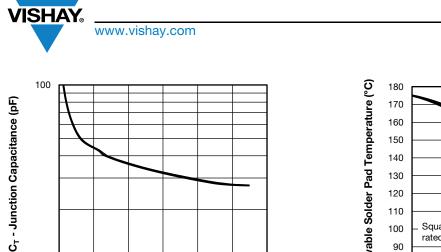


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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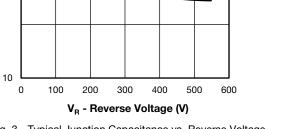
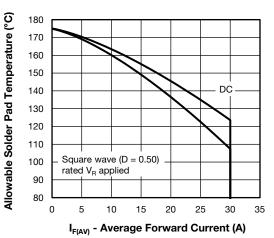


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage



VS-E5PW3006LHN3

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Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

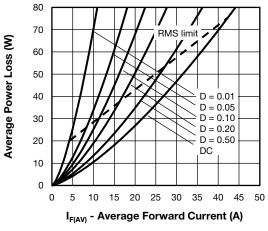


Fig. 5 - Average Power Loss vs. Average Forward Current

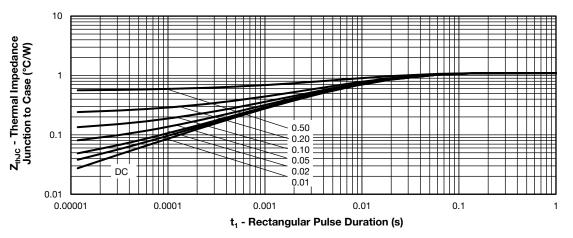


Fig. 6 - Maximum Thermal Impedance ZthJC - Characteristics

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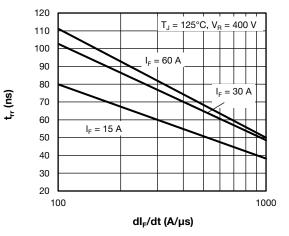


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

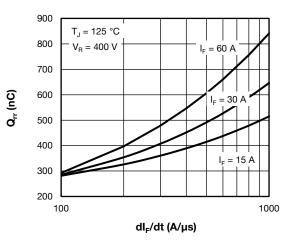


Fig. 8 - Typical Reverse Recovery Charge vs. dl_F/dt

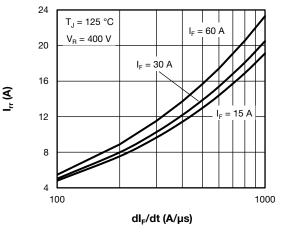


Fig. 9 - Typical Reverse Recovery Current vs. dl_F/dt

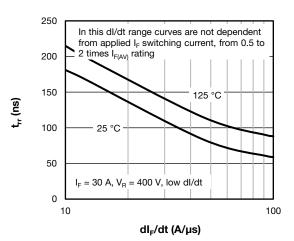


Fig. 10 - Typical Reverse Recovery Time vs. dl_F/dt

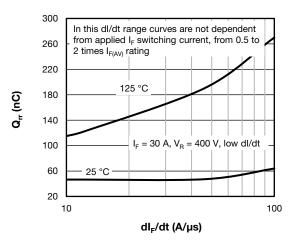


Fig. 11 - Typical Reverse Recovery Charge vs. dl_F/dt

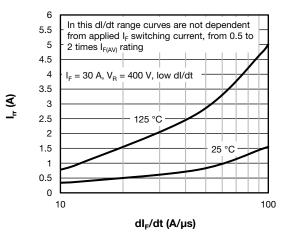


Fig. 12 - Typical Reverse Recovery Current vs. dl_F/dt

Revision: 02-Sep-2022

4

Document Number: 96959

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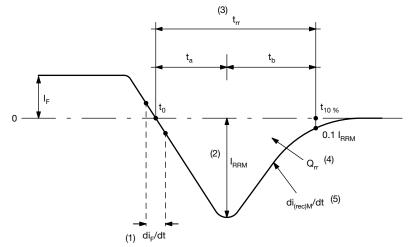


Fig. 13 - Reverse Recovery Waveform and Definitions

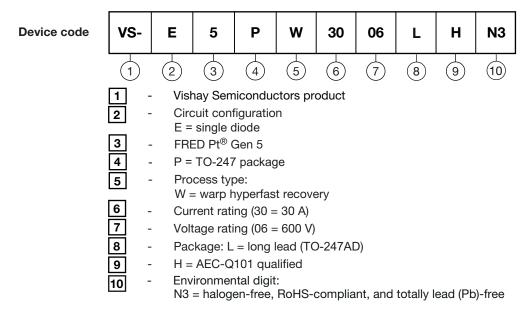
Notes

- (1) di_F/dt rate of change of current through zero crossing
- ⁽²⁾ I_{RRM} peak reverse recovery current
- $^{(3)}$ t_{rr} reverse recovery time measured from t₀, crossing point of negative going I_F, to point t_{10%}, 0.1 I_{RRM}
- $^{(4)}$ Q_{rr} area under curve defined by t_0 and $t_{10\ \%}$

$$Q_{rr} = \int_{t_0}^{t_{10\%}} I(t) dt$$

⁽⁵⁾ di_(rec)M/dt - peak rate of change of current during t_b portion of t_{rr}

ORDERING INFORMATION TABLE



ORDERING INFORMATION (Example)										
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION							
VS-E5PW3006LHN3	25	500	Antistatic plastic tube							

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?95536
Part marking information	www.vishay.com/doc?95648
Revision: 02-Sep-2022	5 Document Number: 96959

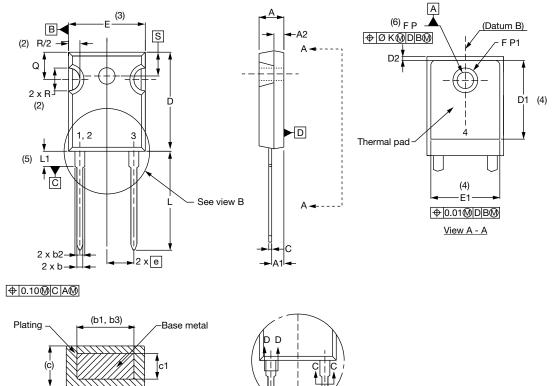
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TO-247AD 2L

DIMENSIONS in millimeters and inches



Section C - C, D - D

(b, b2)

(4)

View	<u>/ B</u>

SYMBOL	MILLIN	MILLIMETERS INCHES NOTES		SYMBOL		INCHES		NOTES				
STIVIDUL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTES	STMDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.65	5.31	0.183	0.209			E	15.29	15.87	0.602	0.625	3
A1	2.21	2.59	0.087	0.102			E1	13.46	-	0.53	-	
A2	1.50	2.49	0.059	0.098			е	5.46	BSC	0.215	BSC	
b	0.99	1.40	0.039	0.055			ØК	0.2	254	0.0)10	
b1	0.99	1.35	0.039	0.053			L	19.81	20.32	0.780	0.800	
b2	1.65	2.39	0.065	0.094			L1	3.71	4.29	0.146	0.169	
b3	1.65	2.34	0.065	0.092			ØР	3.56	3.66	0.14	0.144	
С	0.38	0.89	0.015	0.035			Ø P1	-	6.98	-	0.275	
c1	0.38	0.84	0.015	0.033			Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3		R	4.52	5.49	0.178	0.216	
D1	13.08	-	0.515	-	4		S	5.51	BSC	0.217	' BSC	
D2	0.51	1.35	0.020	0.053						•		•

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5M-1994

(2) Contour of slot optional

(3) Dimension D and E do not include mold flash. These dimensions are measured at the outermost extremes of the plastic body

(4) Thermal pad contour optional with dimensions D1 and E1

(5) Lead finish uncontrolled in L1

⁽⁶⁾ Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-247 with exception of dimension A min., D, E min., Q min., S, and note 4

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