ZYNQ-7000 EPP ZC702 EVALUATION KIT



POWERFUL EVALUATION KIT FOR THE ZYNO-7000 EXTENSIBLE PROCESSING PLATFORM

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Σ Xilinx Solution Highlights

- Productivity-boosting kit, including silicon, development tools, IP, and reference designs
- Wide feature set, with abundant I/O expandability, to develop solutions for most markets
- Foundation for Zynq-7000 EPP-based designs
- Best-in-class tools, operating system support, and ecosystem (including ARM community)

A New Class of Devices: Zynq-7000 Extensible Processing Platform

- ARM[®] dual-core Cortex[™]-A9 processor meets Xilinx 28nm programmable logic
- ASIC-like performance and power with the flexibility of an FPGA and ease of programming of a microprocessor
- Flexibility to create a custom ARMbased ASSP by adding peripherals and accelerators to the programmable logic

The Xilinx[®] ZYNQ[™]-7000 EPP ZC702 Evaluation Kit gives developers a complete development platform including hardware, development tools, IP, and pre-verified reference designs. The included targeted reference design jump-starts development, and enables in-depth exercising of the ARM processing system and Xilinx 28nm scalable optimized programmable logic architecture. Additional reference designs and industry-standard FPGA Mezzanine Connectors (FMCs) let developers scale and customize designs using Xilinx and third-party daughter cards to best fit their application and system with this development board. This evaluation kit offers a wide feature set and abundant I/O expandability, to shorten time to market for applications that call for industry-leading performance, feature differentiation, and power efficiency.

What's Inside the ZC702 Evaluation Kit

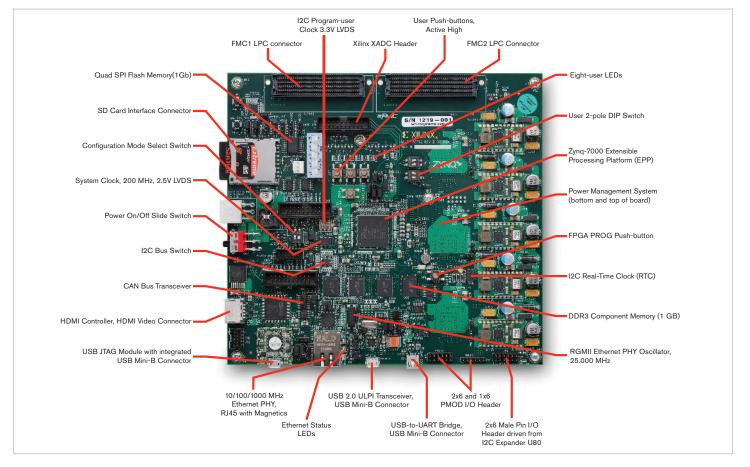
- ZC702 Evaluation Board featuring the XC7Z020-1CLG484CES device
- Agile Mixed Signal (AMS)
- Full seat ISE[®] Design Suite Embedded Edition
- Cables and Power Supply:
 - Universal 12V power supply
 - USB cables, Ethernet cable
 - 8 GB Class 4 SD card
- USB Flash Drive (contains documentation, designs, and demos)

Documentation, Designs, and Demos

- Step-by-Step Getting Started Guide
- Hardware User Guide
- Reference Design and Design Example User Guide
- Schematics and PCB files
- Demonstrations
- Board design files



ZC702 EVALUATION BOARD



Take the NEXT STEP

For more information about the ZC702 Evaluation Kit, please visit: www.xilinx.com/zc702

For more information about the Zynq-7000 EPP product family, please visit: www.xilinx.com/zynq

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