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April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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SILICON POWER TRANSISTOR



2SA1652

PNP SILICON EPITAXIAL TRANSISTOR FOR HIGH-SPEED SWITCHING

The 2SA1652 is a mold power transistor developed for highspeed switching and features a very low collector-to-emitter saturation. This transistor is ideal for use in switching power supplies, DC/DC converters, motor drivers, solenoid drivers, and other low-voltage power supply devices, as well as for high-current switching.

FEATURES

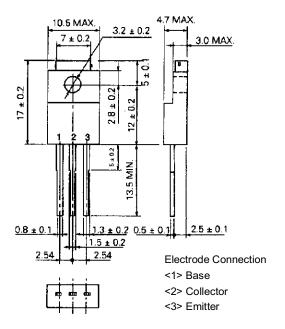
- Mold package that does not require an insulating board or insulation bushing
- · Fast switching speed
- Low collector-to-emitter saturation voltage: $V_{\text{CE(sat)}} \leq -0.3 \text{ V (MAX.) } @ \text{Ic} = -6 \text{ A}$

QUALITY GRADES

Standard

Please refer to "Quality Grades on NEC Semiconductor Devices" (Document No. C11531E) published by NEC Corporation to know the specification of quality grade on the devices and its recommended applications.

PACKAGE DRAWING (UNIT: mm)



ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

| Parameter | Symbol | Conditions | Ratings | Unit |
|------------------------------|--------------------|-------------------------------|-------------|------|
| Collector to base voltage | Vсво | | -150 | V |
| Collector to emitter voltage | VCEO | | -100 | V |
| Emitter to base voltage | VEBO | | -7.0 | ٧ |
| Collector current | Ic(DC) | | -10 | Α |
| Collector current | IC(pulse) | PW ≤ 300 µs, duty cycle ≤ 10% | -20 | Α |
| Base current | I _{B(DC)} | | -6.0 | Α |
| Total power dissipation | Рт | Tc = 25°C | 25 | W |
| Total power dissipation | Рт | Ta = 25°C | 2.0 | W |
| Junction temperature | Tj | | 150 | °C |
| Storage temperature | Tstg | | -55 to +150 | °C |

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ELECTRICAL CHARACTERISTICS (Ta = 25°C)

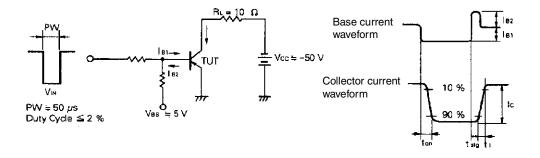
| Parameter | Symbol | Conditions | MIN. | TYP. | MAX. | Unit |
|------------------------------|-------------------------|---|------|------|------|------|
| Collector cutoff current | Ісво | Vcb = -100 V, IE = 0 | | | -10 | μΑ |
| Emitter cutoff current | ІЕВО | V _{EB} = -5 V, I _C = 0 | | | -10 | μΑ |
| DC current gain | h _{FE1} * | $V_{CE} = -2 \text{ V}, \text{ Ic} = -0.5 \text{ A}$ | 100 | | | _ |
| DC current gain | h _{FE2} * | Vce = -2 V, Ic = -2 A | 100 | | 400 | _ |
| DC current gain | h _{FE3} * | Vce = -2 V, Ic = -6 A | 60 | | | _ |
| Collector saturation voltage | V _{CE(sat)1} * | $I_C = -6 \text{ A}, I_B = -0.3 \text{ A}$ | | | -0.3 | V |
| Collector saturation voltage | V _{CE(sat)2} * | $I_C = -8 \text{ A}, I_B = -0.4 \text{ A}$ | | | -0.5 | V |
| Base saturation voltage | V _{BE(sat)1} * | $I_C = -6 \text{ A}, I_B = -0.3 \text{ A}$ | | | -1.2 | V |
| Base saturation voltage | V _{BE(sat)2} * | $I_C = -8 \text{ A}, I_B = -0.4 \text{ A}$ | | | -1.5 | V |
| Gain bandwidth product | f⊤ | $V_{CE} = -10 \text{ V}, \text{ Ic} = -0.5 \text{ A}$ | | 150 | | MHz |
| Collector capacitance | Cob | VcB = −10 V, IE = 0, f = 1 MHz | | 250 | | pF |
| Turn-on time | ton | Ic = -6 A, I _{B1} = $-I_{B2}$ = -0.3 A, R _L = 8.3 Ω , Vcc = -50 V Refer to the test circuit. | | 0.3 | | μs |
| Storage time | tstg | | | 1.5 | | μs |
| Fall time | t _f | | | 0.4 | | μs |

^{*} Pulse test PW \leq 350 μ s, duty cycle \leq 2%

hfe CLASSIFICATION

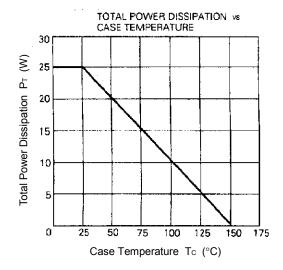
| Marking | М | L | K | |
|------------------|------------|------------|------------|--|
| h _{FE2} | 100 to 200 | 150 to 300 | 200 to 400 | |

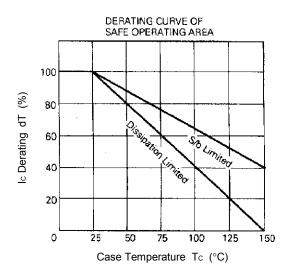
SWITCHING TIME TEST CIRCUIT

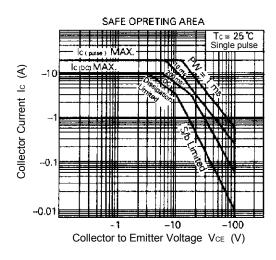


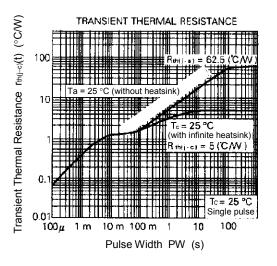


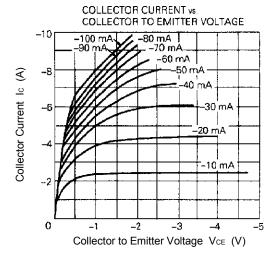
TYPICAL CHARACTERISTICS (Ta = 25°C)

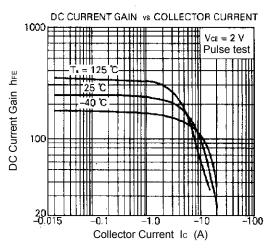


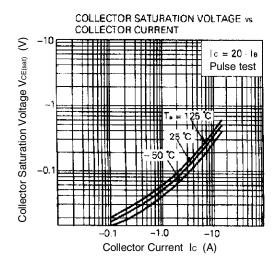


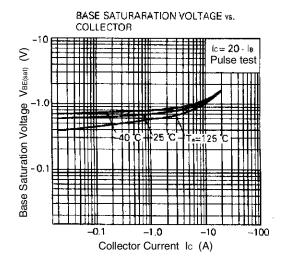


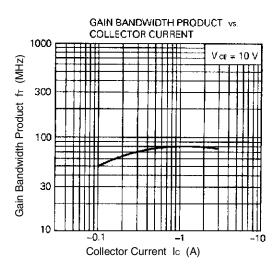


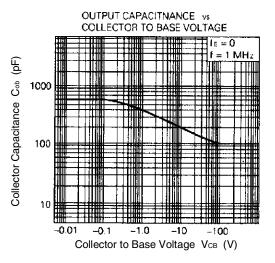


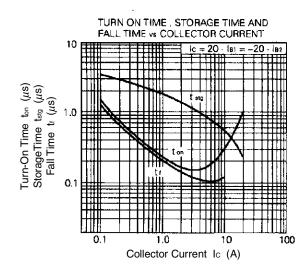












[MEMO]



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