

18-Line Low Capacitance SCSI Active Terminator

FEATURES

- Complies with SCSI, SCSI-2 and SPI-2 Standards
- 6pF Channel Capacitance during Disconnect
- 100µA Supply Current in Disconnect Mode
- Meets SCSI Hot Plugging Capability
- –650mA Sourcing Current for Termination
- +200mA Sinking Current for Active Negation
- Provides Active Termination for 18 Lines
- Logic Command Disconnects all Termination Lines
- Trimmed Termination Current to 5%
- Trimmed Impedance to 5%
- Current Limit and Thermal Shutdown
 Protection

DESCRIPTION

The UC5608 provides 18 lines of active termination for a SCSI (Small Computer Systems Interface) parallel bus. The SCSI standard recommends active termination at both ends of the bus cable.

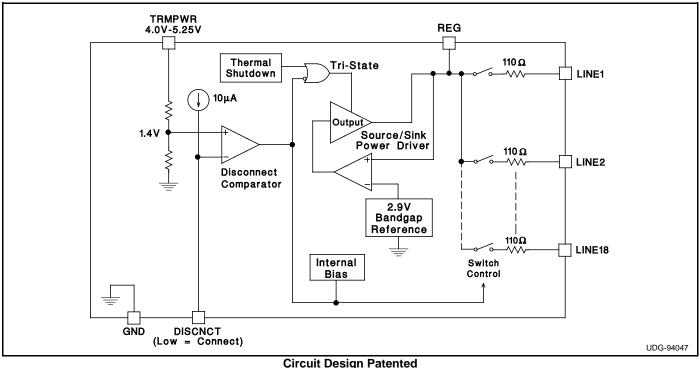
The UC5608 is pin-for-pin compatible with its predecessors, the UC5601 and UC5602 - 18 Line Active Terminator. Parametrically the UC5608 has a 5% tolerance on impedance and current compared to a 3% tolerance on the UC5601 and the sink current is increased from 20 to 200mA. The low side clamps have been removed. Custom power packages are utilized to allow normal operation at full power conditions (2 Watts).

When in disconnect mode the terminator will disconnect all terminating resistors and disable the regulator, greatly reducing standby power. The output channels remain high impedance even without Termpwr applied.

Internal circuit trimming is utilized to trim the impedance to a 5% tolerance and, most importantly, to trim the output current to a 5% tolerance, as close to the max SCSI spec as possible, which maximizes noise margin in fast SCSI operation.

Other features include 4.0 to 5.25V Termpwr, thermal shutdown and current limit.

This device is offered in low thermal resistance versions of the industry standard 28 pin wide body SOIC, 28 pin wide body TSSOP, and 28 pin PLCC, as well as 24 pin DIP.



BLOCK DIAGRAM

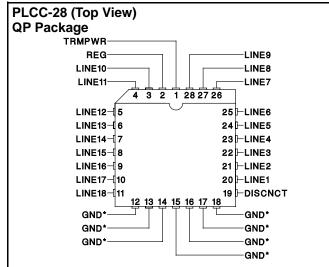
ABSOLUTE MAXIMUM RATINGS

Termpwr Voltage
Signal Line Voltage 0V to +7V
Regulator Output Current
Storage Temperature
Operating Temperature
Lead Temperature (Soldering, 10 Sec.)
Unless otherwise specified all voltages are with respect to Ground. Currents are posi-
tive into, negative out of the specified terminal.
Consult Packaging Section of Unitrode Integrated Circuits databook for thermal limita-
tions and considerations of packages.

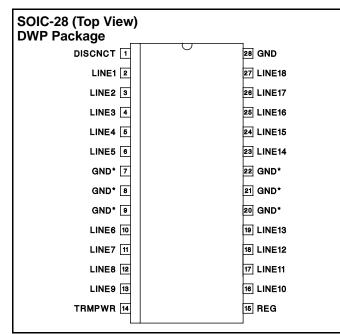
RECOMMENDED OPERATING CONDITIONS

Termpwr Voltage	3V to 5.25V
Signal Line Voltage	0V to +5V
Disconnect Input Voltage 0V t	to Termpwr

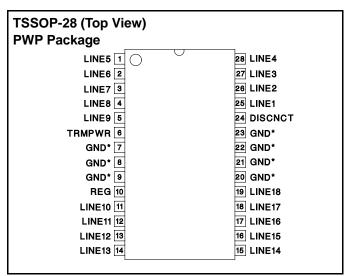
CONNECTION DIAGRAMS

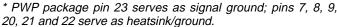


* QP package pins 12 - 18 serve as both heatsink and signal ground.



* DWP package pin 28 serves as signal ground; pins 7, 8, 9, 20, 21, 22 serve as heatsink/ground.





DIL-24 (Top View N or J Package)	
	\bigcirc	
DISCNCT 1		24 GND
LINE1 2		23 LINE18
LINE2 3		22 LINE17
N/C 4		21 N/C
LINE3 5		20 LINE16
LINE4 6		19 LINE15
LINE5 7		18 LINE14
LINE6 8		17 LINE13
LINE7 9		16 LINE12
LINE8 10		15 LINE11
LINE9 11		14 LINE10
TRMPWR 12		13 REG

Note: Drawings are not to scale.

ELECTRICAL CHARACTERISTICS Unless otherwise stated, these specifications apply for TA = 0°C to 70°C. TRMPWR =
4.75V, DISCNCT = Ground. $TA = TJ$.

PARAMETER		MIN	TYP	MAX	UNITS		
Supply Current Section							
Termpwr Supply Current	All termination li	nes = Open			17	25	mA
	All termination lines = 0.5V				400	430	mA
Power Down Mode	DISCNCT = Ope			100	150	μA	
Output Section (Terminator Lines	s)						
Terminator Impedance	Δ ILINE = -5mA to	104.5	110	115.5	Ohms		
Output High Voltage	VTRMPWR = 4V (Note 1)		2.65	2.9	3.0	V
Max Output Current	VLINE = $0.5V$ TJ = $25^{\circ}C$			-20.3	-21.5	-22.4	mA
			$0^{\circ}C < T_J < 70^{\circ}C$	-19.8	-21.5	-22.4	mA
Max Output Current	VLINE = $0.5V$, TF	VLINE = 0.5V, TRMPWR = 4V (Note 1)		-19.5	-21.5	-22.4	mA
		0°			-21.5	-22.4	mA
	VLINE = $0.2V$, TF	RMPWR = 4V to 5.25V	$0^{\circ}C < T_J < 70^{\circ}C$	-21.6	-24.0	-25.4	mA
Output Leakage	DISCNCT = 4V	TRMPWR = 0V to 5.25V REG = 0V	VLINE = 0 to $4V$		10	400	nA
			VLINE = 5.25V			100	μA
		TRMPWR = 0V to 5.25V, REG = Open VLINE = 0V to 5.25V			10	400	nA
Output Capacitance	DISCNCT = Open (Note 2)				6	7	pF
Regulator Section	· ·						<u> </u>
Regulator Output Voltage				2.8	2.9	3	V
Regulator Output Voltage	All Termination	Lines = 4V		2.8	2.9	3	V
Line Regulation	TRMPWR = 4V to 6V				10	20	mV
Drop Out Voltage	All Termination Lines = 0.5V				1.0	1.2	V
Short Circuit Current	VREG = 0V			-450	-650	-950	mA
Sinking Current Capability	VREG = 3.5V			100	200	500	mA
Thermal Shutdown					170		°C
Thermal Shutdown Hysteresis					10		°C
Disconnect Section							
Disconnect Threshold				1.1	1.4	1.7	V

Note 1: Measuring each termination line while other 17 are low (0.5V). Note 2: Guaranteed by design. Not 100% tested in production.

APPLICATION INFORMATION

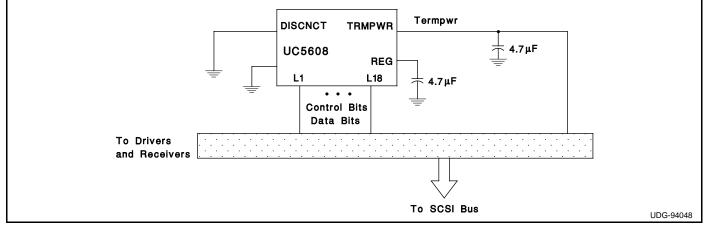


Figure 1: Typical SCSI Bus Configuration

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PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins Package Qty	Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
UC5608DWP	OBSOLETE	SOIC	DW	28	TBD	Call TI	Call TI
UC5608DWP/81168	OBSOLETE	SOIC	DW	28	TBD	Call TI	Call TI
UC5608DWPTR	OBSOLETE	SOIC	DW	28	TBD	Call TI	Call TI
UC5608DWPTR/81168	OBSOLETE	SOIC	DW	28	TBD	Call TI	Call TI
UC5608N	OBSOLETE	PDIP	Ν	24	TBD	Call TI	Call TI
UC5608QP	OBSOLETE	PLCC	FN	28	TBD	Call TI	Call TI
UC5608QPTR	OBSOLETE	PLCC	FN	28	TBD	Call TI	Call TI

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details. TBD: The Pb-Free/Green conversion plan has not been defined.

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⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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