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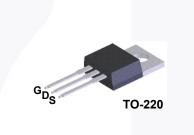
FQP30N06 N-Channel QFET[®] MOSFET 60 V, 30 A, 40 mΩ

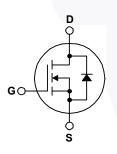
Description

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, audio amplifier, DC motor control, and variable switching power applications.

Features

- 30 A, 60 V, ${\sf R}_{{\sf DS}({\sf on})}$ = 40 m Ω (Max.) @ V_{{\sf GS}} = 10 V, ${\sf I}_{{\sf D}}$ = 15 A
- Low Gate Charge (Typ. 19 nC)
- Low Crss (Typ. 40 pF)
- 100% Avalanche Tested
- 175°C Maximum Junction Temperature Rating





Absolute Maximum Ratings T_c = 25°C unless otherwise noted.

Symbol	Parameter		FQP30N06	Unit
V _{DSS}	Drain-Source Voltage		60	V
I_D Drain Current - Continuous ($T_C = 25^{\circ}C$)			30	A
	- Continuous (T _C = 100°C)		21.3	A
I _{DM}	Drain Current - Pulsed	(Note 1)	120	A
V _{GSS}	Gate-Source Voltage		± 25	V
E _{AS}	Single Pulsed Avalanche Energy	(Note 2)	280	mJ
I _{AR}	Avalanche Current	(Note 1)	30	A
E _{AR}	Repetitive Avalanche Energy	(Note 1)	7.9	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	7.0	V/ns
P _D	Power Dissipation (T _C = 25°C)		79	W
- Derate above 25°C			0.53	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +175	°C
ΤL	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 seconds		300	°C

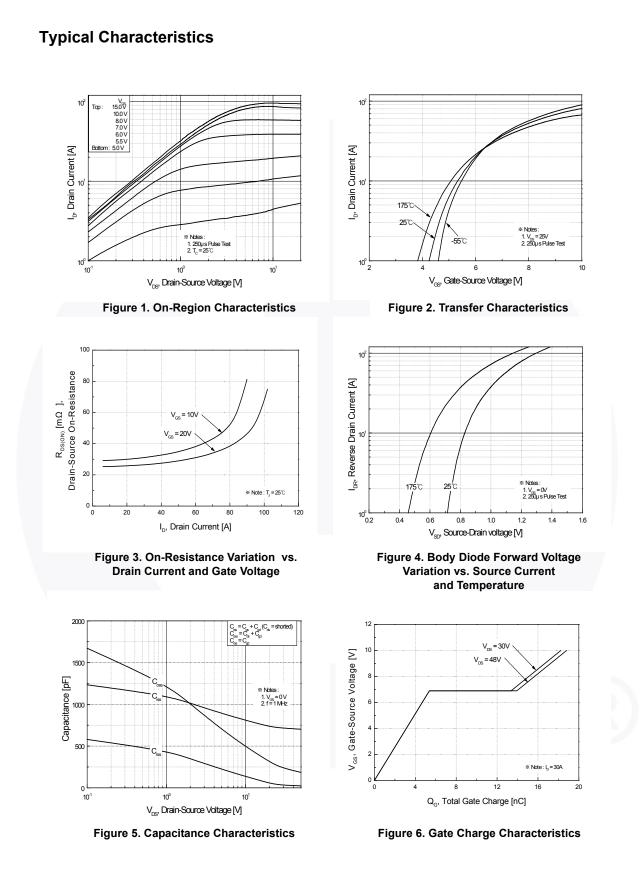
Thermal Characteristics

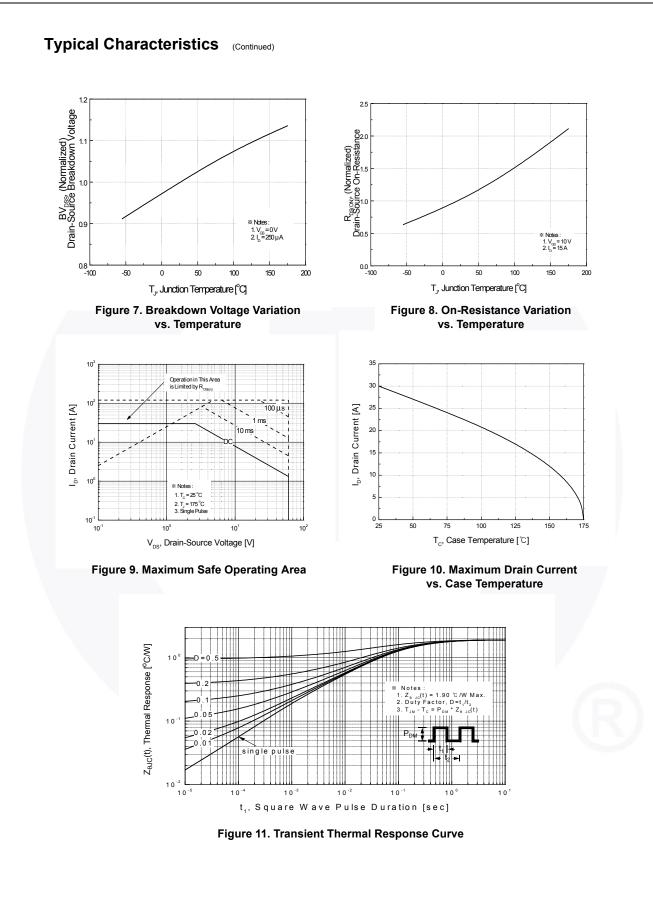
Symbol	Parameter	FQP30N06	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	1.90	°C/W
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction-to-Ambient, Max.	62.5	°C/W

November 2013

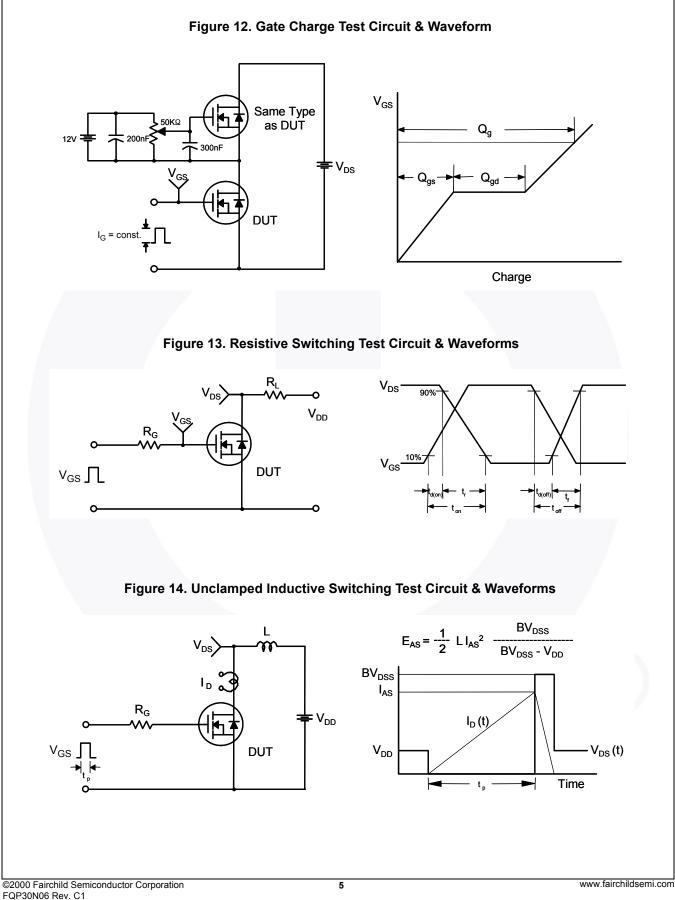
		Package	Packing Method	Reel Size	Tape Width		n Qi	Quantity 50 units	
		TO-220	Tube	N/A			5		
lectri	cal Char	acteristics T _c = 25	°C unless other	wise noted.					
Symbol		Parameter		Test Condit	ions	Min	Тур	Max	Unit
Off Cha	racteristi	cs							
BV _{DSS}		ce Breakdown Voltage		V _{GS} = 0 V, I _D = 250	μA	60			V
ΔBV_{DSS} / ΔT_J		Voltage Temperature Coe	fficient	$I_D = 250 \ \mu A$, Referen			0.06		V/°C
I _{DSS}				V _{DS} = 60 V, V _{GS} = 0	V			1	μA
	∠ero Gate '	Voltage Drain Current		$V_{\rm DS} = 48$ V, T _C = 15				10	μA
I _{GSSF}	Gate-Body	Leakage Current, Forward		$V_{GS} = 25 \text{ V}, \text{ V}_{DS} = 0$	V			100	nA
I _{GSSR}	Gate-Body	Leakage Current, Reverse	1	V_{GS} = -25 V, V_{DS} = 0	V			-100	nA
On Cha	racteristi	cs							
V _{GS(th)}	1	hold Voltage		V _{DS} = V _{GS} , I _D = 250	μA	2.0		4.0	V
R _{DS(on)}	Static Drair On-Resista			$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 15 \text{ A}$			0.031	0.04	Ω
9 _{FS}	Forward Tra	ansconductance		V _{DS} = 25 V, I _D = 15 J	٩		16		S
D									
_	ic Charac						705	0.45	
C _{iss}	Input Capa		_	$V_{DS} = 25 V, V_{GS} = 0$	V,		725	945	pF
C _{oss}	Output Cap			f = 1.0 MHz			270	350	pF
C _{rss}	Reverse Ir	ansfer Capacitance					40	52	pF
Switchi	ing Chara	cteristics							
t _{d(on)}	Turn-On De	elay Time		$V_{DD} = 30 V, I_D = 15 A,$ R _G = 25 Ω			10	30	ns
t _r	Turn-On Ri	se Time					85	180	ns
t _{d(off)}	Turn-Off De	elay Time					35	80	ns
t _f	Turn-Off Fa	all Time		-	(Note 4)		40	90	ns
Qg	Total Gate	Charge		V _{DS} = 48 V, I _D = 30	Δ		19	25	nC
Q _{gs}	Gate-Sourc	e Charge		$V_{GS} = 10 V$			5.4		nC
Q _{gd}	Gate-Drain	Charge			(Note 4)		8.5		nC
		de Oberneterieties		Detinus		I.		1	
Drain-S		ode Characteristics a		•				30	A
	Maximum Continuous Drain-Source Diode Forward Current Maximum Pulsed Drain-Source Diode Forward Current				120	A			
I _{SM} V _{SD}		ce Diode Forward Voltage		$V_{GS} = 0 V, I_S = 30 A$				1.5	V
vsD t _{rr}		ecovery Time		$V_{GS} = 0 V, I_S = 30 A$ $V_{GS} = 0 V, I_S = 30 A,$			45		
Q _{rr}		ecovery Charge		dl _F / dt = 100 A/μs	,		45 65		ns nC
∽ m	INCVEISE RE	covery charge					05	-	no

FQP30N06 — N-Channel QFET[®] MOSFET





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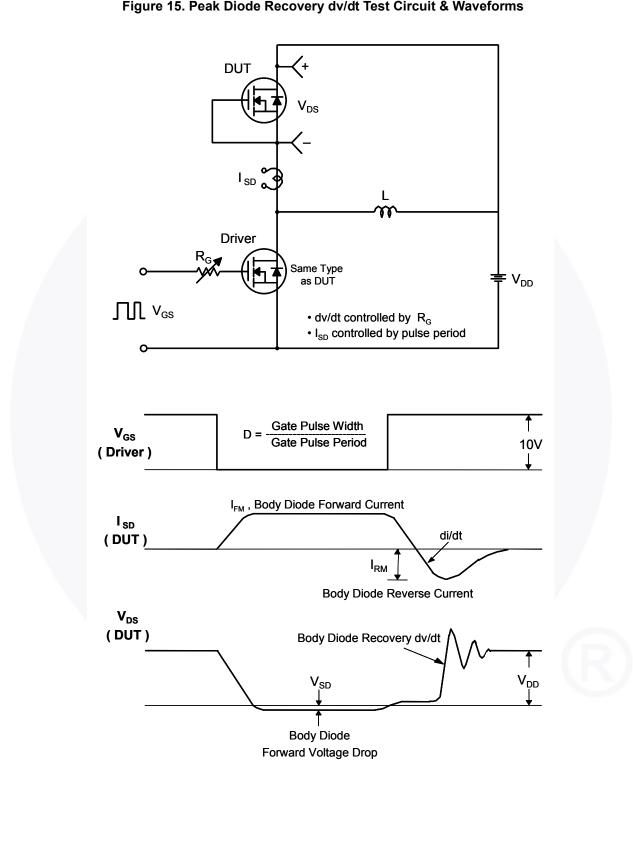
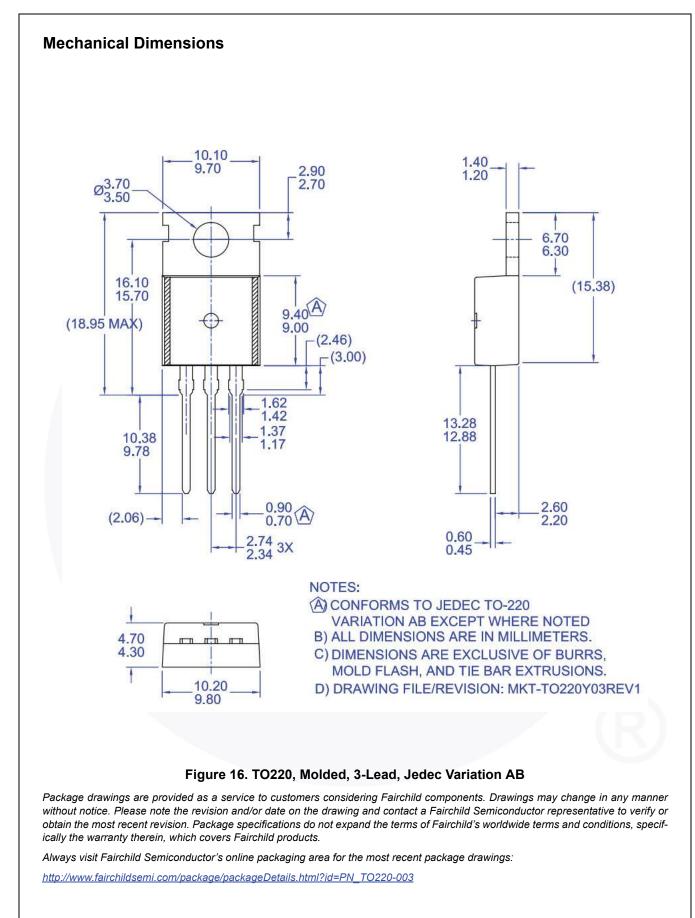


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms





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