



P-Channel 1.8-V (G-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)		
	0.045 at $V_{GS} = -4.5 \text{ V}$	- 3.5		
- 8	0.072 at V _{GS} = - 2.5 V	- 2.8		
	0.120 at V _{GS} = - 1.8 V	- 2.0		

FEATURES

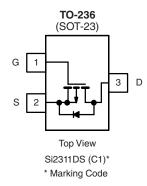
- Halogen-free Option Available
- TrenchFET® Power MOSFET



ROHS

APPLICATIONS

· Load Switch



Ordering Information: Si2311DS-T1-E3 (Lead (Pb)-free)

Si2311DS-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS TA	= 25 °C, unle	ss otherwise r	noted		
Parameter		Symbol	5 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	- 8		٧
Gate-Source Voltage		V_{GS}	± 8		
Continuous Drain Current (T, = 150 °C) ^{a, b}	T _A = 25 °C	- I _D	- 3.5	- 3.0	
Continuous Drain Current (1) = 150 °C)	T _A = 70 °C		- 2.8	- 2.4	•
Pulsed Drain Current		I _{DM}	- 10		А
Continuous Source Current (Diode Conduction) ^{a, b}		I _S	- 0.8	- 0.6	
Mariana Barra Biratania h	T _A = 25 °C	P _D	0.96	0.71	W
Maximum Power Dissipation ^{a, b}	T _A = 70 °C		0.62	0.46	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Manipular landing to Austriant	t ≤ 5 s	- R _{thJA}	100	130	°C/W
Maximum Junction-to-Ambient ^a	Steady State		140	175	
Maximum Junction-to-Foot (Drain)	Steady State		60	75	

Notes

- a. Surface Mounted on FR4 board.
- b. Pulse width limited by maximum junction temperature.

Si2311DS

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			Limits				
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = -10 \mu\text{A}$	- 8			V	
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -250 \mu A$	- 0.45		- 0.8	v	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA	
Zone Onto Vellana Busin Oursel		V _{DS} = - 6.4 V, V _{GS} = 0 V	- 1		- 1		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -6.4 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			- 10	μΑ	
On-State Drain Current ^a		$V_{DS} \le$ - 5 V, V_{GS} = - 4.5 V	- 6			A	
	I _{D(on)}	$V_{DS} \le -5 \text{ V}, V_{GS} = -2.5 \text{ V}$	- 3				
Drain-Source On-Resistance ^a		V _{GS} = - 4.5 V, I _D = - 3.5 A		0.036	0.045		
	R _{DS(on)}	$V_{GS} = -2.5 \text{ V}, I_D = -3 \text{ A}$		0.058	0.072	Ω	
		V _{GS} = - 1.8 V, I _D = - 0.7 A		0.096	0.120		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 5 V, I _D = - 3.5 A		9.0		S	
Diode Forward Voltage	V _{SD}	I _S = - 0.8 A, V _{GS} = 0 V			- 1.2	V	
Dynamic ^b							
Total Gate Charge	Qg	V 4VV 4EV		8.5	12		
Gate-Source Charge	Q _{gs}	$V_{DS} = -4 \text{ V}, V_{GS} = -4.5 \text{ V}$ $I_{D} \cong -3.5 \text{ A}$		1.5		nC	
Gate-Drain Charge	Q _{gd}	1 _D = 0.0 /1		2.1			
Input Capacitance	C _{iss}			970			
Output Capacitance	C _{oss}	$V_{DS} = -4 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		485		pF	
Reverse Transfer Capacitance	C _{rss}			160			
Switching ^b							
Turn-On Time	t _{d(on)}	V 4V.D 40		18	25		
	t _r	$V_{DD} = -4 \text{ V}, R_L = 4 \Omega$ $I_D \cong -1.0 \text{ A}, V_{GEN} = -4.5 \text{ V}$		45	65		
Turn-Off Time	t _{d(off)}			40	60	ns	
ium-on time	t _f	$R_G = 6 \Omega$		45	65		

- a. For DESIGN AID ONLY, not subject to production testing.
- b. Pulse test: PW \leq 300 $\mu s,$ duty cycle \leq 2 %.
- c. Switching time is essentially independent of operating temperature.

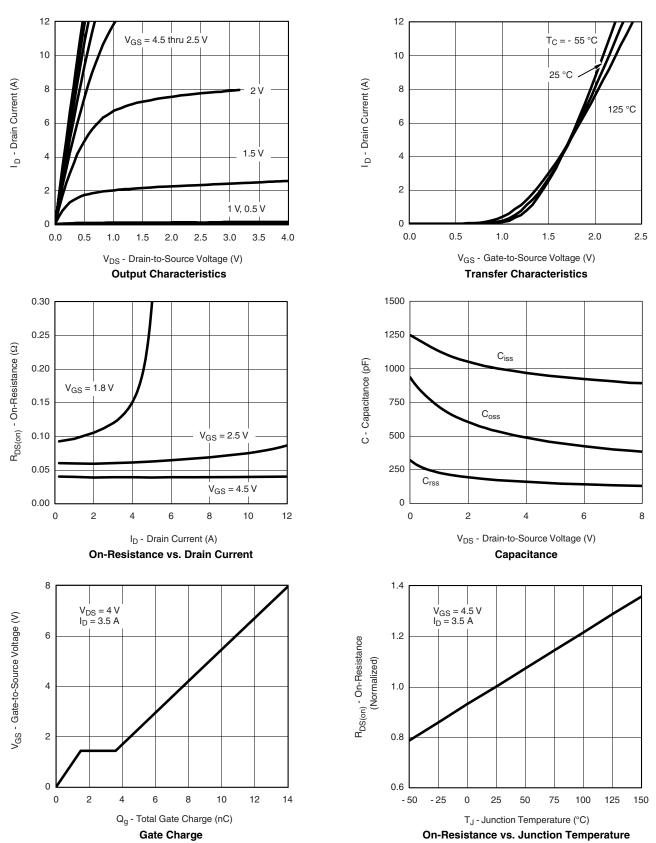
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.







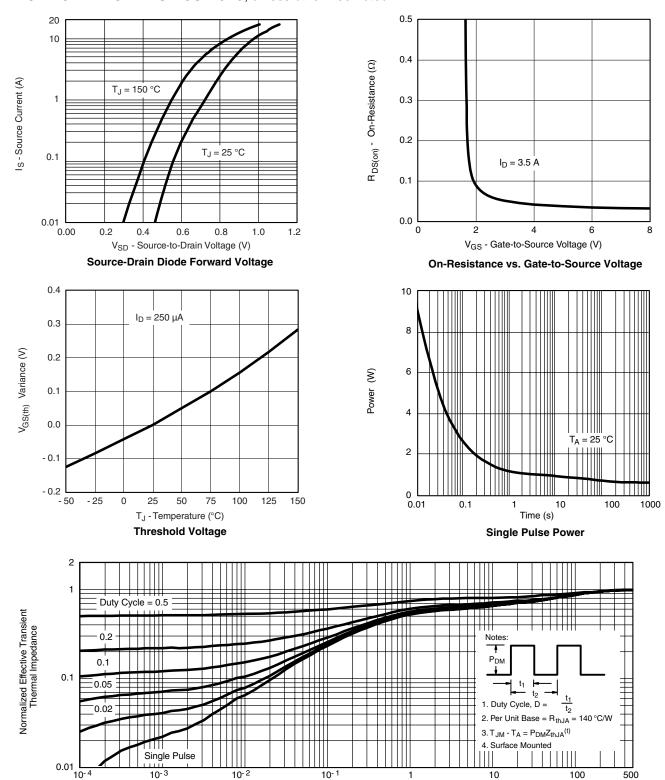
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient

Square Wave Pulse Duration (s)

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