

20 V, single P-channel Trench MOSFET Rev. 1 — 8 May 2012

Product data sheet

Product profile 1.

1.1 General description

P-channel enhancement mode Field-Effect Transistor (FET) in a small SOT457 (SC-74) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

1.2 Features and benefits

- R_{DSon} specified at 1.8 V operation
- Trench MOSFET technology

1.3 Applications

- Relay driver
- High-speed line driver

Fast switching

- High-side loadswitch
- Switching circuits

1.4 Quick reference data

Table 1.	Quick reference data						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{DS}	drain-source voltage	T _{amb} = 25 °C		-	-	-20	V
V_{GS}	gate-source voltage			-12	-	12	V
I _D	drain current	$V_{GS} = -4.5 \text{ V}; \text{ T}_{amb} = 25 \text{ °C}; \text{ t} \le 5 \text{ s}$	[1]	-	-	-3.2	А
Static cha	aracteristics						
R_{DSon}	drain-source on-state resistance	V_{GS} = -4.5 V; I _D = -2.5 A; T _j = 25 °C		-	80	102	mΩ

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².

Pinning information 2.

Table 2.	Pinning	information				
Pin	Symbol	Description	Simplified outline	Graphic symbol		
1	D	drain		2		
2	D	drain		D		
3	G	gate	0			
4	S	source				
5	D	drain	SOT457 (TSOP6)	S 017aaa257		
6	D	drain		0dd_0.		



3. Ordering information

Table 3. Orderi	ng information		
Type number	Package		
	Name	Description	Version
PMN80XP	TSOP6	plastic surface-mounted package (TSOP6); 6 leads	SOT457

4. Marking

Table 4.Marking codes

Type number	Marking code
PMN80XP	WA

5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

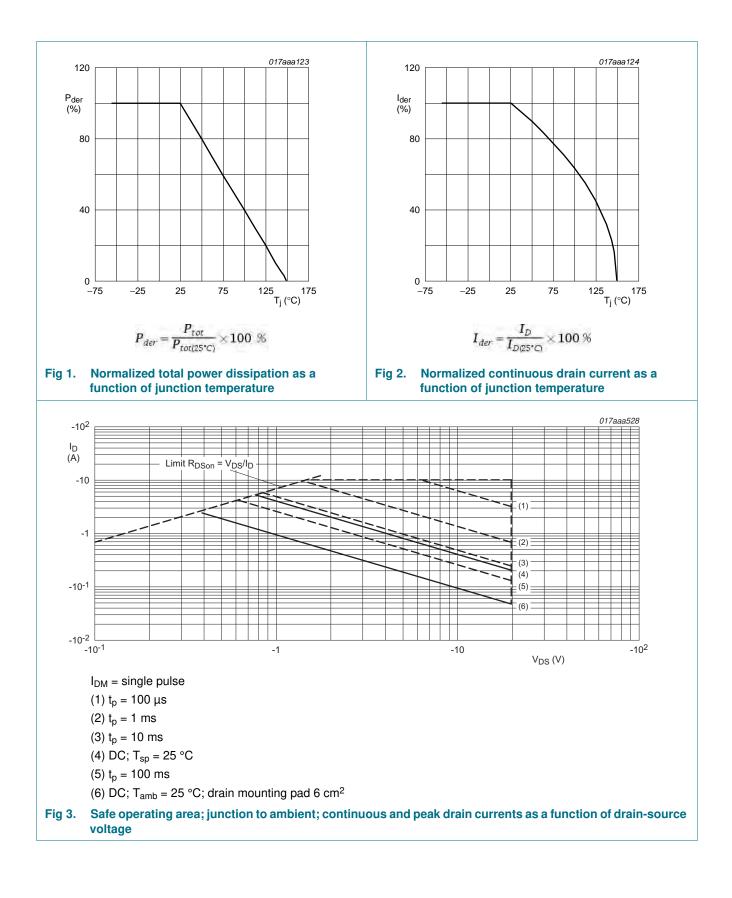
Symbol	Parameter	Conditions		Min	Max	Unit
V _{DS}	drain-source voltage	$T_{amb} = 25 \ ^{\circ}C$		-	-20	V
V _{GS}	gate-source voltage			-12	12	٧
I _D	drain current	$V_{GS} = -4.5 \text{ V}; \text{ T}_{amb} = 25 \text{ °C}; \text{ t} \le 5 \text{ s}$	<u>[1]</u>	-	-3.2	А
		V_{GS} = -4.5 V; T_{amb} = 25 °C	[1]	-	-2.5	А
		V_{GS} = -4.5 V; T_{amb} = 100 °C	<u>[1]</u>	-	-1.6	А
I _{DM}	peak drain current	$T_{amb} = 25 \text{ °C}$; single pulse; $t_p \le 10 \mu\text{s}$		-	-10	А
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	385	mW
			[1]	-	925	mW
		T _{sp} = 25 °C		-	4000	mW
Tj	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
Source-drai	in diode					
I _S	source current	T _{amb} = 25 °C	[1]	-	-1	А

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

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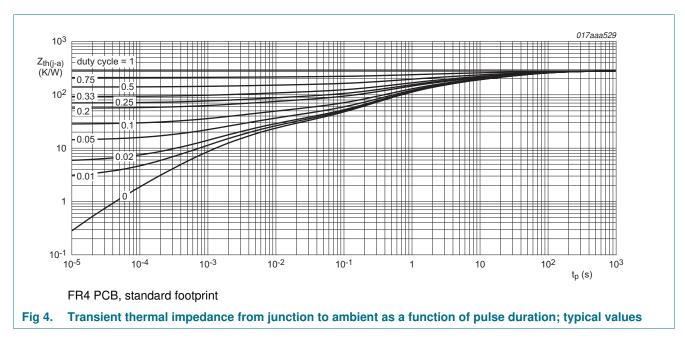
6. Thermal characteristics

Table 6.	Thermal characteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air [1]	<u>[1]</u>	-	281	325	K/W
			-	116	135	K/W	
			<u>[3]</u>	-	73	85	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	27	31	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint

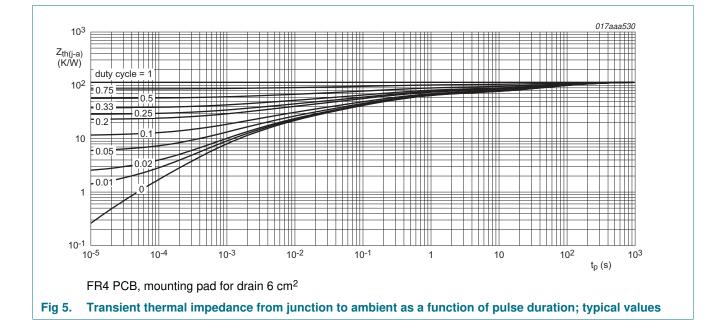
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm²

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm², t ≤ 5 s



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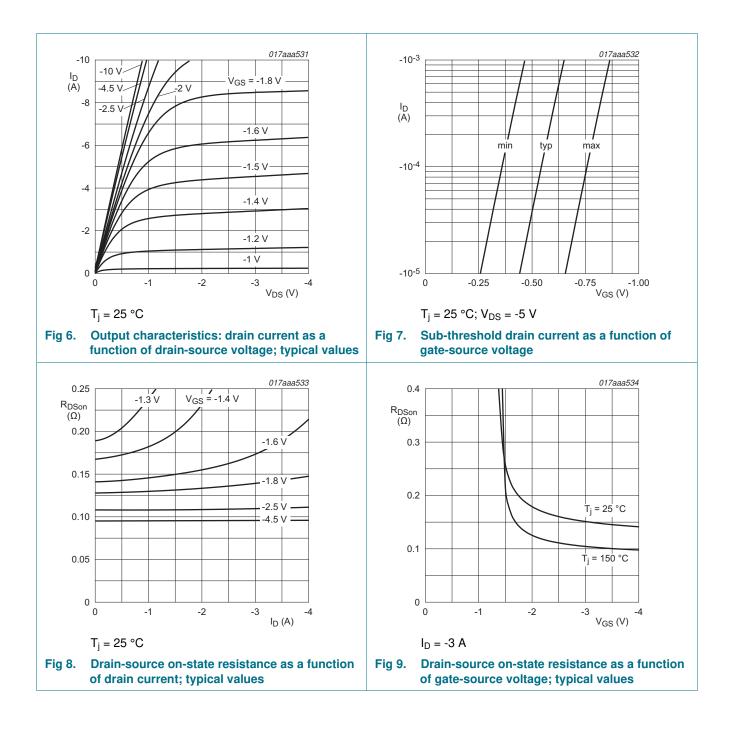
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7. Characteristics

Table 7.	Characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static cha	aracteristics					
V _{(BR)DSS}	drain-source breakdown voltage	$I_D = -250 \ \mu\text{A}; \ V_{GS} = 0 \ V; \ T_j = 25 \ ^\circ\text{C}$	-20	-	-	V
V _{GSth}	gate-source threshold voltage	$I_D = -250 \ \mu\text{A}; \ V_{DS} = V_{GS}; \ T_j = 25 \ ^\circ\text{C}$	-0.45	-0.75	-1	V
I _{DSS}	drain leakage current	$V_{DS} = -20 \text{ V}; \text{ V}_{GS} = 0 \text{ V}; \text{ T}_{j} = 25 \text{ °C}$	-	-	-1	μA
		$V_{DS} = -20 \text{ V}; \text{ V}_{GS} = 0 \text{ V}; \text{ T}_{amb} = 150 \text{ °C}$	-	-	-10	μA
I _{GSS}	gate leakage current	V_{GS} = 12 V; V_{DS} = 0 V; T_j = 25 °C	-	-	-100	nA
		V_{GS} = -12 V; V_{DS} = 0 V; T_j = 25 °C	-	-	-100	nA
R_{DSon}	drain-source on-state	V_{GS} = -4.5 V; I _D = -2.5 A; T _j = 25 °C	-	80	102	mΩ
	resistance	V_{GS} = -4.5 V; I _D = -2.5 A; T _j = 150 °C	-	116	148	mΩ
		V_{GS} = -2.5 V; I_{D} = -2.3 A; T_{j} = 25 °C	-	95	125	mΩ
		V_{GS} = -1.8 V; I _D = -1.1 A; T _j = 25 °C	-	120	156	mΩ
g _{fs}	forward transconductance	V_{DS} = -10 V; I_{D} = -2.5 A; T_{j} = 25 °C	-	15	-	S
Dynamic	characteristics					
Q _{G(tot)}	total gate charge	V_{DS} = -10 V; I_{D} = -2.5 A; V_{GS} = -4.5 V;	-	5	7.5	nC
Q _{GS}	gate-source charge	T _j = 25 °C	-	0.7	-	nC
Q _{GD}	gate-drain charge		-	0.9	-	nC
C _{iss}	input capacitance	$V_{DS} = -10 \text{ V}; \text{ f} = 1 \text{ MHz}; \text{ V}_{GS} = 0 \text{ V};$	-	550	-	pF
C _{oss}	output capacitance	$T_j = 25 \text{ °C}$	-	63	-	pF
C _{rss}	reverse transfer capacitance		-	53	-	pF
t _{d(on)}	turn-on delay time	$V_{DS} = -10 \text{ V}; \text{ I}_{D} = -2.5 \text{ A}; \text{ V}_{GS} = -4.5 \text{ V};$	-	6	-	ns
t _r	rise time	$R_{G(ext)} = 6 \ \Omega; \ T_j = 25 \ ^{\circ}C$	-	14	-	ns
t _{d(off)}	turn-off delay time		-	120	-	ns
t _f	fall time		-	50	-	ns
Source-d	rain diode					
V _{SD}	source-drain voltage	I _S = -1.0 A; V _{GS} = 0 V; T _j = 25 °C	-	-0.8	-1.2	V

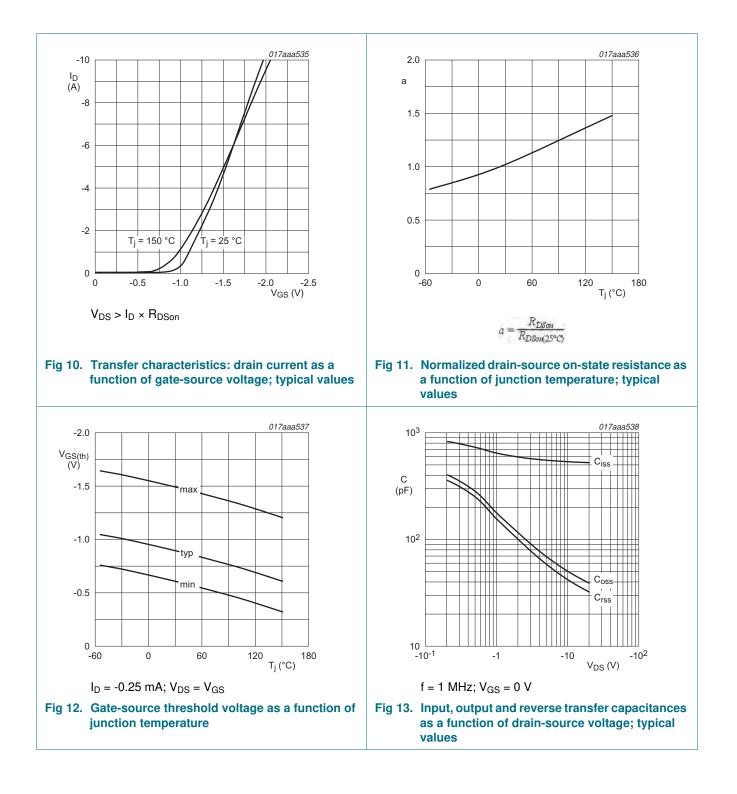
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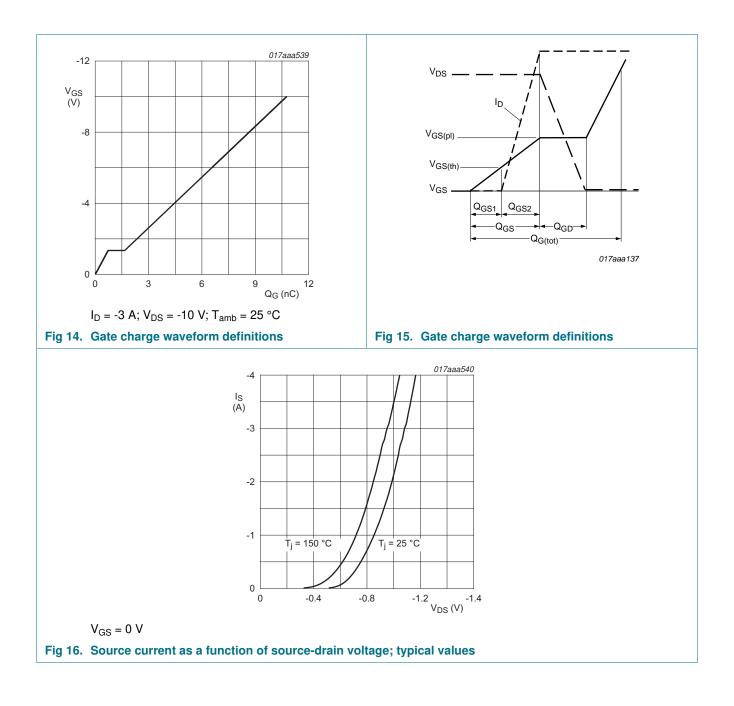
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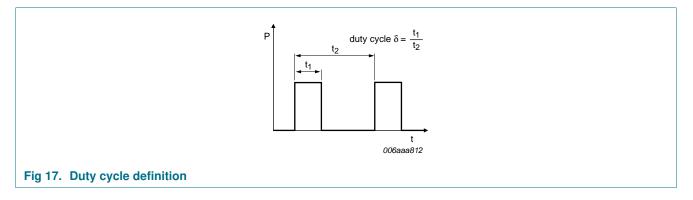
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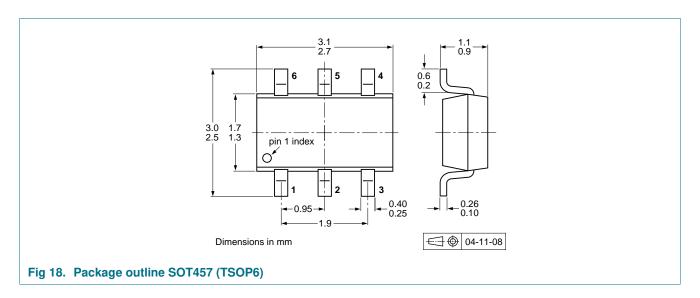


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8. Test information

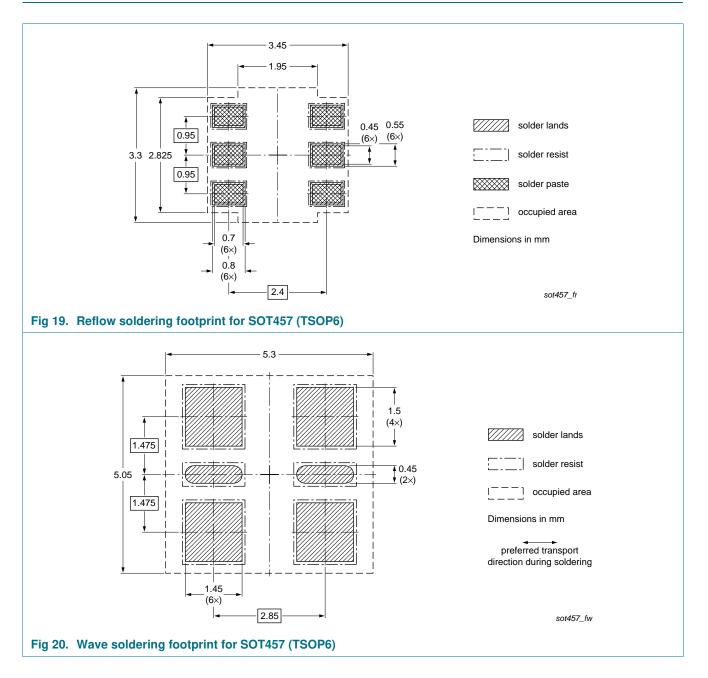


9. Package outline



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10. Soldering



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11. Revision history

Table 8. Revisi	Revision history						
Document ID	Release date	Data sheet status	Change notice	Supersedes			
PMN80XP v.1	20120508	Product data sheet	-	-			

12. Legal information

12.1 Data sheet status

Document status[1] [2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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