

# B70SR12424A/B/C/D

12V Output DC/DC Converter, Box Type Package



#### **FEATURES**

- Wide input voltage range, 36~106V
- 300W Output
- Full Load Efficiency up to 89.5% @48Vin; 90%@72Vin
- Intergrated fuse holder (option)
- Parallel Connection of multiple units
- Box type package with metal base plate
- Package Dimension:
   190.0x76.0x44.0 mm (7.48"x2.99"x1.73")
- Operating Temperature Range 40°C to +75°C
- Input Reverse Polarity Protection
- Minimized Inrush current
- Input UVLO, Output OCL, Short circuit protection, OVP, OTP
- Enable on/off (option)
- 2250VDC Isolation
- IP67 protection for selective model
- RoHs Compliant
- ISO 9001, ISO 14001 certified manufacturing facility
- UL60950
- CE Mark
- EMC compatible: EN12895, CISPR11 ClassA
- Electrical transient conduction: ISO7637-2

The B70SR12424, a wide input voltage range of 36~106V, and single isolated output converter, is the latest product offering from a world leader in power systems technology and manufacturing — Delta Electronics, Inc. Such box type DCDC converter can provide 300W, 12.4V regulated DC output voltage with full load efficiency up to 90% @72Vin; The B70SR12424 offers input UVLO, output over current limit, short circuit, output over voltage, over temperature, and input reverse polarity protections, It has an option for intergrated fuse holder and enable on/off function. It also has parallel function; and allows a wide operating temperature range of -40°C to +75°C. With creative design technology and optimization of component placement, this converter possess outstanding electrical and thermal performance, as well as high reliability under extrmely harsh operating conditions. The B70SR12424 meets IP67 protection (refer to "water protection level" specification).

Input Characteristics					
Item	Condition	Min.	Тур.	Max.	Unit
Continuous Input Voltage		36	72	106	VDC
Max Input voltage	10 minutes, normal operating			126	VDC
Input Under-Voltage Lockout, Turn-On Voltage Threshold		33	34	35	VDC
Input Under-Voltage Lockout, Turn-Off Voltage Threshold		31	32	33	VDC
Lockout Hysteresis Voltage		1	2	3	VDC
Maximum Input Current	Vin=36V, 100% Load		9.2	10	Α
No Lood Input Current	Vin=48V		35	60	mA
No-Load Input Current	Vin=72V, 80V		25	50	mA
Off converter input current	Vin=72V, enable off (C version)		7	12	mA
Reflected input ripple current	Vin=72V, Vpp			0.2	Α
Max Reverse Polarity Input Voltage				106	VDC
Max Inrush current				10	Α
Internal Input Fuse Ø6.35mm*31.75mm 250V/25A Fast-acting fuse					



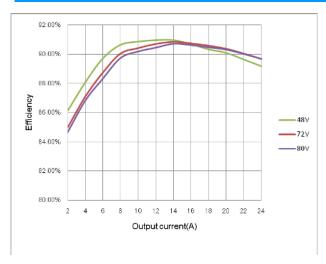
Output Voltage Ripple and Noise,  Vin=  Output Current Limit Current share accuracy Start-up time(start _up time by Vin) Start-up time(start _up time by Enable) Rise time Output Voltage Protection  Output Voltage Current Transient  Nagel  Maximum Output Capacitance Output overshoot Efficiency @ 100% Load Efficiency @ 100% Load Efficiency @ 100% Load Efficiency @ 60% Load	Io=0 Io=24A In=48V, Io=24A, peak to peak, 20MHz bandwidth RMS IVEX. ROW, Io=24A, peak to peak, 20MHz bandwidth RMS IVEX. ROW, Io=24A, peak to peak, 20MHz bandwidth RMS IVEX. ROW, Io=24A, peak to peak, 20MHz bandwidth RMS IVEX. ROW, Io=24A, peak to peak, 20MHz bandwidth RMS IVEX. ROW, Io=24A, peak to peak, 20MHz bandwidth RMS IVEX. ROW, Io=48V, Io=48V, Io=48V, Io=48V IVEX. ROW, IO=48V IVE	Min. 0 12.4 12.0 25	Typ.  12.6  12.2  100  30  150  50  28  6  500  250  30  15  150  150	Max. 24 12.8 12.4 150 50 200 80 31 10 600 350 50 17 250	Unit A V V  MV  MV  MV  MV  MV  MS  MS  MS  MS	
Output Voltage Set Point  Output Voltage Ripple and Noise,  Vin=  Output Current Limit  Current share accuracy Start-up time(start _up time by Vin) Start-up time(start _up time by Enable) Rise time Output Voltage Protection  Output Voltage Current Transient  Naget  Maximum Output Capacitance Output overshoot Efficiency @ 100% Load Efficiency @ 100% Load Efficiency @ 60% Load  Efficiency @ 60% Load  Efficiency @ 60% Load  Item  Isolation Voltage,	Io=24A n=48V, Io=24A, peak to peak, 20MHz bandwidth RMS 72V, 80V, Io=24A, peak to peak, 20MHz bandwidth RMS  24A for each module n=48V,full load (for A/B/C/D) Vin=48V,full load (for C)  ve voltage step, 18A to 12A load dynamic, 0.1A/us slew rate ive voltage step, 12A to 18A load dynamic, 0.1A/us slew rate ESR>10mohm  Vin=48V	12.4 12.0	12.6 12.2 100 30 150 50 28 6 500 250 30 15	12.8 12.4 150 50 200 80 31 10 600 350 50 17	V   V   mV   mV   mV   A   %   mS   mS   V	
Output Voltage Set Point  Output Voltage Ripple and Noise,  Vin=  Output Current Limit  Current share accuracy Start-up time(start _up time by Vin) Start-up time(start _up time by Enable) Rise time Output Voltage Protection  Output Voltage Current Transient  Naget  Maximum Output Capacitance Output overshoot Efficiency @ 100% Load Efficiency @ 100% Load Efficiency @ 60% Load  Efficiency @ 60% Load  Efficiency @ 60% Load  Efficiency @ 60% Load  Efficiency @ 60% Load  Efficiency @ 60% Load  Item  Isolation Voltage,	Io=24A n=48V, Io=24A, peak to peak, 20MHz bandwidth RMS 72V, 80V, Io=24A, peak to peak, 20MHz bandwidth RMS  24A for each module n=48V,full load (for A/B/C/D) Vin=48V,full load (for C)  ve voltage step, 18A to 12A load dynamic, 0.1A/us slew rate ive voltage step, 12A to 18A load dynamic, 0.1A/us slew rate ESR>10mohm  Vin=48V	25	12.2 100 30 150 50 28 6 500 250 30 15 150	12.4 150 50 200 80 31 10 600 350 50 17 250	W mV mV mV A % mS mS wS V	
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Output Voltage Ripple and Noise,  Vin=  Output Current Limit Current share accuracy Start-up time(start _up time by Vin) Start-up time(start _up time by Enable) Rise time Output Voltage Protection  Posit Output Voltage Current Transient  Maximum Output Capacitance Output overshoot Efficiency @ 100% Load Efficiency @ 100% Load Efficiency @ 100% Load Efficiency @ 60% Load Efficiency @ 60% Load Efficiency @ 60% Load  Efficiency @ 60% Load  Efficiency @ 60% Load  Item  Isolation Voltage,	20MHz bandwidth RMS 72V, 80V, Io=24A, peak to peak, 20MHz bandwidth RMS  24A for each module n=48V,full load (for A/B/C/D) Vin=48V,full load (for C)  ve voltage step, 18A to 12A load dynamic, 0.1A/us slew rate ive voltage step, 12A to 18A load dynamic, 0.1A/us slew rate ESR>10mohm  Vin=48V		30 150 50 28 6 500 250 30 15	50 200 80 31 10 600 350 50 17 250	mV mV A % mS mS V	
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Output Current Limit Current share accuracy Start-up time(start _up time by Vin) Start-up time(start _up time by Enable) Rise time Output Voltage Protection  Output Voltage Current Transient  Maximum Output Capacitance Output overshoot Efficiency @ 100% Load Efficiency @ 100% Load Efficiency @ 100% Load Efficiency @ 60% Load Efficiency @ 60% Load Efficiency @ 60% Load  Efficiency @ 60% Load  Efficiency @ 60% Load  Efficiency @ 60% Load  Item  Isolation Voltage,	20MHz bandwidth RMS  24A for each module n=48V,full load (for A/B/C/D) Vin=48V,full load (for C)  ve voltage step, 18A to 12A load dynamic, 0.1A/us slew rate ive voltage step, 12A to 18A load dynamic, 0.1A/us slew rate ESR>10mohm  Vin=48V		50 28 6 500 250 30 15	80 31 10 600 350 50 17 250	mV A % mS mS V	
Current share accuracy  Start-up time(start _up time by Vin)  Start-up time(start _up time by Enable)  Rise time  Output Voltage Protection  Posit  Output Voltage Current Transient  Maximum Output Capacitance  Output overshoot  Efficiency @ 100% Load  Efficiency @ 100% Load  Efficiency @ 100% Load  Efficiency @ 60% Load	RMS  24A for each module n=48V,full load (for A/B/C/D) Vin=48V,full load (for C)  ve voltage step, 18A to 12A load dynamic, 0.1A/us slew rate ive voltage step, 12A to 18A load dynamic, 0.1A/us slew rate ESR>10mohm  Vin=48V		28 6 500 250 30 15	31 10 600 350 50 17 250	A % mS mS v	
Current share accuracy  Start-up time(start _up time by Vin)  Start-up time(start _up time by Enable)  Rise time  Output Voltage Protection  Output Voltage Current Transient  Maximum Output Capacitance  Output overshoot  Efficiency @ 100% Load  Efficiency @ 100% Load  Efficiency @ 100% Load  Efficiency @ 60% Load  Item  Isolation Voltage,	n=48V,full load (for A/B/C/D) Vin=48V,full load (for C)  ve voltage step, 18A to 12A load dynamic, 0.1A/us slew rate live voltage step, 12A to 18A load dynamic, 0.1A/us slew rate ESR>10mohm  Vin=48V		6 500 250 30 15	10 600 350 50 17 250	% mS mS mS	
Current share accuracy  Start-up time(start _up time by Vin)  Start-up time(start _up time by Enable)  Rise time  Output Voltage Protection  Posit  Output Voltage Current Transient  Maximum Output Capacitance  Output overshoot  Efficiency @ 100% Load  Efficiency @ 100% Load  Efficiency @ 100% Load  Efficiency @ 60% Load  Solation Voltage,	n=48V,full load (for A/B/C/D) Vin=48V,full load (for C)  ve voltage step, 18A to 12A load dynamic, 0.1A/us slew rate live voltage step, 12A to 18A load dynamic, 0.1A/us slew rate ESR>10mohm  Vin=48V	13	500 250 30 15	600 350 50 17 250	mS mS mS	
Start-up time(start _up time by Vin)  Start-up time(start _up time by Enable)  Rise time  Output Voltage Protection  Posit  Output Voltage Current Transient  Maximum Output Capacitance  Output overshoot  Efficiency @ 100% Load  Efficiency @ 100% Load  Efficiency @ 100% Load  Efficiency @ 60% Load	n=48V,full load (for A/B/C/D) Vin=48V,full load (for C)  ve voltage step, 18A to 12A load dynamic, 0.1A/us slew rate live voltage step, 12A to 18A load dynamic, 0.1A/us slew rate ESR>10mohm  Vin=48V	13	250 30 15 150	350 50 17 250	mS mS V	
Start-up time(start _up time by Enable)  Rise time  Dutput Voltage Protection  Posit  Dutput Voltage Current Transient  Magel  Maximum Output Capacitance  Dutput overshoot  Efficiency @ 100% Load  Efficiency @ 100% Load  Efficiency @ 60% Load  Formula Characteristics  Item  In	Vin=48V,full load (for C)  ve voltage step, 18A to 12A load dynamic, 0.1A/us slew rate ive voltage step, 12A to 18A load dynamic, 0.1A/us slew rate  ESR>10mohm  Vin=48V	13	250 30 15 150	350 50 17 250	mS V	
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Posit  Output Voltage Current Transient  Maximum Output Capacitance  Output overshoot  Efficiency @ 100% Load  Efficiency @ 100% Load  Efficiency @ 60% Load	dynamic, 0.1A/us slew rate live voltage step, 12A to 18A load dynamic, 0.1A/us slew rate ESR>10mohm		150	250	mV	
Maximum Output Capacitance Output overshoot Efficiency @ 100% Load Efficiency @ 100% Load Efficiency @ 100% Load Efficiency @ 60% Load  Item Isolation Voltage,	dynamic, 0.1A/us slew rate ESR>10mohm  Vin=48V		150	250		
Output overshoot  Efficiency @ 100% Load  Efficiency @ 100% Load  Efficiency @ 100% Load  Efficiency @ 60% Load  Efficiency @ 60% Load  Efficiency @ 60% Load  Efficiency @ 60% Load  Item  Isolation Voltage,	Vin=48V		1	250	mV	
Efficiency @ 100% Load  Efficiency @ 100% Load  Efficiency @ 100% Load  Efficiency @ 60% Load  Efficiency @ 60% Load  Efficiency @ 60% Load  Efficiency @ 60% Load  Item  Isolation Voltage,				5000	μF	
Efficiency @ 100% Load Efficiency @ 100% Load Efficiency @ 60% Load Efficiency @ 60% Load Efficiency @ 60% Load Efficiency @ 60% Load Item  Solation Voltage,				3	%	
Efficiency @ 100% Load Efficiency @ 60% Load Efficiency @ 60% Load Efficiency @ 60% Load Efficiency @ 60% Load  Item  Solation Voltage,	Vin=72V	87.5	89.5		%	
Efficiency @ 60% Load Efficiency @ 60% Load Efficiency @ 60% Load  General Characteristics  Item  solation Voltage,		88.0	90.0		%	
Efficiency @ 60% Load Efficiency @ 60% Load  General Characteristics  Item  solation Voltage,	Vin=80V	88.0	90.0		%	
General Characteristics  Item  solation Voltage,	Vin=48V	88.0	90.0		%	
General Characteristics  Item  solation Voltage,	Vin=72V	88.3	90.3		%	
solation Voltage,	Vin=80V	88.3	90.3		%	
solation Voltage,						
solation Voltage,	Conditions	Min.	Тур.	Max.	Unit	
solation Voltage,	put to Output, Input to Case		.,,,,,,,	2250	VDC	
colotion Decistores, Input to Outset	Ouput to Case			550	VDC	
SOLAHOU RESISTANCE INDUITIO CHITOLIT	Cuput to Cusc	10		000	MΩ	
solation Capacitance, Input to Output		10	5000		pF	
Switching Frequency			175		KHz	
MTBF	Ta=25°C, 80%load		1.08		Mhours	
Weight	14 25 6, 6676644		900		g	
<b>Environmental Specifications</b>					9	
<u>.                                      </u>	Conditions	Min		Max.	Unit	
Parameter Parameter Parameter	Conditions	Min.		1		
Storage Temperature Range	Amalainet Tamanast	-40 -40		+125	℃	
Operating Temperature Range	Ambient Temperature		100	+75	°C	
Over Temperature Protection	NTC Temperature		122	05	_	
Humidity (non condensing)	For model D/N with a set D		l ID/	95	% rel. H	
	For model P/N with suffix B		IP(		`	
/ibration	IEC 60068-2-6		10G/15~200H		)	
Shock	IEC 60068-2-27	<u> </u>	50G 3 P			
Emission	EN11000E	30-1000MHz 34-45dBuV/m				
mmunity ESD	EN12895 EN12895, EN61000-4-3	10\// /07	10V/m /27-1000MHz AM; 10V/m /900MHz PM Direct: ±2KV ±4KV; Air: ±2KV ±4KV ±8KV			

## Notes

- Specifications typical at Ta=+25°C, nominal input voltage and rated full load output current unless otherwise noted.
- 2 Specifications are subject to change without notice.



## **ELECTRICAL CURVES**



40
35
30
25
27
20
15
10
0 2 4 6 8 10 12 14 16 18 20 22 24

Output current(A)

Figure 1: Efficiency vs. Output current @ Vin=48V,72V,80V

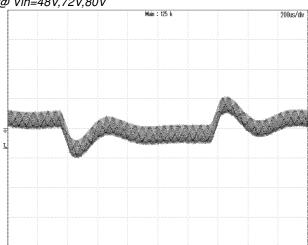


Figure 2: Loss vs. Output current @ Vin=48V,72V,80V

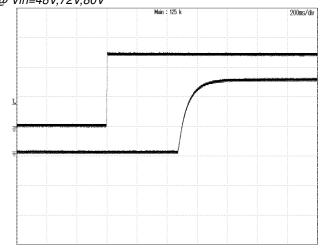


Figure 3: Dynamic response to load step 12A~18A with 0.1A/uS slew rate at 72Vin CH1:VOUT, 200mV/div, 200uS/div

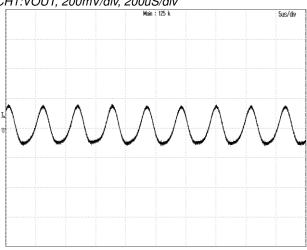
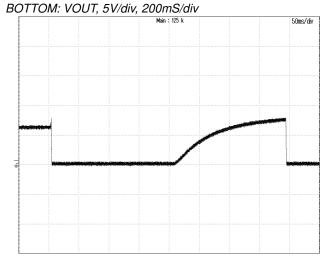


Figure 4: Vout start up with Vin on at 72Vin,24A lout, TOP:VIN, 20V/div, 200mS/div



**Figure 5:** Output ripple & noise at 72Vin, 24A lout CH1:VOUT, 100mV/div, 5uS/div

Figure 6: Output over voltage protection at 72Vin, 24A lout CH1:VOUT, 10V/div, 50mS/div



# **ELECTRICAL CURVES (continous)**

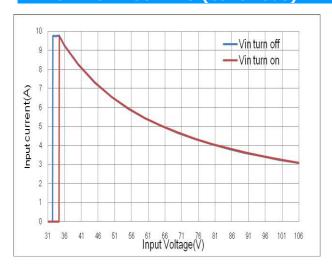
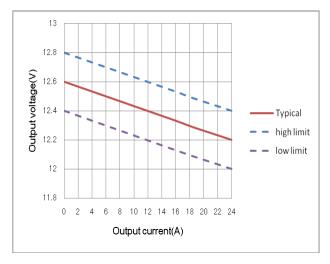


Figure 7: Input current vs. Input voltage @Full load



**Figure 9:** Output voltage vs. Output current @Vin=72V. Droop function.

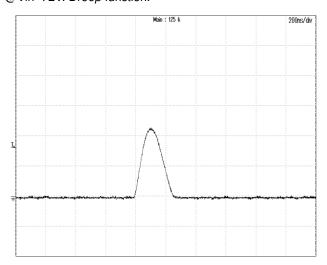


Figure 11: Inrush current @ Vin=72V CH1:Iin, 2A/div, 200nS/div; Max current 4.3A, I2t=1.5E-7 A<sup>2</sup>S

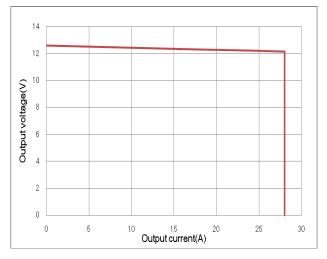
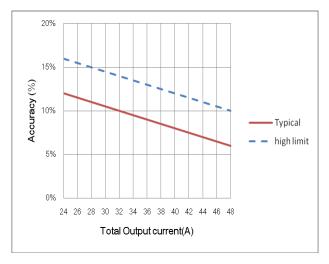


Figure 8: Output voltage vs. Output current OCL Performance



**Figure 10:** Current share accuracy vs. Total output current 2 in parallel.



#### **FEATURES DESCRIPTIONS**

## **Output Over-Current Limit and Short Protection**

The modules include internal output over-current limit (OCL) and short circuit protection (SCP) circuits, the OCL set point is lower than that of the SCP; The response of SCP circuit is much fast than that of the OCL circuit. The slowly increase of the output current will let module enter OCL protection when the current exceeds the OCL set point, while the fast increase of the output current will let module enter SCP when the current exceeds the SCP set point.

When the modules enter OCL protection, the output voltage will decrease while the output current is kept constant, the output voltage will soft start to set point when the overload condition is removed.

The module will enter hiccup mode when it triggers the SCP set point. The module will try to restart after shutdown. If the overload condition still exists, the module will shut down again. This restart trial will continue until the overload condition is removed.

## **Output Over-Voltage Protection**

The power module includes an internal output over-voltage protection(OVP) circuit, which monitors the voltage on the output terminals. If this voltage exceeds the OVP set point, the module will shut down, and then restart after a fixed delay time (hiccup mode), please refer to figure 6 for detail.

## **Over-Temperature Protection**

The over-temperature protection consists of circuitry that provides protection from thermal damage. If the temperature exceeds the preset temperature threshold the module will shut down, and all components will not exceed their absolute maximum temperature ratings. The module will restart after the temperature is within specification.

## Remote On/Off

B70SR12424C has Enable control function. This Enable PIN is designed on the primary side of converter, the converter will turn on when the Enable PIN connected to VIN+, and turn off when the Enable PIN connected to VIN- or floating.•

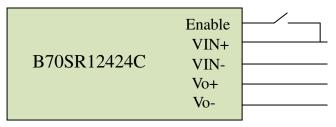


Figure 12: suggested Enable connection

## **Input Reverse Voltage Protection**

The input reverse voltage protection is provided by an diode on the input line, the standoff voltage for the reverse protection shall be no less than -106V.



#### **DESIGN CONSIDERATIONS**

## Parallel connection of multiple units

Two units parallel operation is verified, please contact Delta if more than two units need to be paralleled. While parallelling multiple units, the impedance of the cables from unit to junction point of each unit should be within ±5% of each other.

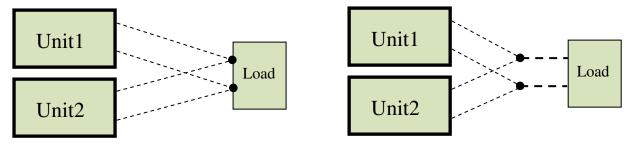


Figure 13: suggested parallel connections

#### **EMC**

The converter has the internal EMI filters and meet the EMC standards EN12895 30-1000MHz 34-45dBuV/m. The test result is showed as below **Conditions:** Vin=72V, Io=24A, 10m measure distance

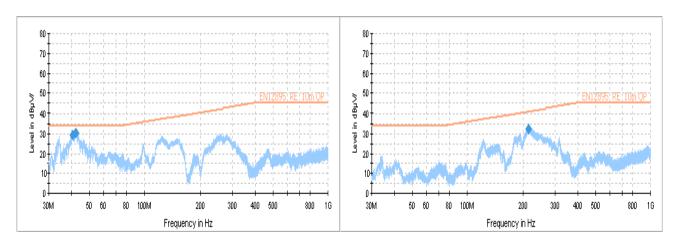


Figure 14: test result(Vertical)

Figure 15: test result(Horizontal)

## **Fuse replacement**

For the versions with the intergratted the fuse holder, when the fuse needs to be replaced, it can be taked down in an anticlockwise direction by slotted type screwdrivers . Recommended fuse replacement P/N:

Littlefuse 0314025.MXP



## THERMAL CONSIDERATION

The thermal curve (Figure 17~19) is based on a 250x300x5 AL table, shown as below figure.

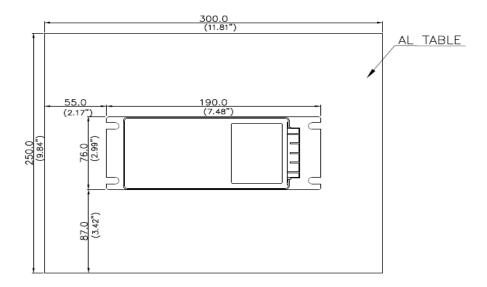


Figure 16: Thermal consideration

## **THERMAL CURVE**

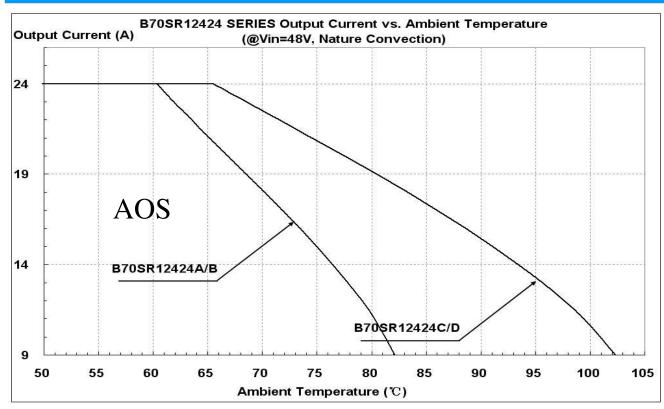


Figure 17: Output Current vs. ambient temperature @Vin=48V



## THERMAL CURVES

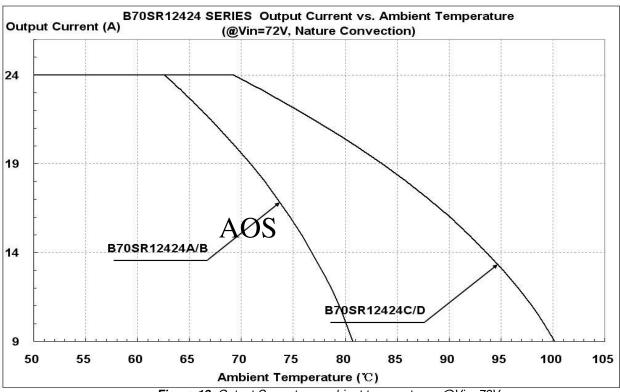


Figure 18: Output Current vs. ambient temperature @Vin=72V

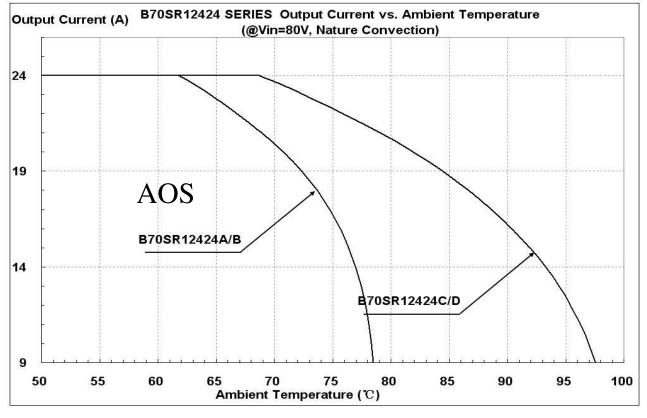


Figure 19: Output Current vs. ambient temperature @Vin=80V



## THERMAL CONSIDERATION

The following figure shows the location to monitor the temperature of base plate. Before customer decides to use this DCDC converter, a thermal evaluation need to be did to make sure the temperature of base plate is lower than that read from below thermal curves (Figure 21~23 base on different input voltage).

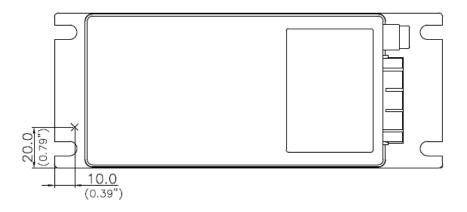


Figure 20: Thermal consideration

## **THERMAL CURVE**

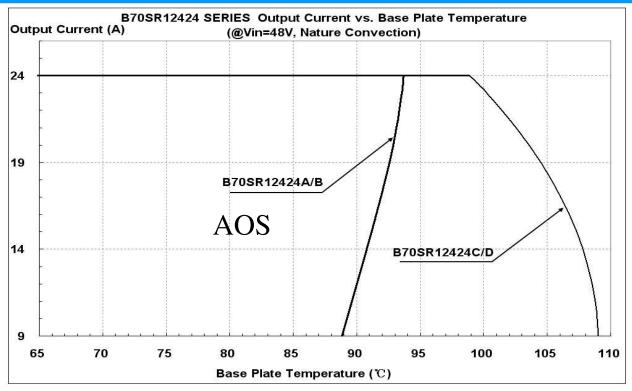


Figure 21: Output Current vs. base plate temperature @Vin=48V



## THERMAL CURVES

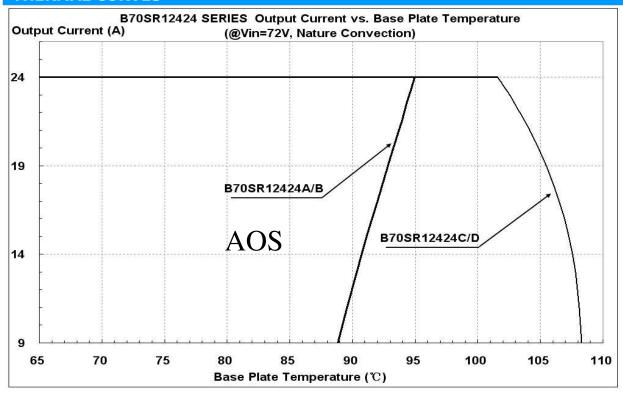


Figure 22: Output Current vs. base plate temperature @Vin=72V

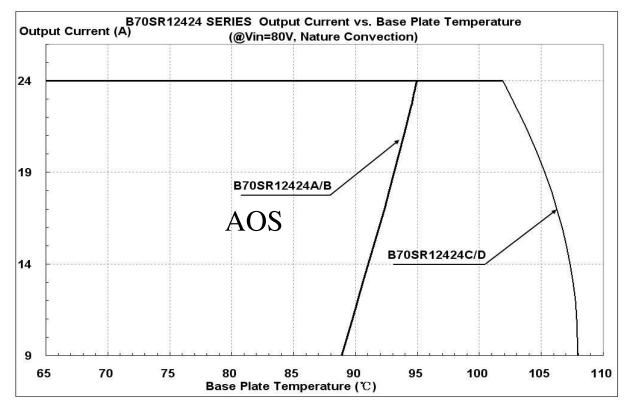
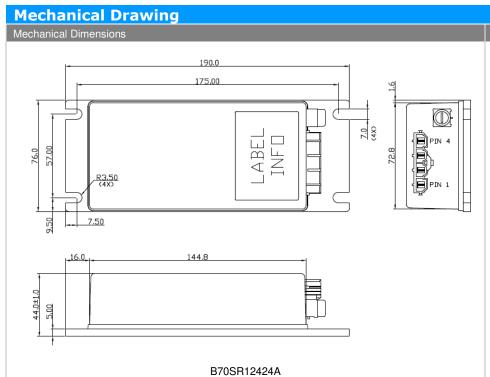


Figure 23: Output Current vs. base plate temperature @Vin=80V

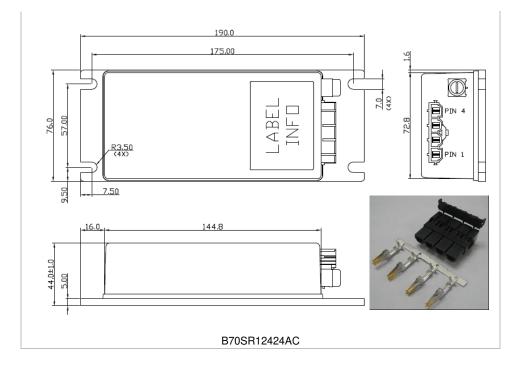




Pin Connections					
Pin	Function Description				
1	OUTPUT -				
2	OUTPUT +				
3	INPUT -				
4	INPUT +				

- > All dimensions in mm (inches)
- ➤ Tolerance:X.X±0.5 (X.XX±0.02)

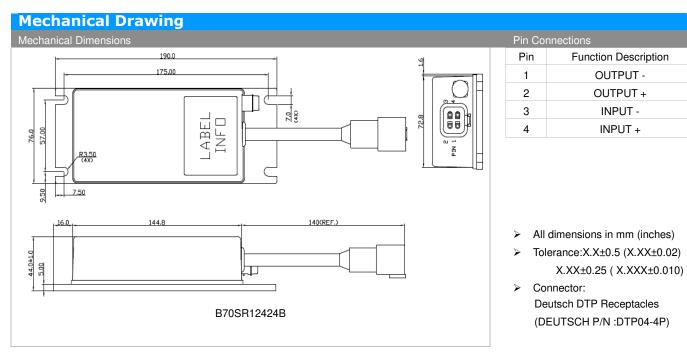
  X.XX±0.25 (X.XXX±0.010)
- ➤ Connector: MOLEX MINI-FIT Sr<sup>TM</sup> Header (MOLEX P/N :42819-4213)

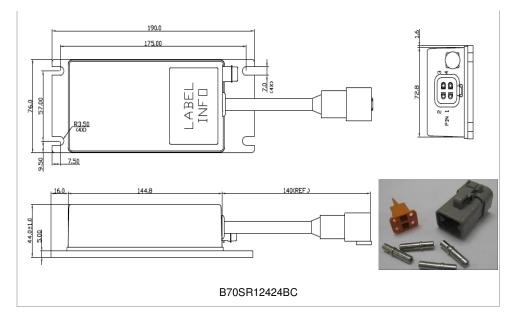


Pin	Function Description
1	OUTPUT -
2	OUTPUT +
3	INPUT -
4	INPUT +

- All dimensions in mm (inches)
- Tolerance:X.X±0.5 (X.XX±0.02)
  X.XX±0.25 (X.XXX±0.010)
- ➤ Connector: MOLEX MINI-FIT Sr<sup>TM</sup> Header (MOLEX P/N :42819-4213)
- Connector kit:
  Housing: 42816-0412
  Terminal: 42815-0042





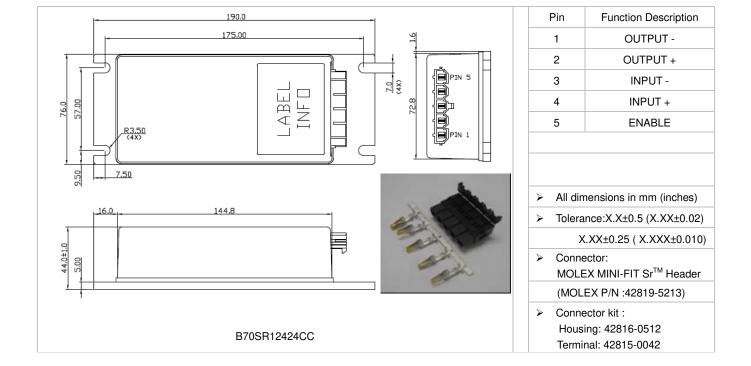


Pin	Function Description
1	OUTPUT -
2	OUTPUT +
3	INPUT -
4	INPUT +

- All dimensions in mm (inches)
- > Tolerance:X.X±0.5 (X.XX±0.02) X.XX±0.25 ( X.XXX±0.010)
- Connector: Deutsch DTP Receptacles (DEUTSCH P/N :DTP04-4P)
- Connector kit:
   Housing: DTP06-4S
   Wedge lock: WP-4S
   Terminal: 0462-203-12141

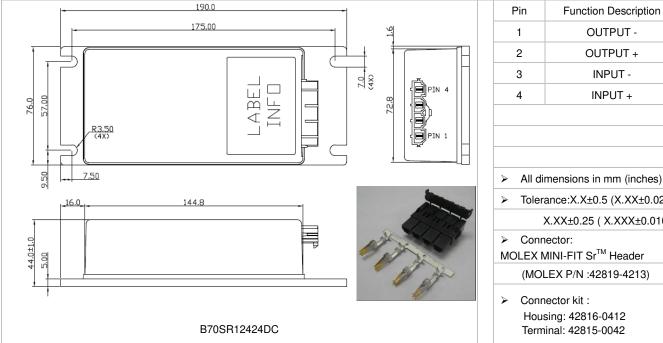


#### **Mechanical Drawing** Pin Connections Mechanical Dimensions Pin Function Description 190.0 OUTPUT -175,00 1 OUTPUT + 2 3 INPUT -INPUT + 4 ABEL 57.00 **ENABLE** 76.0 5 R3.50 (4X) 7,50 All dimensions in mm (inches) Tolerance:X.X±0.5 (X.XX±0.02) 144.8 X.XX±0.25 ( X.XXX±0.010) Connector: 44,0±1,0 $\mathsf{MOLEX}\;\mathsf{MINI}\text{-}\mathsf{FIT}\;\mathsf{Sr}^{\mathsf{TM}}\;\mathsf{Header}$ 5.00 (MOLEX P/N:42819-5213) B70SR12424C





#### **Mechanical Drawing** 190.0 **Function Description** Pin 175.00 OUTPUT -2 OUTPUT + INPUT -3 7.0 (4X) INPUT + LABEL INFO 4 57,00 76.0 R3.50 (4X) All dimensions in mm (inches) 7.50 Tolerance:X.X±0.5 (X.XX±0.02) 16.0 144.8 X.XX±0.25 ( X.XXX±0.010) Connector: 44,0±1,0 $\mathsf{MOLEX}\;\mathsf{MINI}\text{-}\mathsf{FIT}\;\mathsf{Sr}^{\mathsf{TM}}\;\mathsf{Header}$ (MOLEX P/N:42819-4213) B70SR12424D



		r dilotion Beschption				
	1	OUTPUT -				
2 OUTPUT +						
3 INPUT -						
	4	INPUT +				
>	All di	mensions in mm (inches)				
>	Toler	ance:X.X±0.5 (X.XX±0.02)				
		X.XX±0.25 ( X.XXX±0.010)				
>	Conr	ector:				
MC	OLEX N	∕IINI-FIT Sr <sup>™</sup> Header				
	(MOI	LEX P/N :42819-4213)				
<b>&gt;</b>	_	nector kit :				
	Conr	iector kit .				

# **Physical Outline**

190.0x76.0x44.0 mm (7.48"x2.99"x1.73") Case Size

Case Material Case: PC; Plate: AL6063



Part Numbering System																									
В	70	S	R	124	24	A			С																
Form Factor	Input Voltage	Number of Outputs	Product Series	Output Voltage	Output Current	Option Code			Option Fitting																
							With Built-in fuse holder	Enable pin	Sealed connector & fuse holder	Connector Kit															
B-	70 –	S –	R-	R – Regular	R-	R-	R-	R –	R –	R –	R –	124 – 2	124 –	124 –	124 –	124 –	124 –	124 –	124 –	24 –	Α	YES	NO	NO	1xhousing+ 4 terminals
Box	36V~106V Singl	Single Re	V Single			12.4V	12.4V	12.4V	12.4V	12.4V	12.4V	24A	В	YES	NO	YES	1xhousing+ 4 terminals								
																						С	NO	YES	NO
						D	NO	NO	NO	1xhousing+ 4 terminals															

Model List							
Input Voltage Range	Inj	out	Outp	EFF @72VIN 100% LOAD			
B70SR12424(A\B\C\D)	36V~106V	10A	12.4V	24A	90.0%		

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## WARRANTY

Delta offers a two (2) years limited warranty. Complete warranty information is listed on our web site or is available upon request from Delta.

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