

## Features

- Operated at Low Logic Level Gate Drive
- N-Channel Switch with Low  $R_{DS(on)}$
- Epoxy Meets UL 94 V-0 Flammability Rating
- Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note 1)
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

## Maximum Ratings

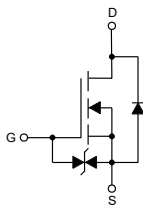
- Operating Junction Temperature Range:  $-55^{\circ}\text{C}$  to  $+150^{\circ}\text{C}$
- Storage Temperature Range:  $-55^{\circ}\text{C}$  to  $+150^{\circ}\text{C}$
- Maximum Thermal Resistance:  $833^{\circ}\text{C/W}$  Junction to Ambient

Parameter	Symbol	Rating	Unit
Drain -source Voltage	$V_{DS}$	20	V
Gate -Source Voltage	$V_{GS}$	$\pm 12$	V
Drain Current-Continuous	$I_D$	0.75	A
Pulsed Drain Current <sup>(Note 2)</sup>	$I_{DM}$	3	A
Power Dissipation	$P_D$	0.15	W

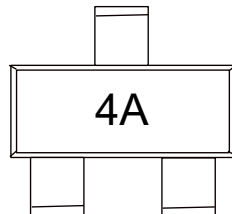
Note: 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

2. Repetitive Rating : Pulse Width Limited by Junction Temperature.

## Internal Structure and Marking Code

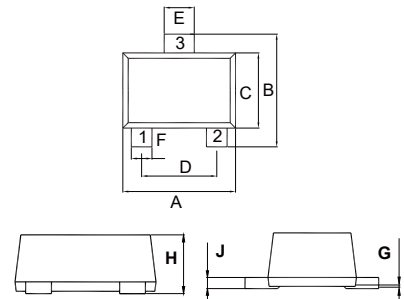


1. GATE
2. SOURCE
3. DRAIN



# N-Channel MOSFET

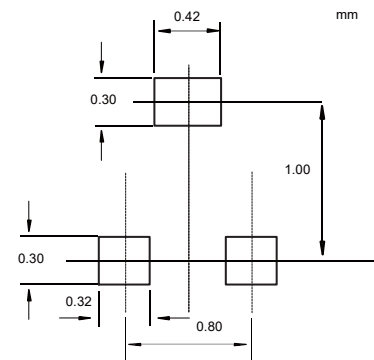
## SOT-723



### DIMENSIONS

DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	0.043	0.051	1.10	1.30	
B	0.043	0.051	1.10	1.30	
C	0.028	0.035	0.70	0.90	
D	0.031		0.80		TYP.
E	0.009	0.017	0.22	0.42	
F	0.005	0.013	0.12	0.32	
G	0.000	0.002	0.00	0.05	
H	0.017	0.021	0.43	0.54	
J	0.003	0.006	0.08	0.15	

### Suggested Solder Pad Layout



**ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)**

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	20			V
Gate-Source Leakage Current	$I_{GSS}$	$V_{DS}=0V, V_{GS}=\pm 10V$			$\pm 10$	$\mu A$
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=20V, V_{GS}=0V$			1	$\mu A$
Gate-Threshold Voltage <sup>(Note 3)</sup>	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	0.35	0.7	1.1	V
Drain-Source On-Resistance <sup>(Note 3)</sup>	$R_{DS(on)}$	$V_{GS}=4.5V, I_D=500mA$		190	300	m $\Omega$
		$V_{GS}=2.5V, I_D=400mA$		280	400	m $\Omega$
		$V_{GS}=1.8V, I_D=200mA$		480	700	m $\Omega$
Forward transconductance	$g_{FS}$	$V_{DS}=10V, I_D=500mA$	0.8			S
Diode Forward Voltage <sup>(Note 3)</sup>	$V_{SD}$	$V_{GS}=0V, I_S=500mA$			1.2	V
<b>Dynamic Characteristics<sup>(Note4,5)</sup></b>						
Input Capacitance	$C_{iss}$	$V_{DS}=16V, V_{GS}=0V, f=1MHz$		33		pF
Output Capacitance	$C_{oss}$			20		
Reverse Transfer Capacitance	$C_{rss}$			10		
Total Gate Charge	$Q_g$	$V_{GS}=4.5V, V_{DS}=10V, I_D=1A$		800		pC
Gate-Source Charge	$Q_{gs}$			290		
Gate-Drain Charge	$Q_{gd}$			160		
Turn-On Delay Time	$t_{d(on)}$	$V_{GS}=4.5V, V_{DS}=10V, I_{DS}=0.5A, R_G=10\Omega$		4		ns
Turn-On Rise Time	$t_r$			18		
Turn-Off Delay Time	$t_{d(off)}$			11.6		
Turn-Off Fall Time	$t_f$			24		

Note 3. Pulse Test : Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .

4. Switching characteristics are independent of operating junction temperature.

5. Guaranteed by Design, Not Subject to Production Testing.

**Curve Characteristics**

Fig. 1 - Output Characteristics

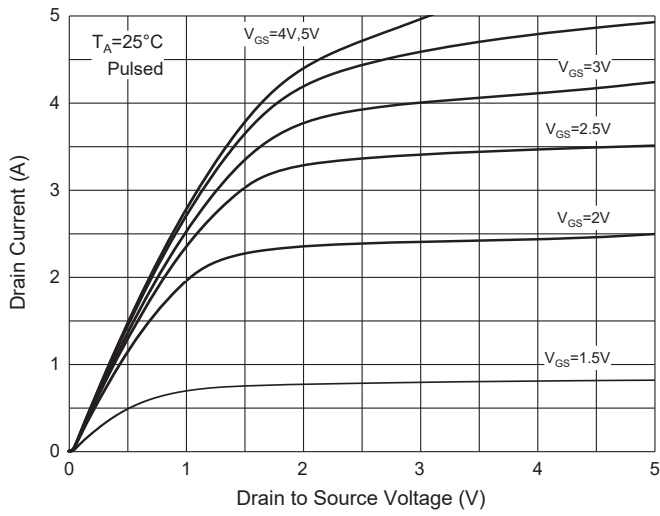


Fig. 2 - Transfer Characteristics

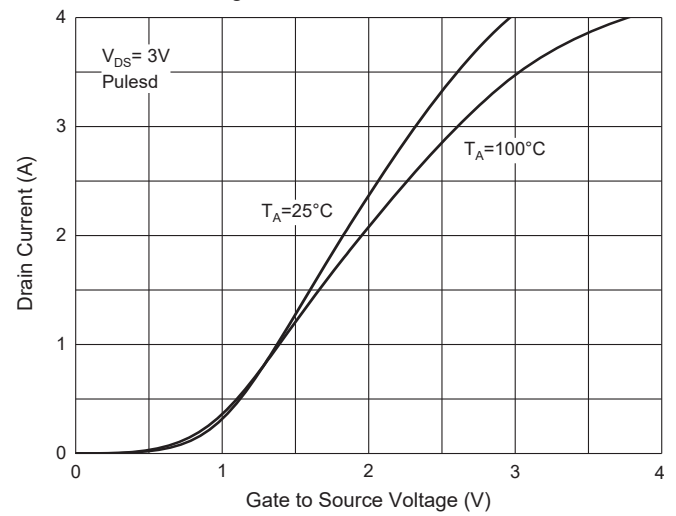


Fig. 3 -  $R_{DS(ON)} - I_D$

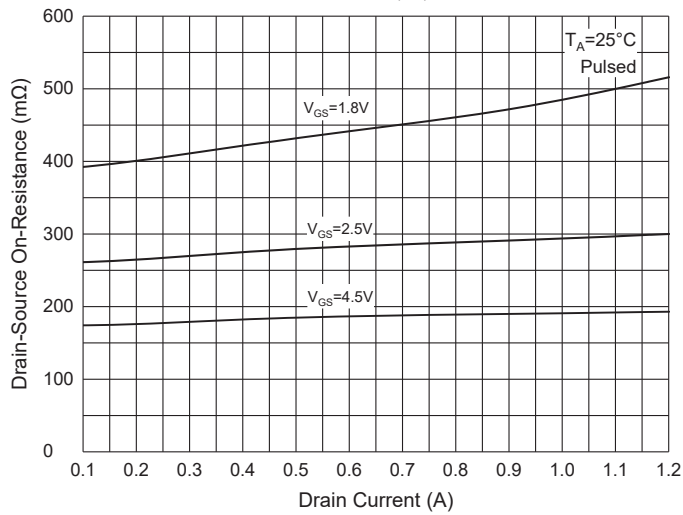


Fig. 4 -  $R_{DS(ON)} - V_{GS}$

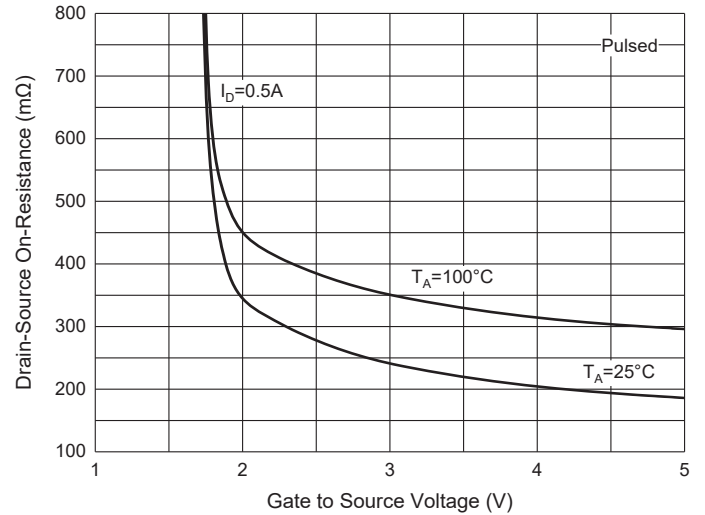


Fig. 5 -  $I_S - V_{SD}$

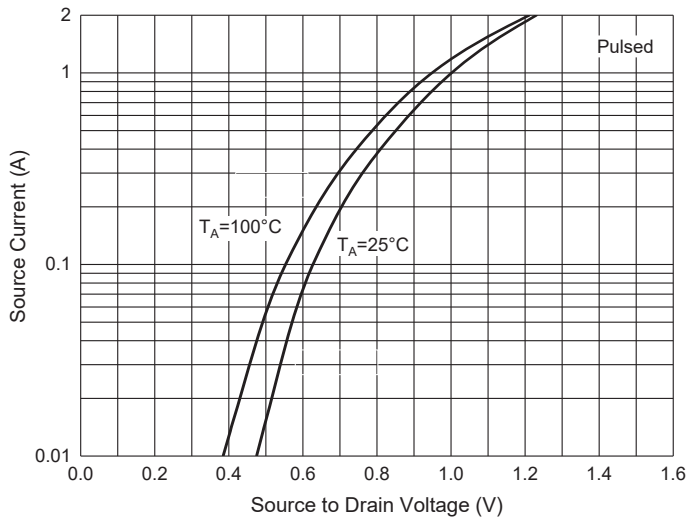
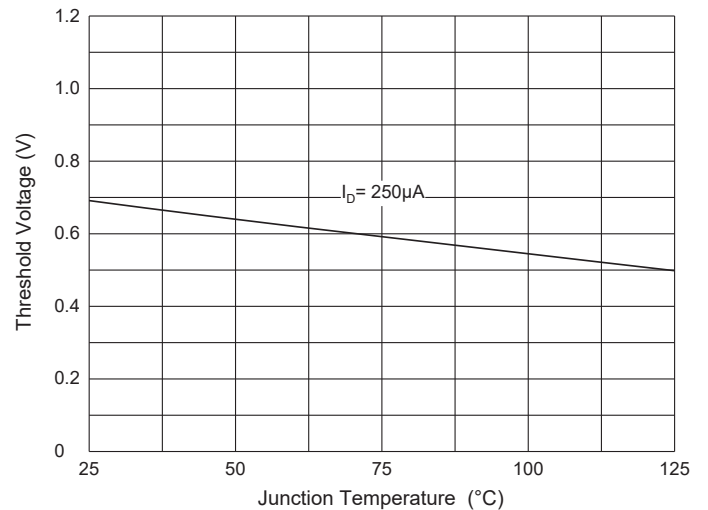


Fig. 6 - Threshold Voltage



Curve Characteristics

Fig. 7 - Capacitance Characteristics

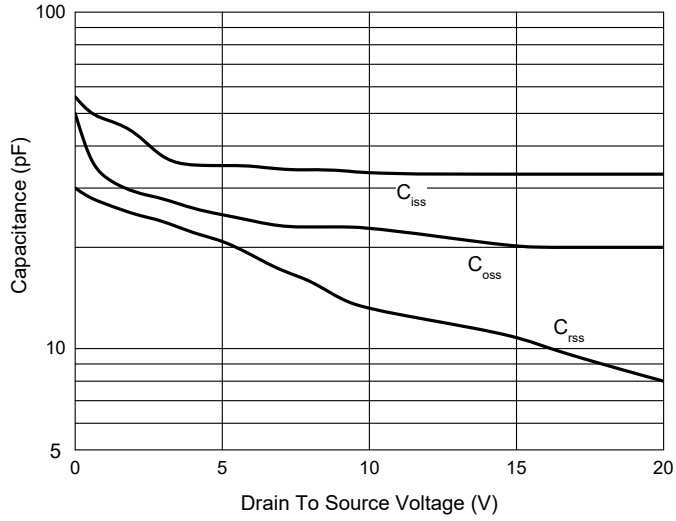
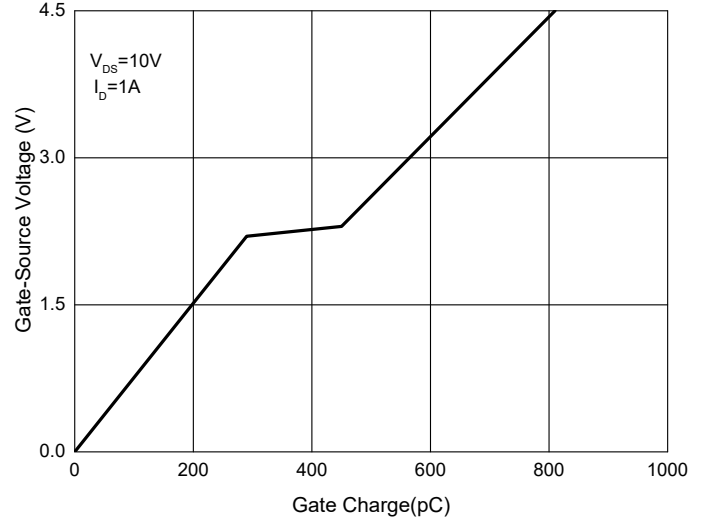


Fig. 8 - Gate Charge



## Ordering Information

Device	Packing
Part Number-TP	Tape&Reel:8Kpcs/Reel

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