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April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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SILICON POWER TRANSISTOR



2SD1692

NPN SILICON EPITAXIAL TRANSISTOR (DARLINGTON CONNECTION)

FEATURES

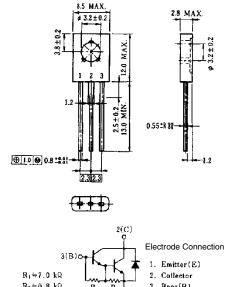
- High DC current gain due to Darlington connection
- Large current capacity and low VcE(sat)
- Large power dissipation TO-126 type power transistor
- Complementary transistor: 2SB1149

ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

Parameter	Symbol	Ratings	Unit
Collector to base voltage	VcBo	150	V
Collector to emitter voltage	VCEO	100	V
Emitter to base voltage	V _{EBO}	8.0	V
Collector current (DC)	Ic(DC)	±3.0	Α
Collector current (pulse)	I _{C(pulse)} *	±5.0	Α
Total power dissipation	P⊤ (Ta = 25°C)	1.3	W
Total power dissipation	P⊤ (Tc = 25°C)	15	W
Junction temperature	Tj	150	°C
Storage temperature	T _{stg}	-55 to +150	°C

^{*} PW \leq 10 ms, duty cycle \leq 50%

PACKAGE DRAWING (UNIT: mm)



$R_2 = 0.8 \text{ k}\Omega$

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Collector to emitter voltage	VCEO(SUS)	Ic = 3.0 A, IB = 3.0 mA, L = 1.0 mH	100			V
Collector cutoff current	Ісво	$V_{CB} = 100 \text{ V}, I_E = 0$			10	μΑ
Collector cutoff current	ICEO	Vce = 100 V, Rbe = ∞			1.0	mA
DC current gain	hFE1**	Vce = 2.0 V, Ic = 1.5 A	2,000		20,000	
DC current gain	hFE2**	Vce = 2.0 V, Ic = 3.0 A	1,000			
Collector saturation voltage	V _{CE(sat)} **	Ic = 1.5 A, I _B = 1.5 mA		0.9	1.2	V
Base saturation voltage	V _{BE(sat)} **	Ic = 1.5 A, I _B = 1.5 mA		1.5	2.0	V
Turn-on time	ton	Ic = 1.5 A		0.5		μs
Storage time	tstg	$I_{B1} = -I_{B2} = 1.5 \text{ mA}$		2.0		μs
Fall time	tf	$R_L = 27 \Omega$, $V_{CC} \cong 40 V$		1.0		μs

^{**} Pulse test PW \leq 350 μ s, duty cycle \leq 2%/per pulsed

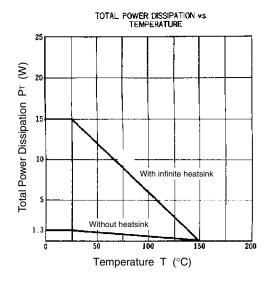
hfe CLASSIFICATION

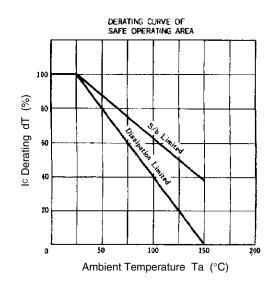
Marking	М	L	K
h _{FE1}	2,000 to 5,000	4,000 to 12,000	8,000 to 20,000

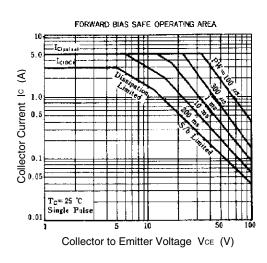
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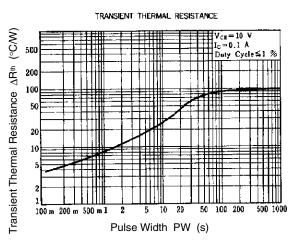


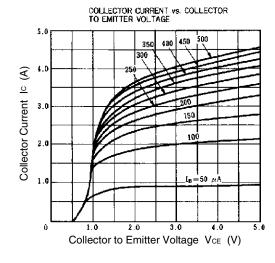
TYPICAL CHARACTERISTICS (Ta = 25°C)

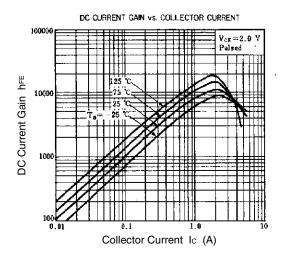


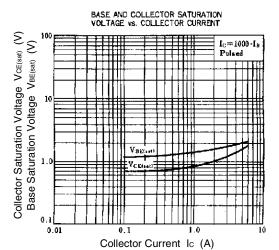




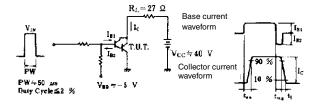








SWITCHING TIME ($t_{on,\ t_{stg},\ t_f}$) TEST CIRCUIT





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