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April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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# RENESAS

# MOS FIELD EFFECT TRANSISTOR NP60N03KUG

# SWITCHING N-CHANNEL POWER MOS FET

# DESCRIPTION

The NP60N03KUG is N-channel MOS Field Effect Transistor designed for high current switching applications.

## FEATURES

- Channel temperature 175 degree rating
- Super low on-state resistance

 $R_{DS(on)}$  = 4.8 m $\Omega$  MAX. (V<sub>GS</sub> = 10 V, I<sub>D</sub> = 30 A)

• Low Ciss: Ciss = 3500 pF TYP.

## ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (VGs = 0 V)	VDSS	30	V
Gate to Source Voltage (V <sub>DS</sub> = 0 V)	Vgss	±20	V
Drain Current (DC) (Tc = 25°C)	D(DC)	±60	Α
Drain Current (pulse) <sup>Note1</sup>	D(pulse)	±240	Α
Total Power Dissipation (T <sub>A</sub> = 25°C)	P <sub>T1</sub>	1.8	W
Total Power Dissipation (Tc = 25°C)	P <sub>T2</sub>	88	W
Channel Temperature	Tch	175	°C
Storage Temperature	Tstg	–55 to +175	°C
Repetitive Avalanche Current Note2	lar	33	А
Repetitive Avalanche Energy Note2	Ear	109	mJ

**Notes 1.** PW  $\leq$  10  $\mu$ s, Duty Cycle  $\leq$  1%

**2.** Tch < 150°C, VDD = 15 V, RG = 25  $\Omega$ , VGS = 20  $\rightarrow$  0 V

#### THERMAL RESISTANCE

Channel to Case Thermal Resistance	Rth(ch-C)	1.70	°C/W
Channel to Ambient Thermal Resistance	Rth(ch-A)	83.3	°C/W

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## ORDERING INFORMATION

PART NUMBER	PACKAGE
NP60N03KUG	TO-263 (MP-25ZK)



(TO-263)

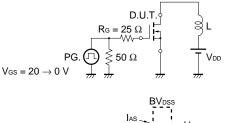
# ELECTRICAL CHARACTERISTICS (TA = 25°C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V			1	μA
Gate Leakage Current	lgss	V <sub>GS</sub> = ±20 V, V <sub>DS</sub> = 0 V			±100	nA
Gate to Source Threshold Voltage	VGS(th)	Vos = Vgs, Id = 250 μA	2.0	3.0	4.0	V
Forward Transfer Admittance Note	y <sub>fs</sub>	Vds = 10 V, Id = 30 A	10	21		S
Drain to Source On-state Resistance Note	RDS(on)	Vgs = 10 V, Id = 30 A		3.6	4.8	mΩ
Input Capacitance	Ciss	V <sub>DS</sub> = 25 V		3500	5300	pF
Output Capacitance	Coss	V <sub>GS</sub> = 0 V		400	600	pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		260	470	pF
Turn-on Delay Time	td(on)	V <sub>DD</sub> = 15 V, I <sub>D</sub> = 30 A		32	70	ns
Rise Time	tr	V <sub>GS</sub> = 10 V		52	130	ns
Turn-off Delay Time	td(off)	R <sub>G</sub> = 0 Ω		73	150	ns
Fall Time	tr			12	30	ns
Total Gate Charge	QG	V <sub>DD</sub> = 24 V		62	93	nC
Gate to Source Charge	QGS	V <sub>GS</sub> = 10 V		14		nC
Gate to Drain Charge	Qgd	I <sub>D</sub> = 60 A		21		nC
Body Diode Forward Voltage Note	VF(S-D)	IF = 60 A, VGS = 0 V		0.9	1.5	V
Reverse Recovery Time	trr	IF = 60 A, VGS = 0 V		38		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/µs		41		nC

Note Pulsed

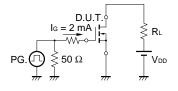
#### TEST CIRCUIT 1 AVALANCHE CAPABILITY

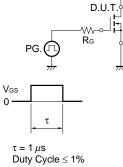
#### **TEST CIRCUIT 2 SWITCHING TIME**

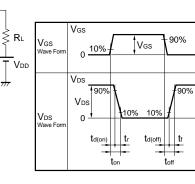




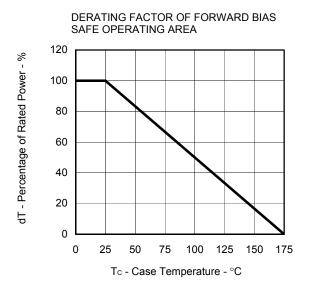
#### TEST CIRCUIT 3 GATE CHARGE

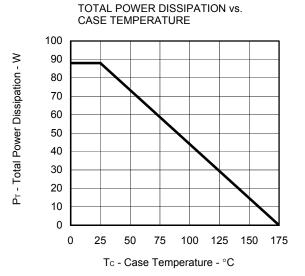




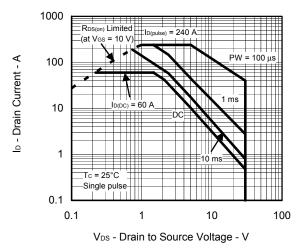


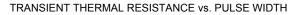
# TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25°C)

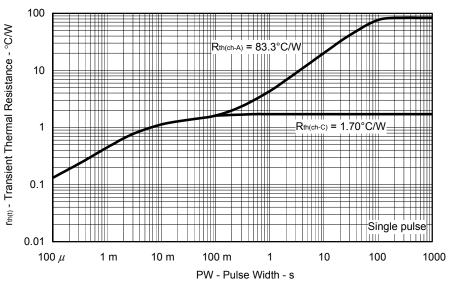


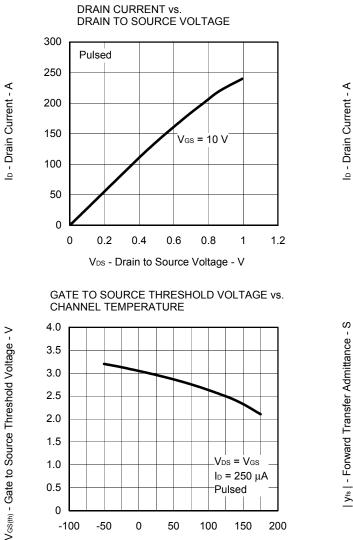


FORWARD BIAS SAFE OPERATING AREA









Tch - Channel Temperature - °C

50

100

150

200

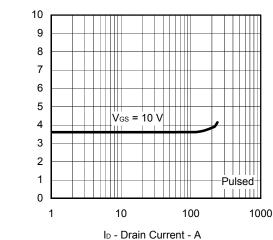
0

-50

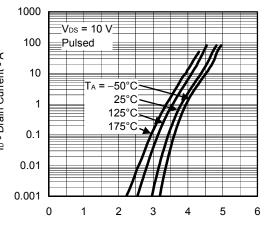
0

-100

DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT

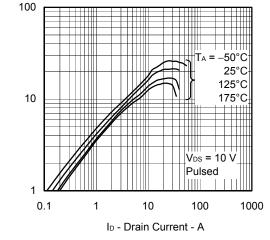


FORWARD TRANSFER CHARACTERISTICS

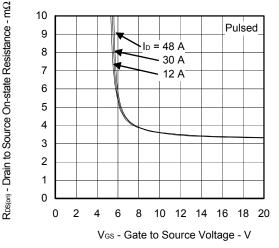


VGS - Gate to Source Voltage - V

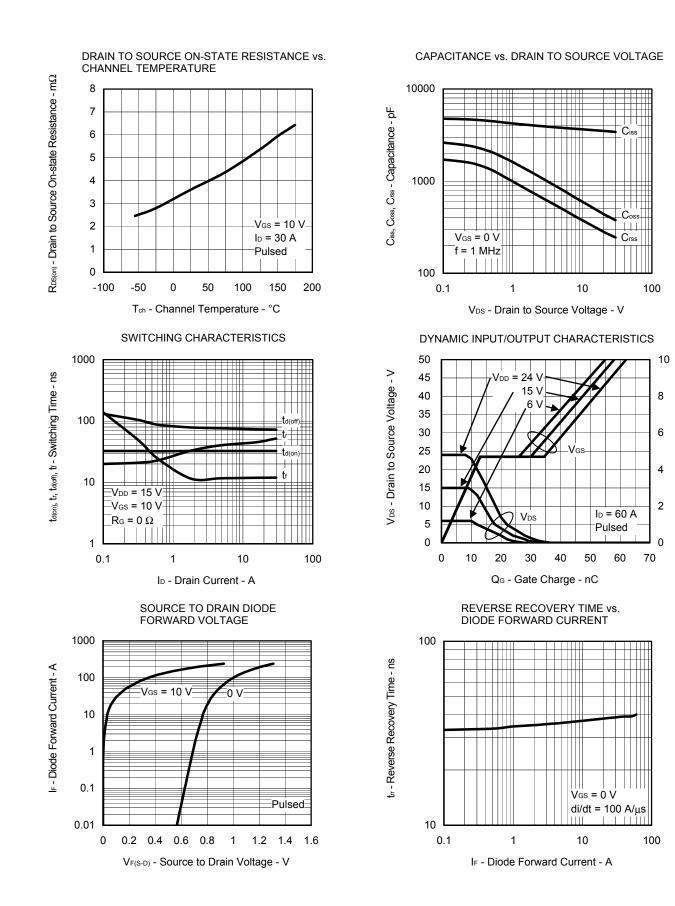
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



 $R_{\text{DS}(\text{on})}$  - Drain to Source On-state Resistance -  $m\Omega$ 

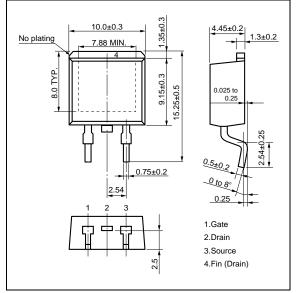


#### Data Sheet D16860EJ1V0DS

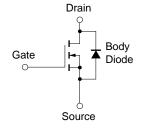
V<sub>GS</sub> - Gate to Source Voltage - V

# PACKAGE DRAWING (Unit: mm)

#### TO-263 (MP-25ZK)



#### EQUIVALENT CIRCUIT



**Remark** Strong electric field, when exposed to this device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop generation of static electricity as much as possible, and quickly dissipate it once, when it has occurred.

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