SDFS081A - MARCH 1987 - REVISED OCTOBER 1993

- Compares Two 8-Bit Words
- 20-kΩ Pullup Resistors on Q Inputs
- Package Options Include Plastic Small-Outline Packages, Ceramic Chip Carriers, and Standard Plastic and Ceramic 300-mil DIPs

description

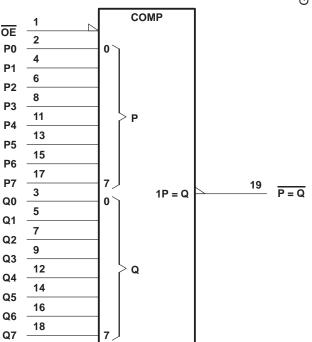
These identity comparators perform comparisons on two 8-bit binary or BCD words. They provide $\overline{P}=\overline{Q}$ outputs. The 'F520 features 20-k Ω pullup termination resistors on the Q inputs for analog or switch data.

The SN54F520 is characterized for operation over the full military temperature range of -55° C to 125°C. The SN74F520 is characterized for operation from 0°C to 70°C.

FUNCTION TABLE

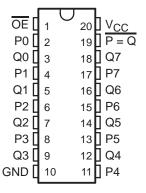
INPU	JTS	OUTPUT				
P, Q	OE	P = Q				
P = Q	L	L				
P≠Q	Χ	Н				
X	Н	Н				

logic symbol†

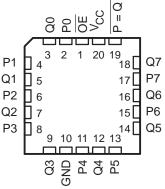


[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

SN54F520 . . . J PACKAGE SN74F520 . . . DW OR N PACKAGE (TOP VIEW)



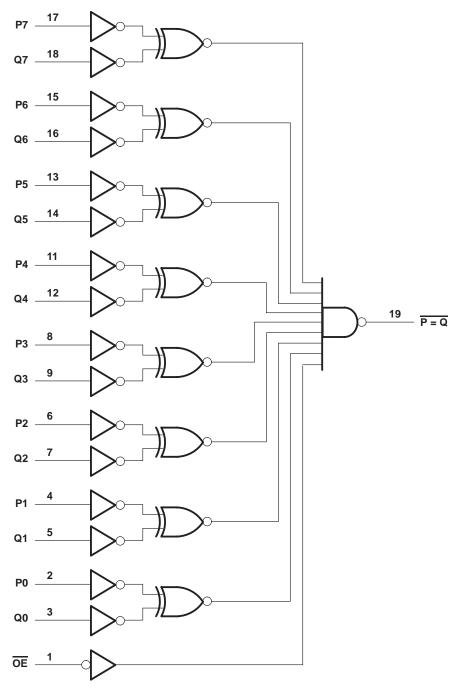
SN54F520 . . . FK PACKAGE (TOP VIEW)





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logic diagram (positive logic)



NOTE: $20-k\Omega$ pullup resistors are on the Q inputs.



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absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage range, V _{CC}	0.5 V to 7 V
Input voltage range, V _I (see Note 1)	–1.2 V to 7 V
Input current range	–30 mA to 5 mA
Voltage range applied to any output in the high state	0.5 V to V _{CC}
Current into any output in the low state	40 mÅ
Operating free-air temperature range: SN54F520	–55°C to 125°C
SN74F520	0°C to 70°C
Storage temperature range	65°C to 150°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

recommended operating conditions

		SN54F520)	SN74F520			
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
VCC	V _{CC} Supply voltage		5	5.5	4.5	5	5.5	V
VIH	High-level input voltage				2			V
V _{IL}	Low-level input voltage			0.8			0.8	V
liK	(Input clamp current			-18			-18	mA
ЮН	High-level output current			- 1			- 1	mA
loL	Low-level output current			20			20	mA
TA	Operating free-air temperature	-55 125 0 70		°C				

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

242445752		TEST CONDITIONS		S	SN54F520			SN74F520			
1	PARAMETER TEST CONDITIONS		I CONDITIONS	MIN	TYP‡	MAX	MIN	TYP [‡]	MAX	UNIT	
VIK		$V_{CC} = 4.5 \text{ V},$	$I_{I} = -18 \text{ mA}$			-1.2			-1.2	V	
	V _{CC} = 4.5 V,		I _{OH} = – 1 mA	2.5	3.4		2.5	3.4		V	
VOH		$V_{CC} = 4.75 \text{ V},$	I _{OH} = – 1 mA				2.7			V	
VOL		$V_{CC} = 4.5 \text{ V},$	$I_{OL} = 20 \text{ mA}$		0.3	0.5		0.3	0.5	V	
	OE and P inputs	V 55V	V _I = 7 V			0.1			0.1	4	
I _I	Q inputs	V _{CC} = 5.5 V	V _I = 5.5 V			0.1			0.1	mA	
	OE and P inputs	.,	V 0.7V			20			20	μΑ	
lн	Q inputs	$V_{CC} = 5.5 \text{ V},$	$V_{ } = 2.7 \text{ V}$			-0.3			-0.3	mA	
	OE and P inputs	V 55V	V 05V			- 0.6			- 0.6	4	
IIL.	Q inputs	$V_{CC} = 5.5 \text{ V},$	$V_{I} = 0.5 V$			- 1			– 1	mA	
los§		V _{CC} = 5.5 V,	VO = 0	-60		-150	-60		-150	mA	
ICC		V _{CC} = 5.5 V,	See Note 2		21	32		21	32	mA	

[‡] All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$.

NOTE 2: ICC is measured with all inputs at 4.5 V.



NOTE 1: The input voltage ratings may be exceeded provided the input current ratings are observed.

[§] Not more than one output should be shorted at a time, and the duration of the short circuit should not exceed one second.

SN54F520, SN74F520 8-BIT IDENTITY COMPARATORS

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switching characteristics (see Note 3)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	T 25°(F, Ω,	C _L = 50 R _L = 50 T _A = M		•		UNIT	
			′F520		SN54F520		SN74F520				
			MIN	TYP	MAX	MIN	MAX	MIN	MAX		
t _{PLH}	D O	$\overline{P} = Q$	3.9	5.7	7.7	3.7	10.2	3.7	8.7		
t _{PHL}	P or Q	P = Q	4.7	7	9.3	4.4	11.3	4.4	10.3	ns	
t _{PLH}	ŌĒ	P = Q	3.5	4.6	5.8	3.4	7	3.4	6.4	20	
^t PHL	OE	P = Q	5.2	7.5	9.5	4.9	11.2	4.9	10.4	ns	

[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions. NOTE 3: Load circuits and waveforms are shown in Section 1.

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