TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74ACT540P,TC74ACT540F,TC74ACT540FW,TC74ACT540FT TC74ACT541P,TC74ACT541F,TC74ACT541FW,TC74ACT541FT

Octal Bus Buffer

TC74ACT540P/F/FW/FT Inverting, 3-State

Outputs

TC74ACT541P/F/FW/FT Non-Inverting,

3-State Outputs

The TC74ACT540/TC74ACT541 are advanced high speed CMOS OCTAL BUS BUFFERs fabricated with silicon gate and double-layer metal wiring C^2MOS technology.

They achieve the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

These devices may be used as a level converter for interfacing TTL or NMOS to High Speed CMOS. The inputs are compatible with TTL, NMOS and CMOS output voltage levels.

The TC74ACT540 is an inverting type, and the TC74ACT541 is a non-inverting type.

When either $\overline{G}1$ or $\overline{G}2$ are high, the terminal outputs are in the high-impedance state.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

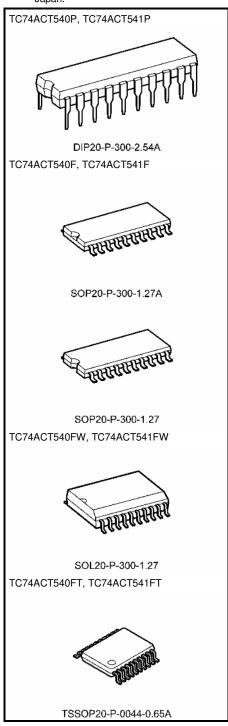
Features

- High speed: $t_{pd} = 4.3 \text{ ns (typ.)}$ at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $ICC = 8 \mu A$ (max) at Ta = 25°C
- Compatible with TTL outputs
 - VIL = 0.8 V (max) VIH = 2.0 V (min)
- Symmetrical output impedance
 - $: |I_{OH}| = I_{OL} = 24 \text{ mA (min)}$

Capability of driving 50Ω transmission lines.

- Balanced propagation delays: t_{pLH} ≃ t_{pHL}
- \bullet Pin and function compatible with 74F540/541

Note: xxxFW (JEDEC SOP) is not available in Japan.

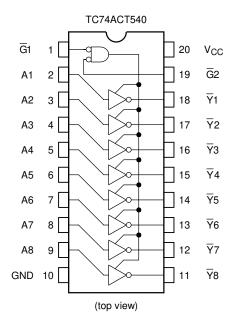


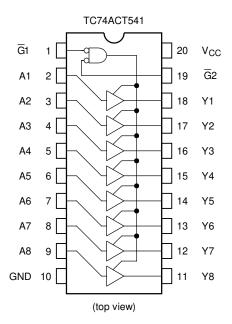
Weight

DIP20-P-300-2.54A : 1.30 g (typ.) SOP20-P-300-1.27A : 0.22 g (typ.) SOP20-P-300-1.27 : 0.22 g (typ.) SOL20-P-300-1.27 : 0.46 g (typ.) TSSOP20-P-0044-0.65A : 0.08 g (typ.)

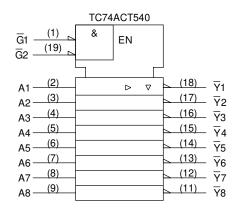


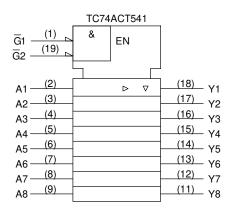
Pin Assignment





IEC Logic Symbol





Truth Table

	Inputs	Outputs				
G1	G2	A _n	Yn	\overline{Y}_n		
Н	Х	Χ	Z	Z		
Х	Н	Х	Z	Z		
L	L	Н	Н	L		
L	L	L	L	Н		

X: Don't care

Z: High impedance

 Y_n : ACT541 \overline{Y}_n : ACT540



Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	−0.5 to 7.0	V
DC input voltage	V _{IN}	-0.5 to V _{CC} + 0.5	V
DC output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5	V
Input diode current	I _{IK}	±20	mA
Output diode current	lok	±50	mA
DC output current	l _{OUT}	±50	mA
DC V _{CC} /ground current	Icc	±200	mA
Power dissipation	P _D	500 (DIP) (Note 2)/180 (SOP/TSSOP)	mW
Storage temperature	T _{stg}	–65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Note 2: 500 mW in the range of Ta = -40 to 65°C. From Ta = 65 to 85°C a derating factor of -10 mW/°C should be applied up to 300 mW.

Recommended Operating Conditions (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	4.5 to 5.5	V
Input voltage	V _{IN}	0 to V _{CC}	V
Output voltage	V _{OUT}	0 to V _{CC}	V
Operating temperature	T _{opr}	-40 to 85	°C
Input rise and fall time	dt/dV	0 to 10	ns/V

Note: The recommended operating conditions are required to ensure the normal operation of the device.

Unused inputs must be tied to either VCC or GND.



Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit	
	Gymbol				V _{CC} (V)	Min	Тур.	Max	Min	Max	Onne
High-level input voltage	V _{IH}	_			4.5 to 5.5	2.0	_	_	2.0	_	٧
Low-level input voltage	V _{IL}	_		4.5 to 5.5	_	_	0.8	_	0.8	V	
	V _{ОН}	V _{IN} = V _{IH} or V _{IL}	$I_{OH} = -50 \mu A$		4.5	4.4	4.5	_	4.4	_	
High-level output voltage			$I_{OH} = -24 \text{ mA}$		4.5	3.94	_	_	3.80	_	٧
l			$I_{OH} = -75 \text{ mA}$	(Note)	5.5	_	_	_	3.85	_	
	V _{OL}	V _{IN} = V _{IH} or V _{IL}	$I_{OL} = 50 \mu A$		4.5	_	0.0	0.1	_	0.1	
Low-level output voltage			I _{OL} = 24 mA		4.5	_	_	0.36	_	0.44	٧
Voltage			$I_{OL} = 75 \text{ mA}$	(Note)	5.5	_	_	_	_	1.65	
3-state output off-state current	I _{OZ}	$V_{IN} = V_{IH}$ or V_{IL} $V_{OUT} = V_{CC}$ or GND		5.5	_	_	±0.5	_	±5.0	μА	
Input leakage current	I _{IN}	V _{IN} = V _{CC} or GND		5.5	_	_	±0.1	_	±1.0	μА	
Quiescent supply current	I _{CC}	V _{IN} = V _{CC} or GND		5.5	_	_	8.0	_	80.0	μΑ	
	IC	Per input: V _{IN} = 3.4 V Other input: V _{CC} or GND		5.5	_	_	1.35	_	1.5	mA	

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Note: This spec indicates the capability of driving 50 Ω transmission lines.

One output should be tested at a time for a 10 ms maximum duration.



AC Characteristics (C_L = 50 pF, R_L = 500 Ω , input: t_r = t_f = 3 ns)

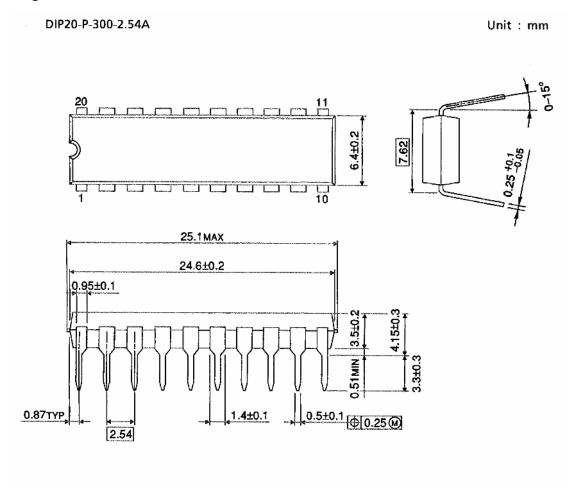
Characteristics	Symbol	Test Condition	Ta = 25°C			Ta = -40 to 85°C		Unit	
	-,		V _{CC} (V)	Min	Тур.	Max	Min	Max	
Propagation delay time (Note 2)	t _{pLH} t _{pHL}	_	5.0 ± 0.5	_	5.0	8.3	1.0	9.5	ns
Propagation delay time (Note 3)	t _{pLH}	_	5.0 ± 0.5	_	5.0	8.3	1.0	9.5	ns
Output enable time	t _{pZL}	_	5.0 ± 0.5	_	7.3	11.4	1.0	13.0	ns
Output disable time	t _{pLZ} t _{pHZ}	_	5.0 ± 0.5	ı	5.9	9.2	1.0	10.5	ns
Input capacitance	C _{IN}	_		_	5	10	_	10	pF
Output capacitance	C _{OUT}	_		_	10	_	_	_	pF
Power dissipation capacitance	C _{PD} (Note 1)	TC74ACT540 TC74ACT541		_	24 27	_	_	_ _	pF

Note 1: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

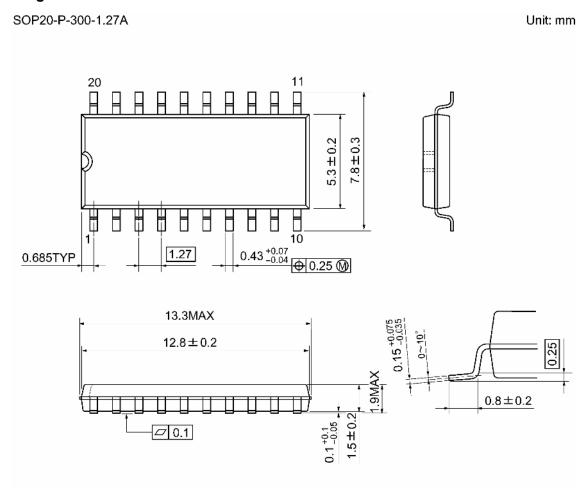
Average operating current can be obtained by the equation:

 $I_{CC\;(opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/8 \;(per\;bit)$

Note 2: For TC74ACT540 only Note 3: For TC74ACT541 only

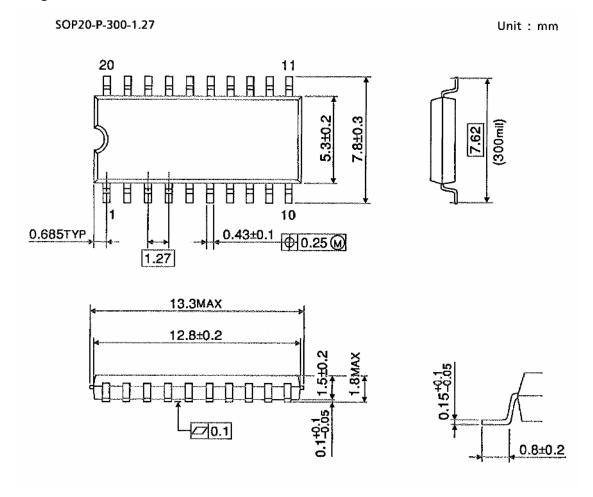


Weight: 1.30 g (typ.)



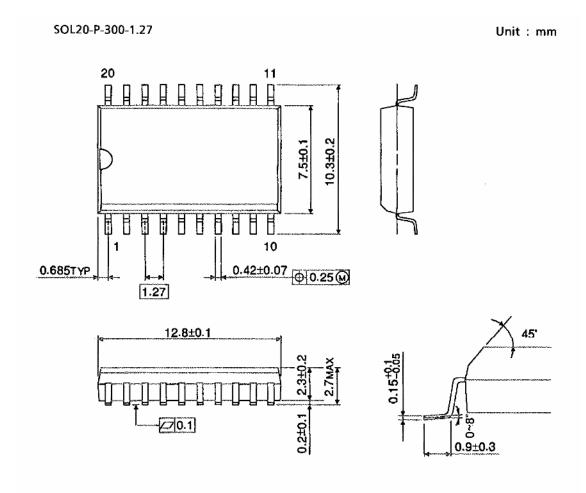
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Weight: 0.22 g (typ.)



Weight: 0.22 g (typ.)

Package Dimensions (Note)



Note: This package is not available in Japan.

Weight: 0.46 g (typ.)

TSSOP20-P-0044-0.65A Unit: mm 6.4±0.2 $0.22\substack{+0.09 \\ -0.06}$ 0.65 0.325TYP ♦0.13**M** 6.9MAX 6.5±0.1 1.2MAX 0~10 0.25 1.0±0.05 0.1±0.05 Ś Ø0.1S (0.5)

Weight: 0.08 g (typ.)

0.45~0.75

Note: Lead (Pb)-Free Packages

DIP20-P-300-2.54A SOP20-P-300-1.27A TSSOP20-P-0044-0.65A

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