

Technical Data Sheet

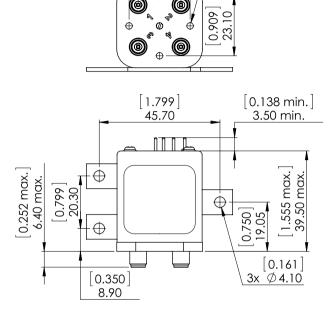
DPDT Ramses SMA2.9 40GHz Latching 12Vdc TTL Diodes Pins Terminals with bracket

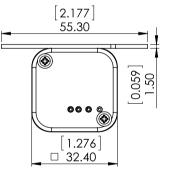
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PAGE 1/2		ISSUE 11-04-17		SERIE : DPDT		PART NUMBER : R577832100		
<u>RF C</u>	HARACTERI	<u>STICS</u>						
	Frequency ra	ange		:	0 - 40 GHz			
	Impedance			:	50 Ohms			
	Frequency (GHz)	DC - 6	6 - 12.4	12.4 - 18	18 - 26.5	26.5 - 40	
VSWR max		1.30	1.40	1.50	1.70	1.90		
Insertion loss max		0.30 dB	0.40 dB	0.50 dB	0.70 dB	0.80 dB		
	Isolation mir		70 dB	60 dB	60 dB	55 dB	50 dB	
	Average pov	ver (*)	80 W	60 W	50 W	20 W	10 W	
ELEC	CTRICAL CHA	RACTERIS	<u>TICS</u>					
	Actuator			:	LATCHING			
	Nominal cur	rent **		:	320 mA			
	Actuator volt	tage (Vcc)		:	12V (10.2 to	13V)		
	Terminals			:	solder pins	(250°C max.	/ 30 sec.)	
	TTL inputs (E) - High level			:	2.2 to 5.5 V	/ 800µA at 5.	5 V	
		- Low	level	:	0 to 0.8 V / 2	20µA at 0.8 V	1	
	Connectors Life Switching Time*** Construction Weight			 SMA 2.9 female per MIL-C 39012 2.5 million cycles < 15 ms Splashproof < 100 g 				
ENV	IRONMENTAI	_ CHARACT	ERISTICS					
	Operating temperature range			:	: -40°C to +85°C			
	Storage temperature range			:	-55°C to +8	5°C	ROHS	
(*	Average pov	ver at 25°C p	per RF Path)					
(**	At 25° C ±10%)							OMPLIAN
(***	Nominal volt	age ; 25° C)						"PLI

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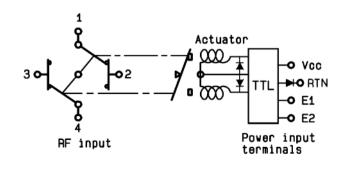






General tolerances : ±0,5 mm [0,02 in]

SCHEMATIC DIAGRAM



TTL input	RF Continuity			
E1=1 / E2=0	$1 \leftrightarrow 3 \ / \ 2 \leftrightarrow 4$			
E1=0 / E2=1	$1 \leftrightarrow 2 \ / \ 3 \leftrightarrow 4$			
E1=0 / E2=0	Memory			
E1=1 / E2=1	Forbidden			

LABEL

RADIALL® R577832100 0 - 40 GHz

Un : 12V

Lot : ____

E1 E2 GND VCC

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