

ISL32704EVAL2Z

User's Manual: Evaluation Board

Industrial Precision



ISL32704EVAL2Z

Evaluation Board User Guide

UG120 Rev.0.00 Apr 3, 2017

1. Overview

The ISL32704EVAL2Z board enables the evaluation of the <u>ISL32704EIBZ</u> isolated, half-duplex RS-485 transceiver in a typical bus node application, used in isolated industrial networks.

1.1 Specifications

This board has been configured and optimized for the following operating conditions:

- Up to 4Mbps data rate
- $V_{CC} = 3.3V$

1.2 Ordering Information

Part Number	Description
ISL32704EVAL2Z	ISL32704EIBZ evaluation board

1.3 Key Features

- Drive capability of up to 32x1 UL transceivers
- Isolated 3.3V to 5V DC/DC converter with regulated output

1.4 Related Literature

- For a full list of related documents, visit our website
 - ISL32704E product page

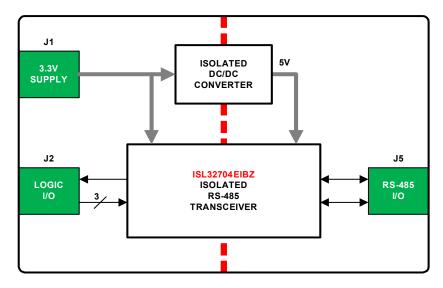


Figure 1.1 Block Diagram

2. Functional Description

The ISL32704EIBZ is an isolated, 4Mbps, half-duplex RS-485 transceiver. The device incorporates a patented refresh circuit to maintain the correct output state with respect to data input. At power-up, the bus outputs follow the truth tables shown in <u>Tables 2.1</u> and <u>2.2</u>.

The DE input should be held low during power-up to prevent false drive data pulses on the bus. This is accomplished by connecting the DE using a $10k\Omega$ pull-down resistor to GND1.

This transceiver uses a differential input receiver for maximum noise immunity and common-mode rejection. Input sensitivity is ± 200 mV, as required by the RS-485 specification.

The receiver input resistance meets the RS-485 "Unit Load" (UL) requirement of $12k\Omega$ minimum. The receiver includes a "fail-safe if open" function that ensures a high-level receiver output if the receiver inputs are unconnected (floating). The receiver output is tri-statable through the active low \overline{RE} input.

The RS-485 driver is a differential output device that delivers at least 1.5V across a 54Ω purely differential load. The driver features low propagation delay skew to maximize bit width and to minimize EMI.

The driver in the ISL32704E is tri-statable through the active high DE input. The outputs of the ISL32704E driver are not slew rate limited, so faster output transition times allow data rates of at least 4Mbps.

Important Notice:

For convenience, an isolated DC/DC converter is used to provide the power supply across the isolation barrier. Its isolation ratings however, are lower than those of the transceiver. Therefore, it must be ensured that the ground potential difference between the left side (GND1, GND2) and the right side (GND3, GND4) of the board remain below 50VAC (see <u>Figure 3.1</u>).

Table 2.1 Truth Table - Receiving

Receiving				
Inputs			Output	
RE	DE	A-B	R	
0	0	≥0.2V	1	
0	0	≤-0.2V	0	
0	0	Inputs Open/Shorted	1	
1	1	X	High-Z	
1	0	X	High-Z	

Table 2.2 Truth Table - Transmitting

Transmitting					
Inputs		Outputs			
RE	DE	D	ISODE	В	Α
Х	1	1	1	0	1
Х	1	0	1	1	0
0	0	Х	0	High-Z	High-Z
1	0	Х	0	High-Z	High-Z

3. PCB Layout Guidelines

Both supplies, V_{DD1} and V_{DD2} , must be bypassed with 100nF ceramic capacitors. These should be placed as close as possible to the supply pins for proper operation.

3.1 ISL32704EVAL2Z Evaluation Board



Figure 3.1 Top Side

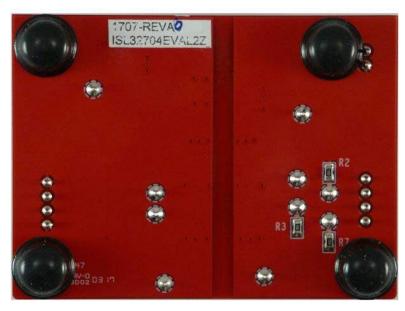


Figure 3.2 Bottom Side

3.2 ISL32704EVAL2Z Circuit Schematic

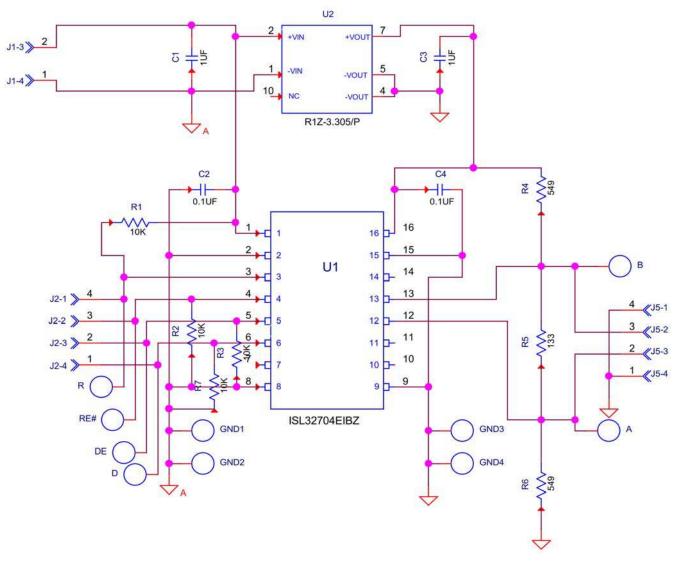
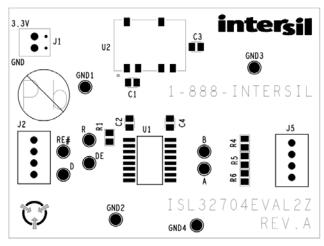


Figure 3.3 ISL32704EVAL2Z Schematic

3.3 Bill of Materials

Manufacturer Part	Qty	Reference Designator	Description	Manufacturer
ISL32704EVAL2ZREVAPCB	1		PWB-PCB, ISL32704EVAL2Z, REVA, ROHS	IMAGINEERING INC
ISL32704EIBZ	1	U1	ISOLATED, 4Mbps, HALF-DUPLEX RS-485 TRANSCEIVER, ROHS	Intersil
R1Z-3.305/P	1	U2	ISOLATED 3.3V-TO-5V DC/DC CONVERTER WITH REGULATED OUTPUT, ROHS	RECOM-POWER
CR0805-8W-1002FT (Pb-Free)	4	R1 - R3, R7	RES, SMD, 0805, 10k, 1/8W, 1%, TF, ROHS	VENKEL
RK73H2AT1330F	1	R5	RES, SMD, 0805, 133Ω, 1/8W, 1%, TF, ROHS	KOA
RC0805FR-07549RL	2	R4, R6	RES, SMD, 0805, 549Ω, 1/8W, 1%, TF, ROHS	YAGEO
C0805X5R160-106KNE	2	C1, C3	CAP, SMD, 0805, 10µF, 16V, 10%, X5R, ROHS	VENKEL
ECJ-2VB1E104K	2	C2, C4	CAP, SMD, 0805, 0.1µF, 25V, 10%, X7R, ROHS	PANASONIC
1725656	1	J1	CONN-TERMINAL BLK, TH, 2P, 6A, 125V, 20-30AWG, 2.54mm, ROHS	PHOENIX CONTACT
1725672	2	J2, J5	CONN-TERMINAL BLOCK, 4POS, 0.1, 26-20AWG, 63V, 6A, ROHS	PHOENIX CONTACT
5001	4	GND1 - GND4	CONN-MINI TEST PT, VERTICAL, BLK, ROHS	KEYSTONE
5003	6	A, B, D, R, DE, RE#	CONN-MINI TEST POINT, VERTICAL, ORG, ROHS	KEYSTONE
S-2261	1	Place assy in bag	BAG, STATIC, 4x6, ZIPLOC, ROHS	ULINE
LABEL-DATE CODE	1	AFFIX TO BACK OF PCB	LABEL-DATE CODE_LINE 1: YRWK/REV#, LINE 2: BOM NAME	INTERSIL

3.4 Board Layout



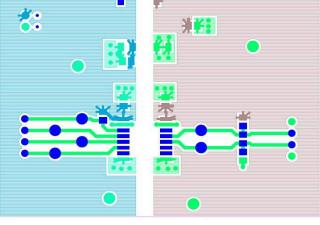
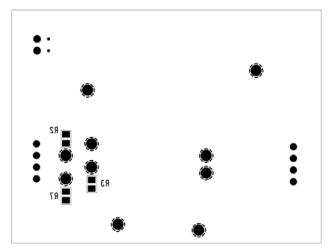


Figure 3.4 Silkscreen Top

Figure 3.5 Top Layer



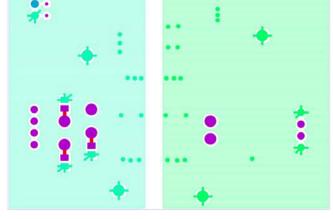


Figure 3.6 Silkscreen Bottom

Figure 3.7 Bottom Layer

4. Typical Performance Curves

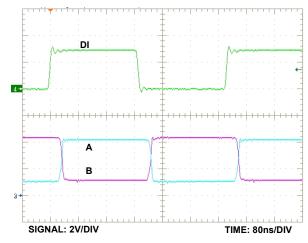


Figure 4.1 Transmit Mode

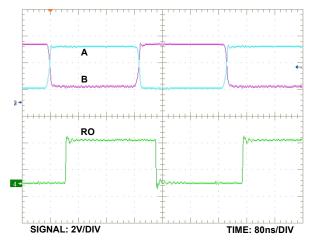


Figure 4.2 Receive Mode

5. Revision History ISL32704EVAL2Z

Revision History 5.

		Description		
Rev.	Date	Page	Summary	
0.00	Apr 3, 2017	_	Initial release	

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(Rev.4.0-1 November 2017)



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Renesas Electronics America Inc. 1001 Murphy Ranch Road, Milpitas, CA 95035, U.S.A. Tel: +1-408-432-8888, Fax: +1-408-434-5351

Renesas Electronics Canada Limited 9251 Yonge Street, Suite 8309 Richmond Hill, Ontario Canada L4C 9T3 Tel: +1-905-237-2004

Renesas Electronics Europe Limited Dukes Meadow, Milliboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K Tei: +44-1628-651-700, Fax: +44-1628-651-804

Renesas Electronics Europe GmbH

Arcadiastrasse 10, 40472 Düsseldorf, German Tel: +49-211-6503-0, Fax: +49-211-6503-1327

Renesas Electronics (China) Co., Ltd.
Room 1709 Quantum Plaza, No.27 ZhichunLu, Haidian District, Beijing, 100191 P. R. China Tel: +86-10-8235-1155, Fax: +86-10-8235-7679

Renesas Electronics (Shanghai) Co., Ltd.
Unit 301, Tower A, Central Towers, 555 Langao Road, Putuo District, Shanghai, 200333 P. R. China Tel: +86-21-2226-0898, Fax: +86-21-2226-0999

Renesas Electronics Hong Kong Limited Unit 1601-1611, 16/F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong Tel: +852-2265-6688, Fax: +852 2886-9022

Renesas Electronics Taiwan Co., Ltd. 13F, No. 363, Fu Shing North Road, Taipei 10543, Taiwan Tel: +886-2-8175-9600, Fax: +886 2-8175-9670

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80 Bendemeer Road, Unit #06-02 Hyflux Innovation Centre, Singapore 339949 Tel: +65-6213-0200, Fax: +65-6213-0300

Renesas Electronics Malaysia Sdn.Bhd. Unit 1207, Block B, Menara Amcorp, Amco Amcorp Trade Centre, No. 18, Jin Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia

Unit 1207, Block B, Menara Amcorp, Amcorp Tel: +60-3-7955-9390, Fax: +60-3-7955-9510

Renesas Electronics India Pvt. Ltd. No.777C, 100 Feet Road, HAL 2nd Stage, Indiranagar, Bangalore 560 038, India Tel: +91-80-67208700, Fax: +91-80-67208777

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ISL32704EVAL2Z User's Manual: Hardware

Publication Date: Rev.0.00 Apr 3, 2017

Published by: Renesas

ISL32704EVAL2Z

