



74AHC1G08Q

SINGLE 2-INPUT POSITIVE AND GATE

Description

The 74AHC1G08Q is an automotive-compliant single, two-input positive AND gate with a standard push-pull output. The device is designed for operation with a power supply range of 2.0V to 5.5V. The gate performs the positive Boolean function:

$$Y = A \bullet B \text{ or } Y = \overline{\overline{A} + \overline{B}}$$

Features

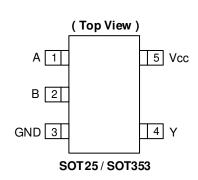
- Grade 1 Ambient Temperature Operation: -40°C to +125°C
- Supply Voltage Range from 2.0V to 5.5V
- ±8mA Output Drive at 4.5V
- CMOS Low-Power Consumption
- Schmitt Trigger Action at All Inputs Make the Circuit Tolerant for Slower Input Rise and Fall Time
- Inputs not Limited by Vcc
- Balanced Propagation Delays
- Balanced Drive Capability
- ESD Protection Tested per AEC-Q100
- Exceeds 2000-V Human Body Model (AEC-Q100-002)
- Exceeds 1000-V Charged Device Model (AEC-Q100-011)
- Latch-Up Exceeds 100mA (AEC-Q100-004)
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The 74AHC1G08Q is suitable for automotive applications requiring specific change control; this part is AEC-Q100 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 - 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 - 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

Pin Assignments



Applications

- General Purpose Logic
- Wide Array of Products, such as:
 - Automotive Applications within Grade 1 Temperature Range
 - Industrial Computing/Controls/Automation
 - High Reliability Networking/Communications
 - Industrial/Agricultural Equipment



Pin Descriptions

Pin Name	Description			
А	Data Input			
В	Data Input			
GND	Ground			
Y	Data Output			
V _{CC}	Supply Voltage			

Function Table

Inp	Output	
Α	В	Y
н	Н	Н
L	Х	L
х	L	L

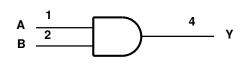
Absolute Maximum Ratings (Notes 4 & 5)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
Vcc	Supply Voltage Range	-0.5 to 6.5	V
VI	Input Voltage Range	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High or Low State	-0.5 to V _{CC} + 0.5	V
Ік	Input Clamp Current VI < 0	-20	mA
loк	Output Clamp Current (V _O < 0 or V _O > V _{CC})	±20	mA
lo	Continuous Output Current (Vo = 0 to Vcc)	±25	mA
lcc	Continuous Current Through Vcc	75	mA
Ignd	Continuous Current Through GND	-75	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T _{STG}	Storage Temperature	-65 to +150	°C
PD	Total Power Dissipation (Note 6)	250	mW

Notes: 4. Stresses beyond the absolute maximum can result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

Forcing the maximum allowed voltage could cause a condition exceeding the maximum current or conversely forcing the maximum current could cause a condition exceeding the maximum voltage. The ratings of both current and voltage must be maintained within the controlled range.
 This will need to be derated at higher operating temperatures to prevent exceeding maximum T_J. Refer to package thermal characteristics section.

Logic Diagram





Recommended Operating Conditions (Note 7)

Symbol		Parameter	Min	Max	Unit	
Vcc	Operating Voltage	_	2	5.5	V	
	Vcc = 2V		1.5	_		
VIH	High-Level Input Voltage	$V_{CC} = 3V$	2.1	_	V	
		Vcc = 5.5V	3.85	_		
		$V_{CC} = 2V$	—	0.5		
VIL	Low-Level Input Voltage	$V_{CC} = 3V$	—	0.9	V	
		Vcc = 5.5V	—	1.65		
VI	Input Voltage		0	5.5	V	
Vo	Output Voltage		0	Vcc	V	
		$V_{CC} = 2V$	—	-50	μA	
Іон	High-Level Output Current	$V_{CC} = 3.3V \pm 0.3V$	—	-4		
		$V_{CC} = 5V \pm 0.5V$	_	-8	mA	
		V _{CC} = 2V	—	50	μA	
Iol	Low-Level Output Current	$V_{CC} = 3.3V \pm 0.3V$	—	4		
		$V_{CC} = 5V \pm 0.5V$	—	8	mA	
	Input Transition Rise or Fall	$V_{CC} = 3.3V \pm 0.3V$	—	100		
Δt/ΔV	Rate	$V_{CC} = 5V \pm 0.5V$		20	ns/V	
TA	Ambient Temperature	_	-40	+125	°C	

Note: 7. Unused inputs should be held at V_{CC} or Ground.

Electrical Characteristics (All typical values are at V_{CC} = 3.3V, T_A = +25°C.)

		1		1							
Symbol	Parameter	Test Conditions	Vcc		+25°C		-40°C to	o +85°C	-40°C to	+125°C	Unit
Symbol	Farameter 1	Test Conditions	VCC	Min	Тур	Max	Min	Мах	Min	Мах	Unit
			2V	1.9	2	—	1.9	—	1.9	_	
			3V	2.9	3		2.9	_	2.9	_	
	Link Laval	I _{OH} = -50µА	4.5V	4.4	4.5		4.4	_	4.4	_	
Vон	High Level Output Voltage	VI = VIH or VIL IOH = -4mA	3V	2.58	_	_	2.48	—	2.40	—	V
		Vı = Vıн or Vı∟ Iон = -8mA	4.5V	3.94	_	_	3.80	_	3.70	_	
			2V	_	_	0.1	—	0.1	_	0.1	
			3V		_	0.1	_	0.1		0.1	
		Ι _{ΟL} = 50μΑ	4.5V		_	0.1	—	0.1	_	0.1	
Vol	Low Level Output Voltage	VI = VIH or VIL IOL = 4mA	3V	_	_	0.36	_	0.44	_	0.55	V
		VI = VIH or VIL IOL = 8mA	4.5V	_	_	0.36	_	0.44	_	0.55	
h	Input Current	$V_1 = 5.5V$ or GND	0 to 5.5V	_		±0.1	—	±1	—	±2	μA
lcc	Supply Current	VI = 5.5V or GND Io = 0	5.5V	_	_	1	_	10	_	40	μΑ
Cı	Input Capacitance	VI = VCC or GND	5.5V	_	1.5	10	_	10	_	10	pF



Package Characteristics

Symbol	Parameter	Package	Test Conditions	Min	Тур	Max	Unit
0	Thermal Resistance	SOT25	Note 0		184	_	0 0 MI
ΑLθ	Junction-to-Ambient	SOT353	Note 8	-	385	_	°C/W
0	Thermal Resistance	SOT25	Nista O	_	62	_	00M
θις	Junction-to-Case	SOT353	Note 8	_	164	_	°C/W

Note: 8. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

Switching Characteristics

Vcc = 3.3V ± 0.3V (See Figure 1)

Parameter	From	То	Test		+25°C		-40°C t	o +85°C	-40°C to	+125°C	Unit
	(Input)	(Input) (Output)	Conditions	Min	Тур	Max	Min	Мах	Min	Мах	
	A av D	V	$C_L = 15 pF$	1.0	4.6	8.8	1.0	10.5	1.0	12.0	ns
tpd	A or B	Y	CL = 50pF	1.0	6.5	12.3	1.0	14.0	1.0	16.0	ns

Vcc = 5V ± 0.5V (See Figure 1)

Parameter	From	То	Test		+25°C		-40°C t	o +85°C	-40°C to	+125°C	Unit
	(Input)	(Output)	Conditions	Min	Тур	Max	Min	Max	Min	Max	
4	A	V	C∟ = 15pF	1.0	3.2	5.9	1.0	7.0	1.0	8.0	ns
tpd	A or B	Y	CL = 50pF	1.0	4.6	7.9	1.0	9.0	1.0	10.5	ns

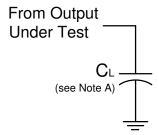
Operating Characteristics

 $T_A = +25^{\circ}C$

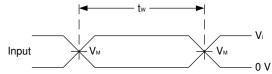
	Parameter	Test Conditions	V _{CC} = 5V Typ	Unit
Cpd	Power Dissipation Capacitance	f = 1MHz No Load	10	pF



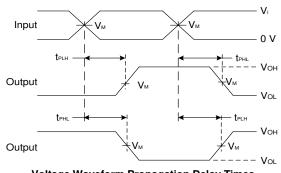
Measurement Information



Vcc	Ir	iputs	VM	CL
100	VI	t _R /t _F	V IVI	UL
3.3V±0.3V	Vcc	≤3ns	V _{CC} /2	15pF
5V±0.5V	V _{CC}	≤3ns	V _{CC} /2	15pF
3.3V±0.3V	V _{CC}	≤3ns	V _{CC} /2	50pF
5V±0.5V	V _{CC}	≤3ns	V _{CC} /2	50pF







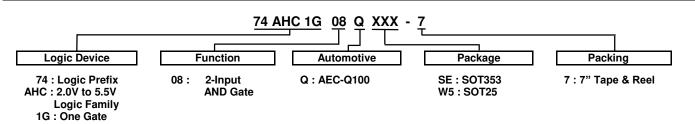
Voltage Waveform Propagation Delay Times Inverting and Non-Inverting Outputs



- Notes:
- A. Includes test lead and test apparatus capacitance.
 B. All pulses are supplied at pulse repetition rate ≤ 1MHz.
 C. Inputs are measured separately one transition per measurement.
- D. t_{PLH} and t_{PHL} are the same as t_{PD} .



Ordering Information (Note 9)

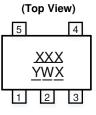


Part Number	Package	Package	Package Size	7" Таре	and Reel
Farthumber	Code	(Notes 10 & 11)	Fackage Size	Quantity	Part Number Suffix
74AHC1G08QSE-7	SE	SOT353	2.15mm × 2.1mm × 1.1mm 0.65mm lead pitch	3000/Tape & Reel	-7
74AHC1G08QW5-7	W5	SOT25	3.0mm × 2.8 mm × 1.2 mm 0.95mm lead pitch	3000/Tape & Reel	-7

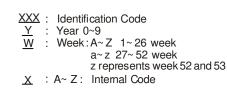
Notes:

For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.
 Pad layout as shown in Diodes Incorporated's package outline PDFs, which can be found on our website at http://www.diodes.com/package-outlines.html.
 The taping orientation is located on our website at https://www.diodes.com/assets/Packaging-Support-Docs/ap02007.pdf.

Marking Information



SOT 25 / SOT 353



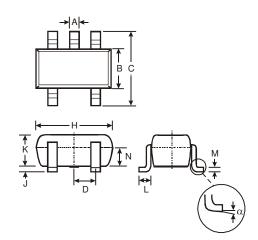
Part Number	Package	Identification Code
74AHC1G08QW5-7	SOT25	YUQ
74AHC1G08QSE-7	SOT353	YUQ



Package Outline Dimensions

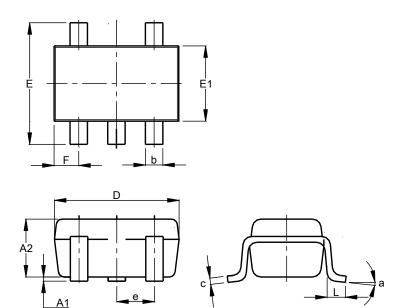
Please see http://www.diodes.com/package-outlines.html for the latest version.

(1) Package Type: SOT25



SOT25					
Dim	Min	Max	Тур		
Α	0.35	0.50	0.38		
В	1.50	1.70	1.60		
С	2.70	3.00	2.80		
D	-	-	0.95		
Н	2.90	3.10	3.00		
J	0.013	0.10	0.05		
К	1.00	1.30	1.10		
L	0.35	0.55	0.40		
М	0.10	0.20	0.15		
Ν	0.70	0.80	0.75		
α	0°	8°	-		
All Dimensions in mm					

(2) Package Type: SOT353



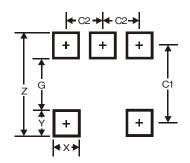
	SOT353				
Dim	Min	Max	Тур		
A1	0.00	0.10	0.05		
A2	0.90	1.00	0.95		
b	0.10	0.30	0.25		
С	0.10	0.22	0.11		
D	1.80	2.20	2.15		
Е	2.00	2.20	2.10		
E1	1.15	1.35	1.30		
е	0.650 BSC				
F	0.40	0.45	0.425		
L	0.25	0.40	0.30		
а	0°	8°			
All Dimensions in mm					



Suggested Pad Layout

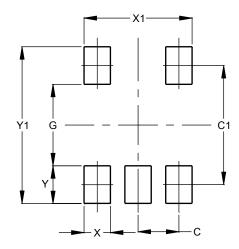
Please see http://www.diodes.com/package-outlines.html for the latest version.

(1) Package Type: SOT25



Dimensions	Value
Z	3.20
G	1.60
Х	0.55
Y	0.80
C1	2.40
C2	0.95

(2) Package Type: SOT353



Dimensions	Value (in mm)
С	0.650
C1	1.900
G	1.300
Х	0.420
X1	1.720
Ŷ	0.600
Y1	2.500

Mechanical Data

SOT25

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 3
- Weight: 0.0158 grams (Approximate)

SOT353

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 3
- Weight: 0.0064 grams (Approximate)



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