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Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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Phase-out/Discontinued

L, S-BAND SPDT SWITCH

DESCRIPTION

The μPG2008TK is a GaAs MMIC for L, S-band SPDT (Single Pole Double Throw) switch which were developed for mobile phone and another L, S-band application.

This device can operate frequency from 0.5 to 2.5 GHz, having the low insertion loss and high isolation.

This device is housed in a 6-pin lead-less minimold package (1511). And this package is able to high-density surface mounting.

FEATURES

- Switch control voltage : $V_{cont(H)} = 2.5$ to 5.3 V (2.8 V TYP.)
: $V_{cont(L)} = -0.2$ to $+0.2$ V (0 V TYP.)
- Low insertion loss : $LINS1 = 0.40$ dB TYP. @ $f = 0.5$ to 1.0 GHz, $V_{cont} = 2.8$ V/0 V
: $LINS2 = 0.55$ dB TYP. @ $f = 2.0$ GHz, $V_{cont} = 2.8$ V/0 V
: $LINS3 = 0.60$ dB TYP. @ $f = 2.5$ GHz, $V_{cont} = 2.8$ V/0 V
- High isolation : $ISL1 = 25$ dB TYP. @ $f = 0.5$ to 2.0 GHz, $V_{cont} = 2.8$ V/0 V
: $ISL2 = 25$ dB TYP. @ $f = 2.5$ GHz, $V_{cont} = 2.8$ V/0 V
- High-density surface mounting : 6-pin lead-less minimold package ($1.5 \times 1.1 \times 0.55$ mm)

APPLICATIONS

- L-band digital cellular or cordless telephone
- PCS, W-LAN, WLL and Bluetooth™ etc.

ORDERING INFORMATION

Part Number	Package	Marking	Supplying Form
μPG2008TK-E2	6-pin lead-less minimold (1511)	G2R	<ul style="list-style-type: none"> • Embossed tape 8 mm wide • Pin 1, 6 face the perforation side of the tape • Qty 5 kpcs/reel

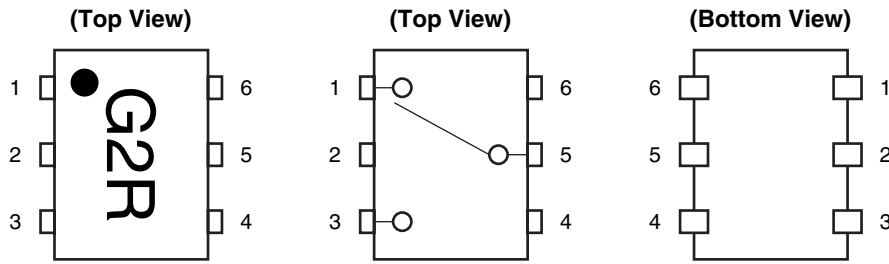
Remark To order evaluation samples, contact your nearby sales office.

Part number for sample order: μPG2008TK

Caution Observe precautions when handling because these devices are sensitive to electrostatic discharge.

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Not all devices/types available in every country. Please check with local NEC Compound Semiconductor Devices representative for availability and additional information.

★ PIN CONNECTIONS AND INTERNAL BLOCK DIAGRAM



Pin No.	Pin Name
1	OUTPUT1
2	GND
3	OUTPUT2
4	V _{cont2}
5	INPUT
6	V _{cont1}

TRUTH TABLE

V _{cont1}	V _{cont2}	INPUT-OUTPUT1	INPUT-OUTPUT2
Low	High	ON	OFF
High	Low	OFF	ON

ABSOLUTE MAXIMUM RATINGS (T_A = +25°C, unless otherwise specified)

Parameter	Symbol	Ratings	Unit
Switch Control Voltage	V _{cont}	-6.0 to +6.0 ^{Note 1}	V
Input Power	P _{in}	+28	dBm
Power Dissipation	P _D	150 ^{Note 2}	mW
Operating Ambient Temperature	T _A	-45 to +85	°C
Storage Temperature	T _{stg}	-55 to +150	°C

Notes 1. $|V_{cont1} - V_{cont2}| \leq 6.0 \text{ V}$

2. Mounted on double-sided copper-clad 50 × 50 × 1.6 mm epoxy glass PWB, T_A = +85°C

RECOMMENDED OPERATING RANGE (T_A = +25°C, unless otherwise specified)

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Switch Control Voltage (H)	V _{cont (H)}	2.5	2.8	5.3	V
Switch Control Voltage (L)	V _{cont (L)}	-0.2	0	0.2	V

ELECTRICAL CHARACTERISTICS

(T_A = +25°C, V_{cont} = 2.8 V/0 V, DC cut capacitors = 56 pF, unless otherwise specified)

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Insertion Loss1	L _{INS1}	f = 0.5 to 1.0 GHz	–	0.40	0.70	dB
Insertion Loss2	L _{INS2}	f = 2.0 GHz	–	0.55	0.80	dB
Insertion Loss3	L _{INS3}	f = 2.5 GHz	–	0.60	0.85	dB
Isolation1	ISL1	f = 0.5 to 2.0 GHz	22	25	–	dB
Isolation2	ISL2	f = 2.5 GHz	20	25	–	dB
Input Return Loss	RL _{in}	f = 0.5 to 2.5 GHz	13	20	–	dB
Output Return Loss	RL _{out}	f = 0.5 to 2.5 GHz	13	20	–	dB
1 dB Gain Compression Input Power ^{Note}	P _{in (1 dB)}	f = 2.0 GHz	+20.0	+25.0	–	dBm
Switching Control Current	I _{cont}		–	0.5	10	μA

Note P_{in (1 dB)} is measured the input power level when the insertion loss increases more 1 dB than that of linear range.

STANDARD CHARACTERISTICS FOR REFERENCE

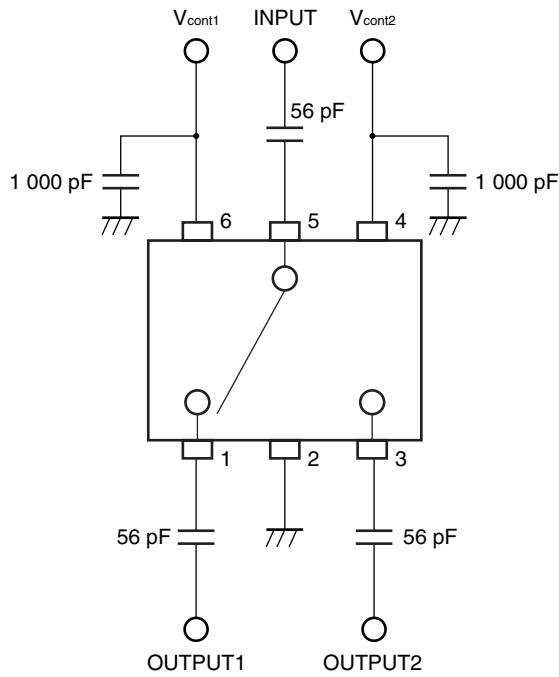
(T_A = +25°C, V_{cont} = 2.8 V/0 V, DC cut capacitors = 56 pF, unless otherwise specified)

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
0.1 dB Gain Compression Input Power ^{Note}	P _{in (0.1 dB)}	f = 2.0 GHz	–	+20.0	–	dBm
Switching Control Speed	t _{sw}		–	50	–	ns

Note P_{in (0.1 dB)} is measured the input power level when the insertion loss increases more 0.1 dB than that of linear range.

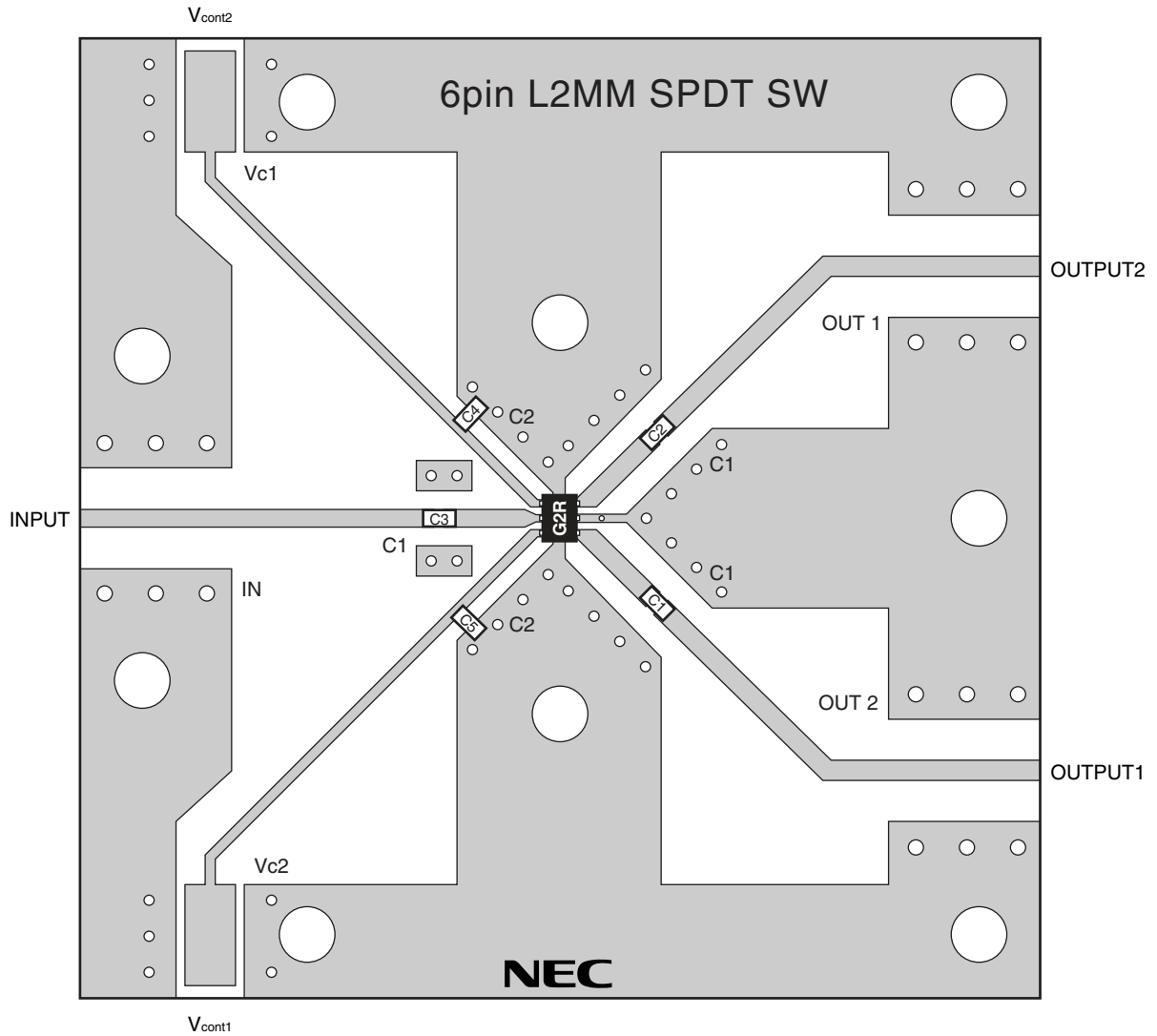
Caution This device is used it is necessary to use DC cut capacitors. The value of DC cut capacitors should be chosen to accommodate the frequency of operation, bandwidth, switching speed and the condition with actual board of your system. The range of recommended DC cut capacitor value is less than 100 pF.

EVALUATION CIRCUIT ($V_{cont} = 2.8 V/0 V$, DC cut capacitors = 56 pF)



The application circuits and their parameters are for reference only and are not intended for use in actual design-ins.

ILLUSTRATION OF THE TEST CIRCUIT ASSEMBLED ON EVALUATION BOARD



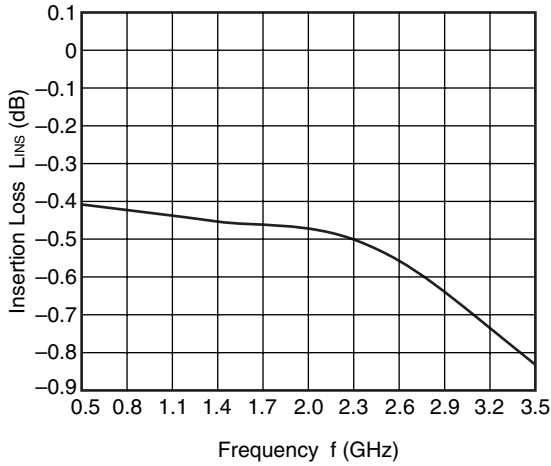
USING THE NEC EVALUATION BOARD

Symbol	Values
C1, C2, C3	56 pF
C4, C5	1 000 pF

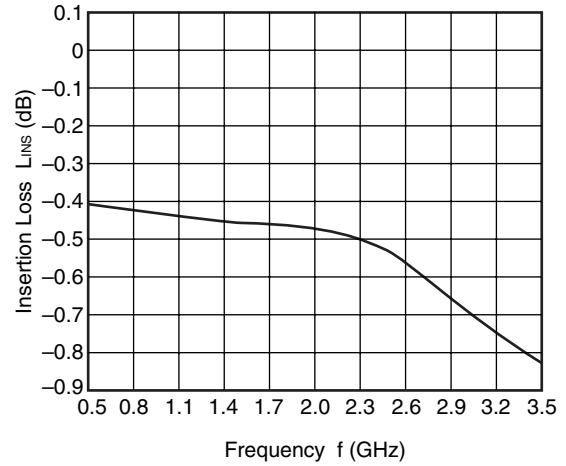
TYPICAL CHARACTERISTICS

($T_A = +25^\circ\text{C}$, $V_{\text{cont}} = 2.8 \text{ V/O V}$, DC cut capacitors = 56 pF, unless otherwise specified)

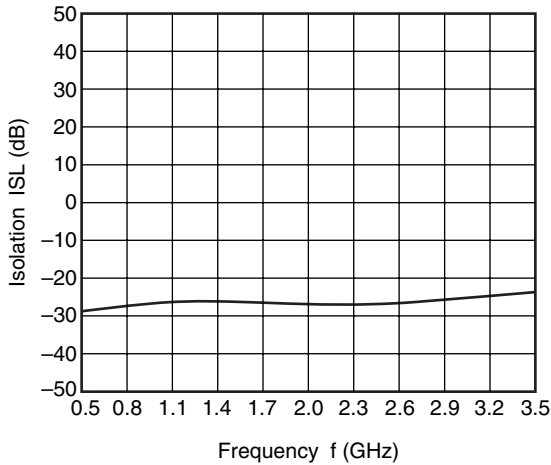
INPUT-OUTPUT1
INSERTION LOSS vs. FREQUENCY



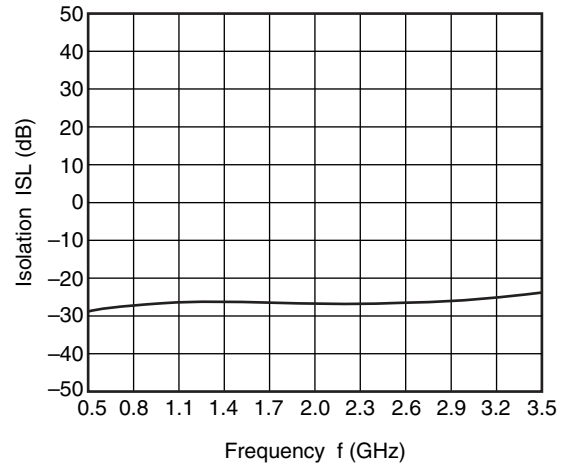
INPUT-OUTPUT2
INSERTION LOSS vs. FREQUENCY



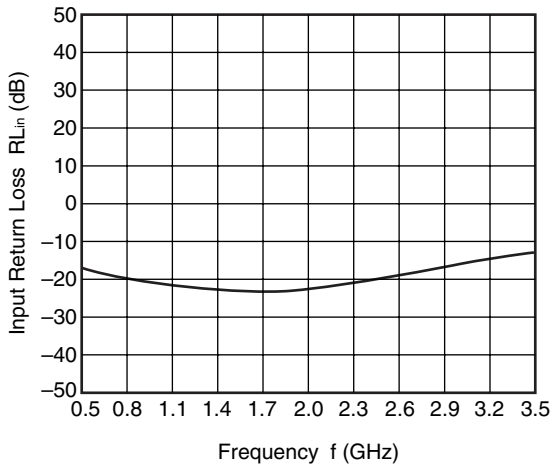
INPUT-OUTPUT1
ISOLATION vs. FREQUENCY



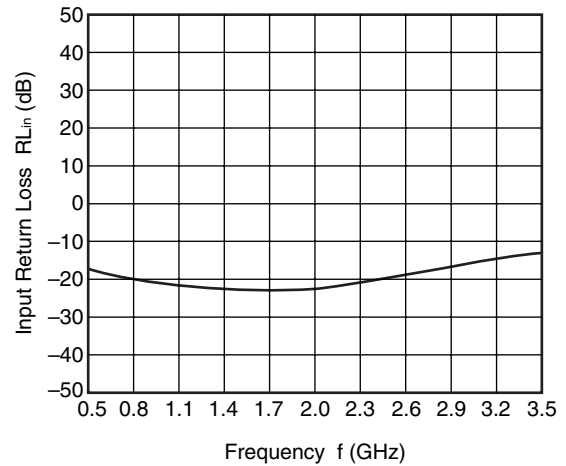
INPUT-OUTPUT2
ISOLATION vs. FREQUENCY



INPUT-OUTPUT1
INPUT RETURN LOSS vs. FREQUENCY

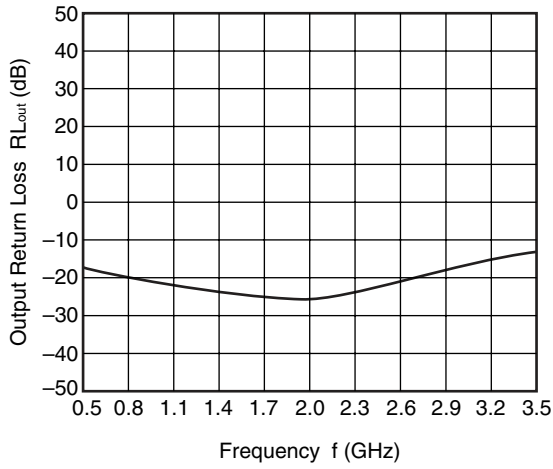


INPUT-OUTPUT2
INPUT RETURN LOSS vs. FREQUENCY

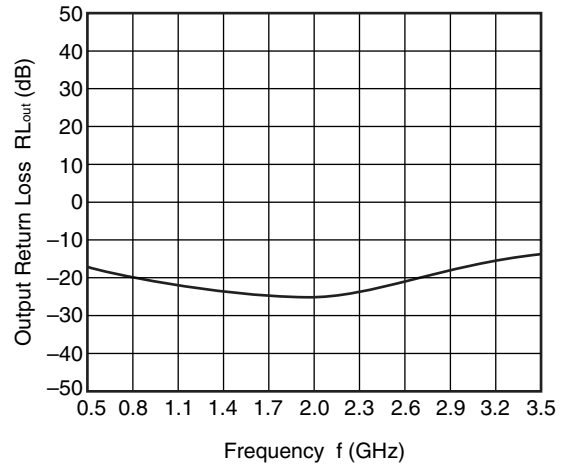


Remark The graphs indicate nominal characteristics.

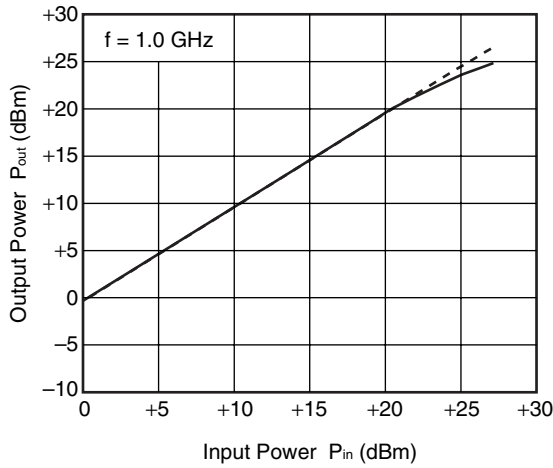
INPUT-OUTPUT1
OUTPUT RETURN LOSS vs. FREQUENCY



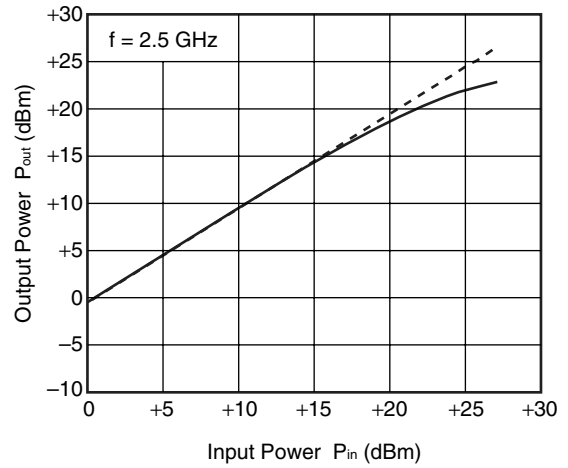
INPUT-OUTPUT2
OUTPUT RETURN LOSS vs. FREQUENCY



OUTPUT POWER vs. INPUT POWER



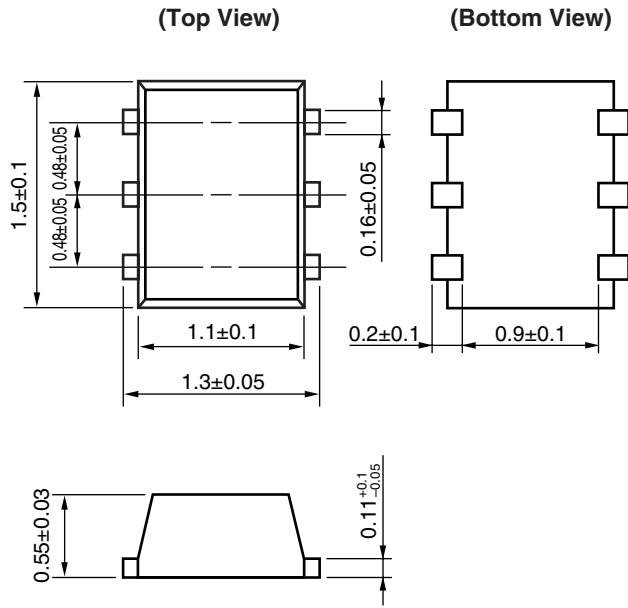
OUTPUT POWER vs. INPUT POWER



Remark The graphs indicate nominal characteristics.

★ PACKAGE DIMENSIONS

6-PIN LEAD-LESS MINIMOLD (1511) (UNIT: mm)



RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions	Condition Symbol
Infrared Reflow	Peak temperature (package surface temperature) : 260°C or below Time at peak temperature : 10 seconds or less Time at temperature of 220°C or higher : 60 seconds or less Preheating time at 120 to 180°C : 120±30 seconds Maximum number of reflow processes : 3 times Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below	IR260
VPS	Peak temperature (package surface temperature) : 215°C or below Time at temperature of 200°C or higher : 25 to 40 seconds Preheating time at 120 to 150°C : 30 to 60 seconds Maximum number of reflow processes : 3 times Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below	VP215
Wave Soldering	Peak temperature (molten solder temperature) : 260°C or below Time at peak temperature : 10 seconds or less Preheating temperature (package surface temperature) : 120°C or below Maximum number of flow processes : 1 time Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below	WS260
Partial Heating	Peak temperature (pin temperature) : 350°C or below Soldering time (per side of device) : 3 seconds or less Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below	HS350

Caution Do not use different soldering methods together (except for partial heating).

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"Special": Transportation equipment (automobiles, trains, ships, etc.), traffic control systems, anti-disaster systems, anti-crime systems, safety equipment and medical equipment (not specifically designed for life support)
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M8E 00.4-0110

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► For further information, please contact

NEC Compound Semiconductor Devices, Ltd. <http://www.ncsd.necel.com/>

E-mail: salesinfo@ml.ncsd.necel.com (sales and general)

techinfo@ml.ncsd.necel.com (technical)

Sales Division TEL: +81-44-435-1588 FAX: +81-44-435-1579

NEC Compound Semiconductor Devices Hong Kong Limited

E-mail: ncsd-hk@elhk.nec.com.hk (sales, technical and general)

Hong Kong Head Office TEL: +852-3107-7303 FAX: +852-3107-7309

Taipei Branch Office TEL: +886-2-8712-0478 FAX: +886-2-2545-3859

Korea Branch Office TEL: +82-2-558-2120 FAX: +82-2-558-5209

NEC Electronics (Europe) GmbH <http://www.ee.nec.de/>

TEL: +49-211-6503-0 FAX: +49-211-6503-1327

California Eastern Laboratories, Inc. <http://www.cel.com/>

TEL: +1-408-988-3500 FAX: +1-408-988-0279