

PC715V0NSZX Series

DIP 6 pin Darlington Phototransistor Output Photocoupler



■ Description

PC715V0NSZX Series contains an IRED optically coupled to a phototransistor.

It is packaged in a 6 pin DIP.

Input-output isolation voltage(rms) is 5.0kV.

CTR is MIN. 600% at input current of 1mA.

■ Features

- 1. 6 pin DIP package
- Double transfer mold package (Ideal for Flow Soldering)
- 3. Darlington phototransistor output (CTR : MIN. 600% at $I_F=1mA$, $V_{CE}=2V$)
- 4. High isolation voltage between input and output (V_{iso(rms)}: 5.0kV)

■ Agency approvals/Compliance

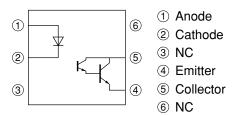
- 1. Recognized by UL1577 (Double protection isolation), file No. E64380 (as model No. **PC715V**)
- 2. Approved by TÜV (VDE0884) (as an option) file No. R-9151576 (as model No. **PC715V**)
- 3. Package resin: UL flammability grade (94V-0)

■ Applications

- 1. Home appliances
- 2. Programmable controllers
- 3. Personal computer peripherals

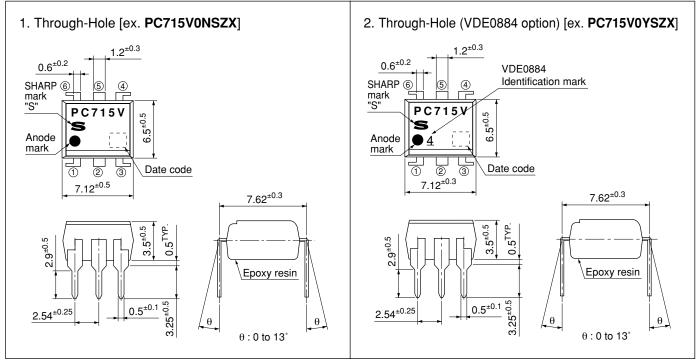


■ Internal Connection Diagram



■ Outline Dimensions

(Unit: mm)



Product mass: approx. 0.36g



Date code (2 digit)

1st digit				2nd digit		
	Year of production			Month of production		
A.D.	Mark	A.D	Mark	Month	Mark	
1990	A	2002	P	January	1	
1991	В	2003	R	February	2	
1992	С	2004	S	March	3	
1993	D	2005	T	April	4	
1994	Е	2006	U	May	5	
1995	F	2007	V	June	6	
1996	Н	2008	W	July	7	
1997	J	2009	X	August	8	
1998	K	2010	A	September	9	
1999	L	2011	В	October	0	
2000	M	2012	С	November	N	
2001	N	:	:	December	D	

repeats in a 20 year cycle

Country of origin Japan



■ Absolute Maximum Ratings

Absolute Maximum Hattings $(T_a=25^{\circ}C)$						
	Parameter	Symbol	Rating	Unit		
	Forward current	I_F	50	mA		
Input	*1 Peak forward current	I_{FM}	1	A		
	Reverse voltage	V_R	6	V		
	Power dissipation	P	70	mW		
Output	Collector-emitter voltage	V_{CEO}	35	V		
	Emitter-collector voltage	V _{ECO}	6	V		
	Collector current	I_C	80	mA		
	Collector power dissipation	P_{C}	150	mW		
Total power dissipation		P_{tot}	170	mW		
Operating temperature		Topr	-25 to +100	°C		
Storage temperature		T _{stg}	-40 to +125	°C		
*2 Isolation voltage		V _{iso (rms)}	5	kV		
*3 Soldering temperature		T_{sol}	260	°C		

^{*1} Pulse width≤100µs, Duty ratio: 0.001 *2 40 to 60%RH, AC for 1minute, f=60Hz *3 For 10s

■ Electro-optical Characteristics

 $(T_0=25^{\circ}C)$

								$(1_a=25 \text{ C})$
Parameter		Symbol	Conditions	MIN.	TYP.	MAX.	Unit	
Input	Forward voltage		V_F	$I_F=10mA$	_	1.2	1.4	V
	Peak forward voltage		V_{FM}	$I_{FM}=0.5V$	_	_	3.0	V
	Reverse current		I_R	$V_R=4V$	_	_	10	μΑ
	Terminal capacitance		C_t	V=0, $f=1kHz$	_	30	250	pF
	Collector dark current		I_{CEO}	$V_{CE}=10V, I_{F}=0$	_	_	1 000	nA
Output	Collector-emitter breakdown voltage		BV_{CEO}	I_{C} =0.1mA, I_{F} =0	35	_	_	V
	Emitter-collector breakdown voltage		$\mathrm{BV}_{\mathrm{ECO}}$	$I_{E}=10\mu A, I_{F}=0$	6	_	_	V
Transfer charac- teristics	Current transfer ratio		I_{C}	$I_F=1mA$, $V_{CE}=2V$	6.0	16.0	75.0	mA
	Collector-emitter saturation voltage		V _{CE (sat)}	$I_F=20mA$, $I_C=5mA$	_	_	1.0	V
	Isolation resistance		R _{ISO}	DC500V, 40 to 60%RH	5×10 ¹⁰	1×10 ¹¹	-	Ω
	Floating capacitance		C_{f}	V=0, f=1MHz	_	0.6	1.0	pF
	Cut-off frequency		f_C	V_{CE} =2V, I_{C} =2mA, R_{L} =100 Ω -3dB	_	6	-	kHz
	Response time	Rise time	t_r	V 2V I 10m A B 1000	_	60	250	μs
		Fall time	t_{f}	$V_{CE}=2V$, $I_{C}=10mA$, $R_{L}=100\Omega$	_	53	250	μs



■ Model Line-up

Lead Form	Through-Hole		
Doolroos	Sleeve		
Package	50pcs/sleeve		
VDE0884		Approved	
Model No.	PC715V0NSZX	PC715V0YSZX	

Please contact a local SHARP sales representative to inquire about production status and Lead-Free options.



Fig.1 Forward Current vs. Ambient Temperature

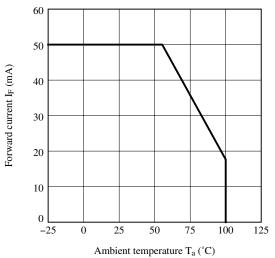


Fig.3 Collector Power Dissipation vs. Ambient Temperature

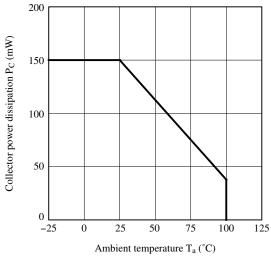


Fig.5 Peak Forward Current vs. Duty Ratio

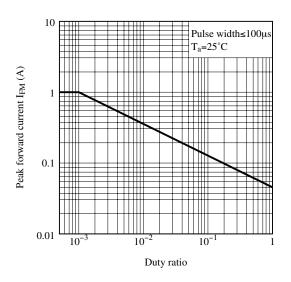


Fig.2 Diode Power Dissipation vs. Ambient Temperature

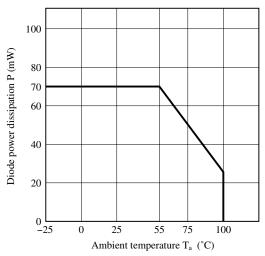


Fig.4 Total Power Dissipation vs. Ambient Temperature

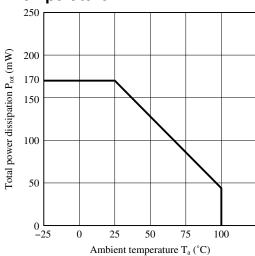


Fig.6 Forward Current vs. Forward Voltage

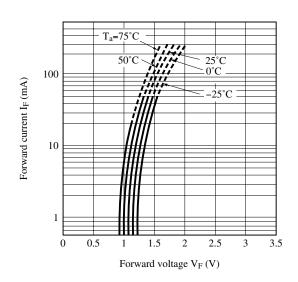




Fig.7 Current Transfer Ratio vs. Forward Current

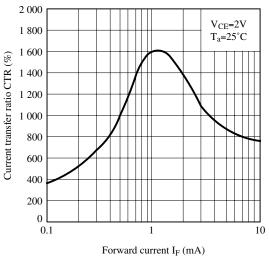


Fig.9 Collector Current vs. Collectoremitter Voltage

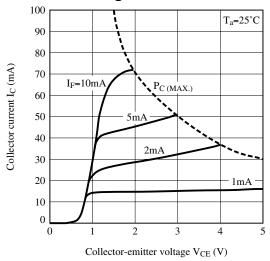


Fig.11 Collector - emitter Saturation Voltage vs. Ambient Temperature

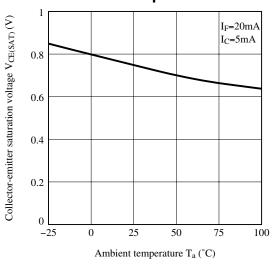


Fig.8 Collector Current vs. Collectoremitter Voltage

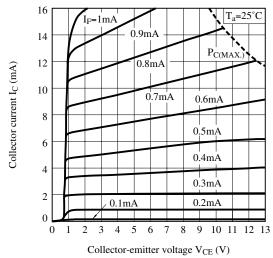


Fig.10 Relative Current Transfer Ratio vs.
Ambient Temperature

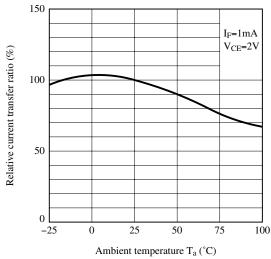


Fig.12 Collector Dark Current vs. Ambient Temperature

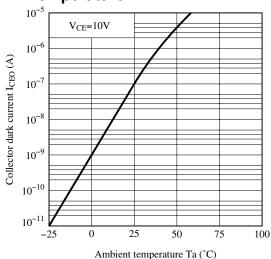
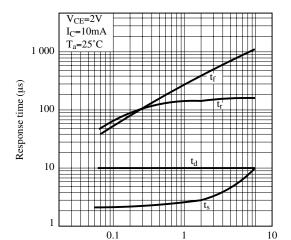




Fig.13 Response Time vs. Load Resistance



Load resistance $R_L(k\Omega)$

Fig.15 Frequency Response

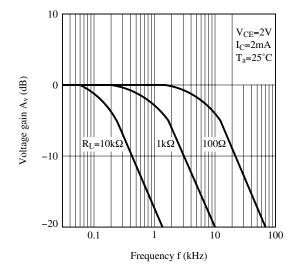
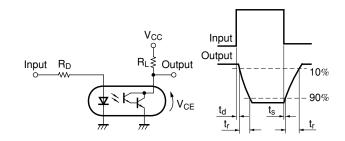
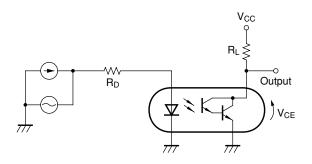


Fig.14 Test Circuit for Response Time



Please refer to the conditions in Fig.13

Fig.16 Test Circuit for Frequency Response



Please refer to the conditions in Fig.15

Remarks: Please be aware that all data in the graph are just for reference and not for guarantee.



■ Design Considerations

Design guide

While operating at I_F<1.0mA, CTR variation may increase.

Please make design considering this fact.

This product is not designed against irradiation and incorporates non-coherent IRED.

Degradation

In general, the emission of the IRED used in photocouplers will degrade over time.

In the case of long term operation, please take the general IRED degradation (50% degradation over 5years) into the design consideration.

[☆] For additional design assistance, please review our corresponding Optoelectronic Application Notes.



■ Manufacturing Guidelines

Soldering Method

Flow Soldering:

Due to SHARP's double transfer mold construction submersion in flow solder bath is allowed under the below listed guidelines.

Flow soldering should be completed below 270°C and within 10s.

Preheating is within the bounds of 100 to 150°C and 30 to 80s.

Please don't solder more than twice.

Hand soldering

Hand soldering should be completed within 3s when the point of solder iron is below 400°C.

Please don't solder more than twice.

Other notices

Please test the soldering method in actual condition and make sure the soldering works fine, since the impact on the junction between the device and PCB varies depending on the tooling and soldering conditions.



Cleaning instructions

Solvent cleaning:

Solvent temperature should be 45°C or below Immersion time should be 3minutes or less

Ultrasonic cleaning:

The impact on the device varies depending on the size of the cleaning bath, ultrasonic output, cleaning time, size of PCB and mounting method of the device.

Therefore, please make sure the device withstands the ultrasonic cleaning in actual conditions in advance of mass production.

Recommended solvent materials:

Ethyl alcohol, Methyl alcohol and Isopropyl alcohol

In case the other type of solvent materials are intended to be used, please make sure they work fine in actual using conditions since some materials may erode the packaging resin.

Presence of ODC

This product shall not contain the following materials.

And they are not used in the production process for this device.

Regulation substances: CFCs, Halon, Carbon tetrachloride, 1.1.1-Trichloroethane (Methylchloroform)

Specific brominated flame retardants such as the PBBOs and PBBs are not used in this product at all.



■ Package specification

Sleeve package

Package materials

Sleeve: HIPS (with anti-static material)

Stopper: Styrene-Elastomer

Package method

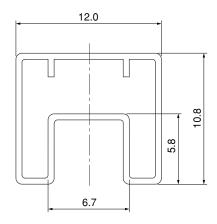
MAX. 50 pcs. of products shall be packaged in a sleeve.

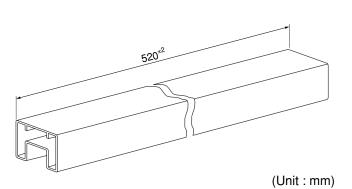
Both ends shall be closed by tabbed and tabless stoppers.

The product shall be arranged in the sleeve with its anode mark on the tabless stopper side.

MAX. 20 sleeves in one case.

Sleeve outline dimensions







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 - --- Office automation equipment
 - --- Telecommunication equipment [terminal]
 - --- Test and measurement equipment
 - --- Industrial control
 - --- Audio visual equipment
 - --- Consumer electronics
- (ii) Measures such as fail-safe function and redundant design should be taken to ensure reliability and safety when SHARP devices are used for or in connection

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- --- Traffic signals
- --- Gas leakage sensor breakers
- --- Alarm equipment
- --- Various safety devices, etc.
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