

<IGBT Modules>

# CM150RX-12A

HIGH POWER SWITCHING USE INSULATED TYPE

INSULATED TYPE	
Collector current I <sub>c</sub>	. 150A
Collector current I <sub>c</sub> Collector current I <sub>c</sub> Collector-emitter voltage V <sub>CES</sub> Maximum junction temperature T <sub>jmax</sub> •Flat base Type •Copper base plate (non-plating) •RoHS Directive compliant	
Maximum junction temperature T <sub>jmax</sub>	150°C
•Flat base Type	
•Copper base plate (non-plating)	
RoHS Directive compliant	
•Recognized under UL1557, File E323585	
sevenpack (3ø Inverter + Brake Chopper)	
APPLICATION	
AC Motor Control, Motion/Servo Control, etc.	
	Dimension in mm
	TERMINAL (3.81)
	1.15
	0.65
(7.75)- (7.75)- * 15- * 15- * 15- * 16- * 28- * 49.28 * 49.28 * 49.28 * 49.28 * 49.28 * 49.28 * 49.28 * 49.25 * 49.28 * 114.06	7.4)
110 ±0.5 99 4-¢5.5 MOUNTING HOLES(20.5)	t=0.8
	SECTION A
	<b>↓</b>     .
	12.5
	Ⅰ <u>↓</u> //.
12 17 6-M5 NUTS   (SCREWING DEPTH) 13.5 20.71 22.86 22.86	
INTERNAL CONNECTION	
	herwise specified Dimension Tolerance
	to 3 ±0.2
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	to 6 ±0.3 to 30 ±0.5
EUP(33)	to 120 ±0.8
	to 400 ±1.2
$V(2)$ $W(3)$ $B(4)$ of $\oplus$	Ø0.5
N(36) EUN(29) EVP(21) EWN(13) EB(5)	

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#### MAXIMUM RATINGS (Tj=25 °C, unless otherwise specified)

Symbol	Item	Conditions	Rating	Unit
V <sub>CES</sub>	Collector-emitter voltage	G-E short-circuited	600	V
V <sub>GES</sub>	Gate-emitter voltage	C-E short-circuited	± 20	V
Ic		DC, T <sub>C</sub> =63 °C (Note2, 4)	150	
ICRM	Collector current	Pulse, Repetitive (Note3)	300	- A
Ptot	Total power dissipation	T <sub>C</sub> =25 °C (Note2, 4)	520	W
IE (Note1)		DC (Note2)	150	
I <sub>ERM</sub> (Note1)	Emitter current	Pulse, Repetitive (Note3)	300	A

#### BRAKE PART IGBT/DIODE

Symbol	Item	Conditions	Rating	Unit
V <sub>CES</sub>	Collector-emitter voltage	G-E short-circuited	600	V
V <sub>GES</sub>	Gate-emitter voltage	C-E short-circuited	± 20	V
lc	Collector ourrent	DC, T <sub>C</sub> =70 °C (Note2, 4)	75	•
I <sub>CRM</sub>	Collector current	Pulse, Repetitive (Note3)	150	A
Ptot	Total power dissipation	T <sub>C</sub> =25 °C (Note2, 4)	280	W
V <sub>RRM</sub>	Repetitive peak reverse voltage	G-E short-circuited	600	V
I <sub>F</sub>	Forward current	DC (Note2)	75	
I <sub>FRM</sub>	Forward current	Pulse, Repetitive (Note3)	150	A

#### MODULE

Symbol	Item	Conditions	Rating	Unit
Visol	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	2500	V
Tj	Junction temperature	-	-40 ~ +150	°C
T <sub>stg</sub>	Storage temperature	-	-40 ~ +125	C
T <sub>Cmax</sub>	Maximum case temperature	(Note4)	125	°C

## ELECTRICAL CHARACTERISTICS ( $T_j=25$ °C, unless otherwise specified) INVERTER PART IGBT/DIODE

Currente e l	ltere	Conditions			Limits		المناسبة الم
Symbol Item Conditions		-	Min.	Тур.	Max.	Unit	
I <sub>CES</sub>	Collector-emitter cut-off current	V <sub>CE</sub> =V <sub>CES</sub> , G-E short-circuited		-	-	1.0	mA
I <sub>GES</sub>	Gate-emitter leakage current	V <sub>GE</sub> =V <sub>GES</sub> , C-E short-circuited		-	-	0.5	μA
$V_{\text{GE(th)}}$	Gate-emitter threshold voltage	I <sub>C</sub> =15 mA, V <sub>CE</sub> =10 V		5	6	7	V
		I <sub>C</sub> =150 A, V <sub>GE</sub> =15 V <sup>(Note5)</sup>	T <sub>j</sub> =25 °C	-	1.7	2.1	
$V_{\text{CEsat}}$	Collector-emitter saturation voltage	Refer to the figure of test circuit	T <sub>j</sub> =125 °C	-	1.9	-	V
		$I_{C}$ =150 A, $V_{GE}$ =15 V, chip (Note5)		-	1.6	-	
Cies	Input capacitance			-	-	18	
Coes	Output capacitance	V <sub>CE</sub> =10 V, G-E short-circuited	-	-	-	2.0	nF
Cres	Reverse transfer capacitance		-	-	-	0.6	
$Q_{G}$	Gate charge	V <sub>CC</sub> =300 V, I <sub>C</sub> =150 A, V <sub>GE</sub> =15 V		-	400	-	nC
t <sub>d(on)</sub>	Turn-on delay time			-	-	120	
tr	Rise time	- V <sub>CC</sub> =300 V, I <sub>C</sub> =150 A, V <sub>GE</sub> =±15 V,	-	-	-	100	
$t_{d(off)}$	Turn-off delay time		-	-	-	350	ns
t <sub>f</sub>	Fall time	- R <sub>G</sub> =6.2 Ω, Inductive load	-	-	-	600	1
r <sub>g</sub>	Internal gate resistance	Per switch		-	0	-	Ω

## ELECTRICAL CHARACTERISTICS (cont.; $T_j=25$ °C, unless otherwise specified) INVERTER PART IGBT/DIODE

Cumbal	ltom	Conditions		Limits			Link
Symbol Item		Conditions		Min.	Тур.	Max.	Unit
		I <sub>E</sub> =150 A, G-E short-circuited <sup>(Note5)</sup>	T <sub>j</sub> =25 °C	-	2.0	2.8	
V <sub>EC</sub> (Note1)	Emitter-collector voltage	Refer to the figure of test circuit	T <sub>j</sub> =125 °C	-	1.95	-	V
		I <sub>E</sub> =150 A, G-E short-circuited, chip (Note5)		-	1.9	-	
trr (Note1)	Reverse recovery time	$V_{CC}$ =300 V, I <sub>E</sub> =150 A, V <sub>GE</sub> =±15 V,		-	-	200	ns
Qrr (Note1)	Reverse recovery charge	$R_G=6.2 \Omega$ , Inductive load		-	5.0	-	μC
Eon	Turn-on switching energy per pulse	$V_{CC}$ =300 V, $I_{C}$ = $I_{E}$ =150 A,		-	3.2	-	- m l
E <sub>off</sub>	Turn-off switching energy per pulse	$V_{GE}=\pm 15 V, R_{G}=6.2 \Omega, T_{j}=125 °C,$		-	7.4	-	mJ
Err (Note1)	Reverse recovery energy per pulse	Inductive load		-	1.47	-	mJ

#### BRAKE PART IGBT/DIODE

Currente e l	literee	Canditiana		Limits			1.1
Symbol	Item	Conditions		Min.	Тур.	Max.	Unit
ICES	Collector-emitter cut-off current	V <sub>CE</sub> =V <sub>CES</sub> , G-E short-circuited		-	-	1.0	mA
I <sub>GES</sub>	Gate-emitter leakage current	V <sub>GE</sub> =V <sub>GES</sub> , C-E short-circuited		-	-	0.5	μA
$V_{\text{GE(th)}}$	Gate-emitter threshold voltage	I <sub>C</sub> =7.5 mA, V <sub>CE</sub> =10 V		5	6	7	V
		I <sub>C</sub> =75 A, V <sub>GE</sub> =15 V <sup>(Note5)</sup>	T <sub>j</sub> =25 °C	-	1.7	2.1	
$V_{\text{CEsat}}$	Collector-emitter saturation voltage	Refer to the figure of test circuit	T <sub>j</sub> =125 °C	-	1.9	-	V
		I <sub>C</sub> =75 A, V <sub>GE</sub> =15 V, chip (Note5)		-	1.6	-	1
Cies	Input capacitance			-	-	9.3	
Coes	Output capacitance	V <sub>CE</sub> =10 V, G-E short-circuited		-	-	1.0	nF
Cres	Reverse transfer capacitance			-	-	0.3	1
$Q_{G}$	Gate charge	$V_{CC}$ =300 V, I <sub>C</sub> =75 A, V <sub>GE</sub> =15 V		-	200	-	nC
I <sub>RRM</sub>	Repetitive peak reverse current	V <sub>R</sub> =V <sub>RRM</sub> , G-E short-circuited		-	-	1.0	mA
		I <sub>F</sub> =75 A, G-E short-circuited <sup>(Note5)</sup>	T <sub>j</sub> =25 °C	-	2.0	2.8	
VF	Forward voltage	Refer to the figure of test circuit	T <sub>j</sub> =125 °C	-	1.95	-	V
		I <sub>F</sub> =75 A, G-E short-circuited, chip <sup>(N</sup>	lote5)	-	1.9	-	1
r <sub>g</sub>	Internal gate resistance	-		-	0	-	Ω

#### NTC THERMISTOR PART

Symbol	Item	Conditions -		Unit		
			Min.	Тур.	Max.	Unit
R <sub>25</sub>	Zero-power resistance	T <sub>c</sub> =25 °C <sup>(Note4)</sup>	4.85	5.00	5.15	kΩ
ΔR/R	Deviation of resistance	$R_{100}$ =493 Ω, $T_{C}$ =100 °C <sup>(Note4)</sup>	-7.3	-	+7.8	%
B <sub>(25/50)</sub>	B-constant	Approximate by equation (Note6)	-	3375	-	K
P <sub>25</sub>	Power dissipation	T <sub>c</sub> =25 °C <sup>(Note4)</sup>	-	-	10	mW

#### THERMAL RESISTANCE CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
Symbol	nem	Conditions	Min.	Тур.	Max.	Unit
R <sub>th(j-c)Q</sub>		Junction to case, per Inverter IGBT (Note4)	-	-	0.24	K/W
R <sub>th(j-c)D</sub>		Junction to case, per Inverter DIODE (Note4)	-	-	0.46	r\/ VV
R <sub>th(j-c)Q</sub>	Thermal resistance	Junction to case, Brake IGBT (Note4)	-	-	0.44	K/W
R <sub>th(j-c)D</sub>		Junction to case, Brake DIODE (Note4)	-	-	0.85	r\/ VV
$R_{th(c-s)}$	Contact thermal resistance	Case to heat sink, per 1 module, Thermal grease applied (Note4, 7)	-	15	-	K/kW

## HIGH POWER SWITCHING USE INSULATED TYPE

#### MECHANICAL CHARACTERISTICS

Symbol	Itom	Conditions	O a se all'hi a se a		Limits		
	Item	Conditions		Min.	Тур.	Max.	Unit
Mt	Mounting torque	Main terminals	M 5 screw	2.5	3.0	3.5	N∙m
Ms	Mounting torque	Mounting to heat sink	M 5 screw	2.5	3.0	3.5	N∙m
	Creepage distance	Terminal to terminal		10.28	-	-	
ds		Terminal to base plate		12.46	-	-	mm
d	Clearance	Terminal to terminal		9.88	-	-	
da	Clearance	Terminal to base plate		10.12	-	-	mm
m	mass	-		-	350	-	g
ec	Flatness of base plate	On the centerline X, Y (Note8)		±0	-	+100	μm

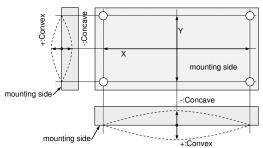
Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free wheeling diode (DIODE).

- 2. Junction temperature  $(T_j)$  should not increase beyond  $T_{jmax}$  rating.
- 3. Pulse width and repetition rate should be such that the device junction temperature (T<sub>j</sub>) dose not exceed T<sub>jmax</sub> rating.
- 4. Case temperature (T<sub>c</sub>) and heat sink temperature (T<sub>s</sub>) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
- 5. Pulse width and repetition rate should be such as to cause negligible temperature rise.

$$6. \mathsf{B}_{(25/50)} = \mathsf{In}(\frac{\mathsf{R}_{25}}{\mathsf{R}_{50}}) / (\frac{1}{\mathsf{T}_{25}} - \frac{1}{\mathsf{T}_{50}}) \ ,$$

R<sub>25</sub>: resistance at absolute temperature T<sub>25</sub> [K]; T<sub>25</sub>=25 [°C]+273.15=298.15 [K]

- $R_{50}$ : resistance at absolute temperature  $T_{50}$  [K];  $T_{50}$ =50 [°C]+273.15=323.15 [K]
- 7. Typical value is measured by using thermally conductive grease of  $\lambda$ =0.9 W/(m·K).
- 8. The base plate (mounting side) flatness measurement points (X, Y) are as follows of the following figure.



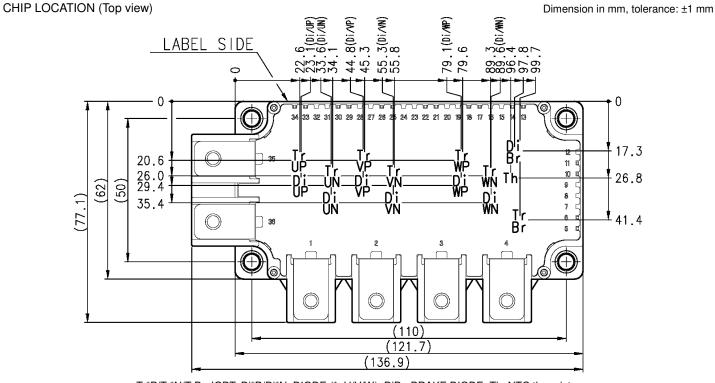
9. Use the following screws when mounting the printed circuit board (PCB) on the standoffs. " $\phi$ 2.3×10 or  $\phi$ 2.3×12, B1 tapping screw"

The length of the screw depends on the thickness (t1.6~t2.0) of the PCB.

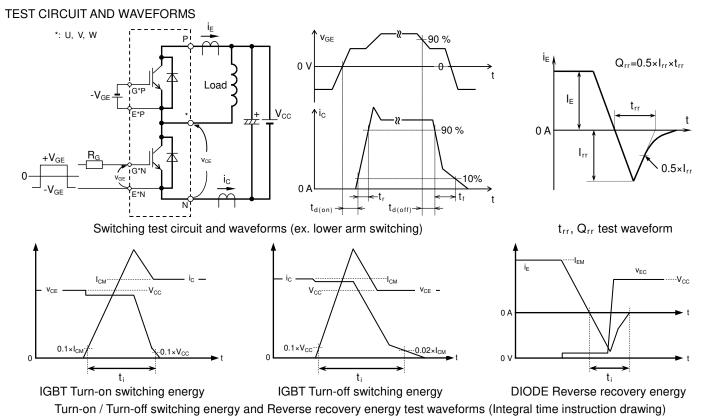
#### RECOMMENDED OPERATING CONDITIONS

Symbol	Item	Conditions			Unit		
Symbol	item			Min.	Тур.	Max.	Unit
V <sub>cc</sub>	(DC) Supply voltage	Applied across P-N terminals		-	300	400	V
V <sub>GEon</sub>	Gate (-emitter drive) voltage	Applied across GB-EB / G*P-E*P / G*N-E*N (*=U, V, W) terminals		13.5	15.0	16.5	V
R <sub>G</sub>	External acto registence	Per switch	Inverter IGBT	4.1	-	41	Ω
п <sub>G</sub>	External gate resistance	Brake IGBT		8.0	-	83	- 12

HIGH POWER SWITCHING USE INSULATED TYPE



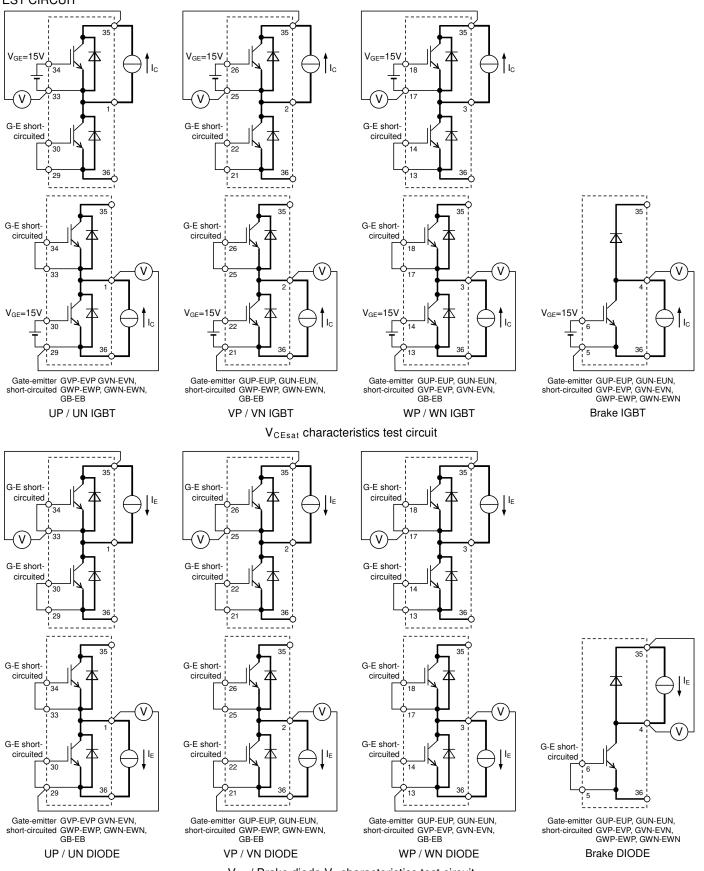
Tr\*P/Tr\*N/TrBr: IGBT, Di\*P/Di\*N: DIODE (\*=U/V/W), DiBr: BRAKE DIODE, Th: NTC thermistor

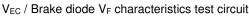


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HIGH POWER SWITCHING USE INSULATED TYPE

#### TEST CIRCUIT

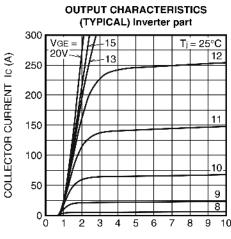




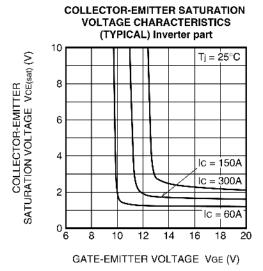
HIGH POWER SWITCHING USE INSULATED TYPE

#### PERFORMANCE CURVES

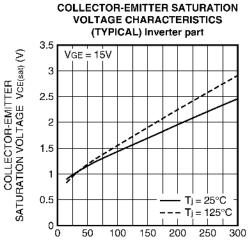
#### INVERTER PART



COLLECTOR-EMITTER VOLTAGE VCE (V)

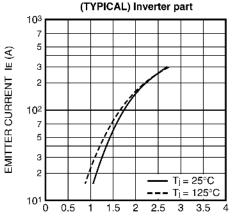


CAPACITANCE CHARACTERISTICS (TYPICAL) Inverter part  $10^{2}$ £ 3 2 CAPACITANCE (nF) 10<sup>1</sup> ŝ 3 2 10<sup>0</sup> 5 3 2 VGE = ÓV 10-1 10-1 2 3 57100 2 3 57101 2 3 57102 COLLECTOR-EMITTER VOLTAGE VCE (V)

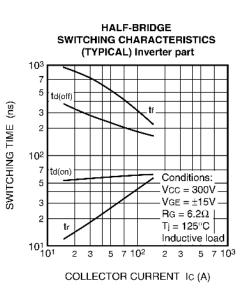


COLLECTOR CURRENT IC (A)

FREE WHEELING DIODE FORWARD CHARACTERISTICS



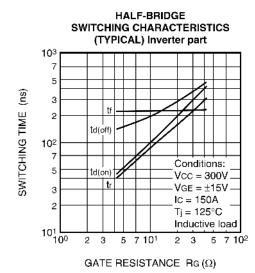
EMITTER-COLLECTOR VOLTAGE VEC (V)



HIGH POWER SWITCHING USE INSULATED TYPE

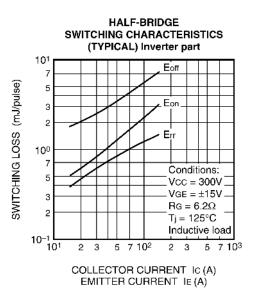
#### PERFORMANCE CURVES

#### **INVERTER PART**

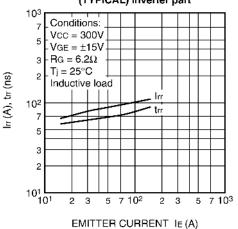


HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL) Inverter part 10<sup>2</sup> 5 SWITCHING LOSS (mJ/pulse) 3 2 Eon 101 5 З 2 Conditions: 100 VCC = 300VFr 5  $V_{GE} = \pm 15V$ IC, IE = 150A 3 Tj = 125°C 2 Inductive load 10-1 L 100 5 7 10<sup>1</sup> 7 102 З 2 2 3 5 GATE RESISTANCE  $\operatorname{RG}(\Omega)$ 

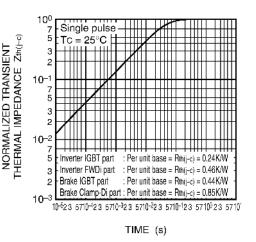
GATE CHARGE CHARACTERISTICS (TYPICAL) Inverter part 20 IC = 150A GATE-EMITTER VOLTAGE VGE (V) Vcc = 200 15 Vcc = 300V 10 5 0⊾ 0 100 200 300 400 500 600 GATE CHARGE QG (nC)



REVERSE RECOVERY CHARACTERISTICS OF FREE WHEELING DIODE (TYPICAL) Inverter part



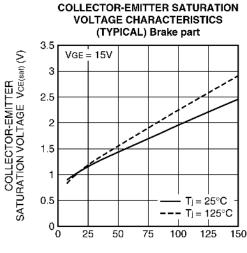
TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS



HIGH POWER SWITCHING USE INSULATED TYPE

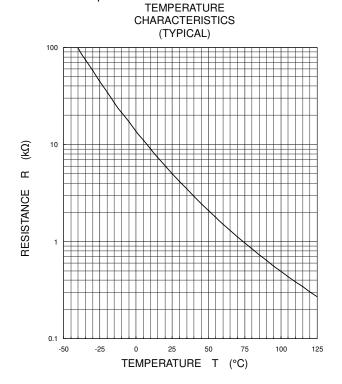
#### PERFORMANCE CURVES

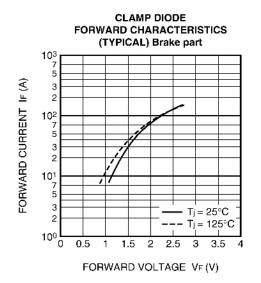
#### **BRAKE PART**



COLLECTOR CURRENT Ic (A)

NTC thermistor part





### Keep safety first in your circuit designs!

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