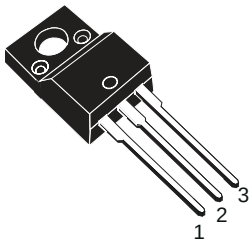
ST13007DFP

HIGH VOLTAGE FAST-SWITCHING NPN POWER TRANSISTOR

- IMPROVED SPECIFICATION:
  - LOWER LEAKAGE CURRENT
  - TIGHTER GAIN RANGE
  - DC CURRENT GAIN PRESELECTION
  - TIGHTER STORAGE TIME RANGE
- HIGH VOLTAGE CAPABILITY
- INTEGRATED FREE-WHEELING DIODE
- LOW SPREAD OF DYNAMIC PARAMETERS
- MINIMUM LOT-TO-LOT SPREAD FOR RELIABLE OPERATION
- VERY HIGH SWITCHING SPEED
- FULLY CHARACTERIZED AT 125 ^{o}C
- LARGE RBSOA
- FULLY INSULATED PACKAGE (U.L. COMPLIANT) FOR EASY MOUNTING

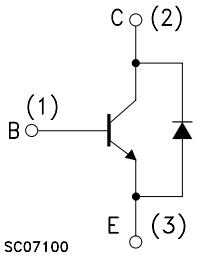


TO-220FP

APPLICATIONS

- UP TO 120W ELECTRONIC TRANSFORMERS FOR HALOGEN LAMPS
- SWITCH MODE POWER SUPPLIES

INTERNAL SCHEMATIC DIAGRAM



DESCRIPTION

The device is manufactured using high voltage Multi Epitaxial Planar technology for high switching speeds and high voltage capability.

It uses a Cellular Emitter structure to enhance switching speeds.

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V_{CEV}	Collector-Emitter Voltage (V_{BE} = -1.5V)	700	V
V_{CEO}	Collector-Emitter Voltage (I_B = 0)	400	V
V_{EBO}	Emitter-Base Voltage (I_C = 0)	9	V
I_C	Collector Current	8	A
I_{CM}	Collector Peak Current	16	A
I_B	Base Current	4	A
I_{BM}	Base Peak Current	8	A
P_{tot}	Total Dissipation at $T_c \leq 25$ ^{o}C	36	W
V_{isol}	Insulation Withstand Voltage (RMS) from All Three Leads to External Heatsink	1500	V
T_{stg}	Storage Temperature	-65 to 150	^{o}C
T_j	Max. Operating Junction Temperature	150	^{o}C

THERMAL DATA

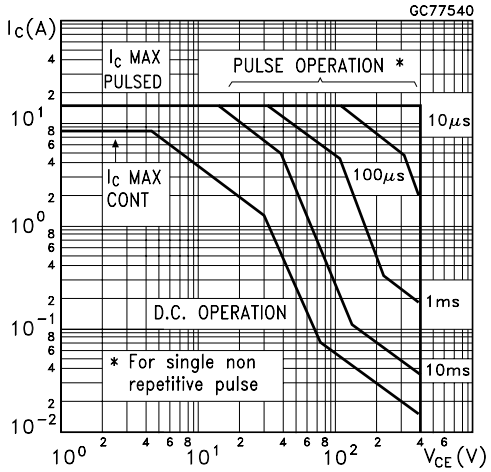
$R_{thj\text{-}case}$	Thermal Resistance Junction-case	Max	3.47	$^oC/W$
$R_{thj\text{-}amb}$	Thermal Resistance Junction-ambient	Max	62.5	$^oC/W$

ELECTRICAL CHARACTERISTICS (T_{case} = 25 ^oC unless otherwise specified)

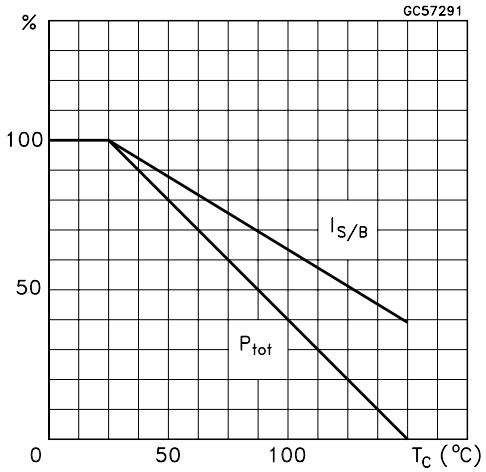
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
I_{CES}	Collector Cut-off Current (V_{BE} = 0)	V_{CE} = 700 V V_{CE} = 700 V T_c = 100 ^oC			10 0.5	µA mA
I_{CEO}	Collector Cut-off Current (I_B = 0)	V_{CE} = 400 V			100	µA
I_{EBO}	Emitter Cut-off Current (I_C = 0)	V_{EB} = 9 V			100	µA
$V_{CEO(sus)}$*	Collector-Emitter Sustaining Voltage (I_B = 0)	I_C = 10 mA	400			V
$V_{CE(sat)}$*	Collector-Emitter Saturation Voltage	I_C = 2 A I_B = 0.4 A I_C = 5 A I_B = 1 A I_C = 8 A I_B = 2 A I_C = 5 A I_B = 1 A T_c = 100 ^oC			0.8 1.5 2 3	V V V V
$V_{BE(sat)}$*	Base-Emitter Saturation Voltage	I_C = 2 A I_B = 0.4 A I_C = 5 A I_B = 1 A I_C = 5 A I_B = 1 A T_c = 100 ^oC			1.2 1.6 1.5	V V V
h_{FE}*	DC Current Gain	I_C = 2 A V_{CE} = 5 V I_C = 5 A V_{CE} = 5 V	18 8		40 25	
V_f	Diode Forward Voltage	I_C = 3 A			2.5	V
 t_s t_f	INDUCTIVE LOAD Storage Time Fall Time	I_C = 5 A V_{CL} = 250 V R_{BB} = 0Ω I_{B1} = 1 A $V_{BE(off)}$ = -5 V L = 200 µH (see figure 1)		 1.7 90	 2.3 150	 µs ns
 t_s t_f	INDUCTIVE LOAD Storage Time Fall Time	I_C = 5 A V_{CL} = 250 V R_{BB} = 0Ω I_{B1} = 1 A $V_{BE(off)}$ = -5 V L = 200 µH T_C = 125 ^oC (see figure 1)		 2.2 150		 µs ns

* Pulsed: Pulse duration = 300 µs, duty cycle 2 %.

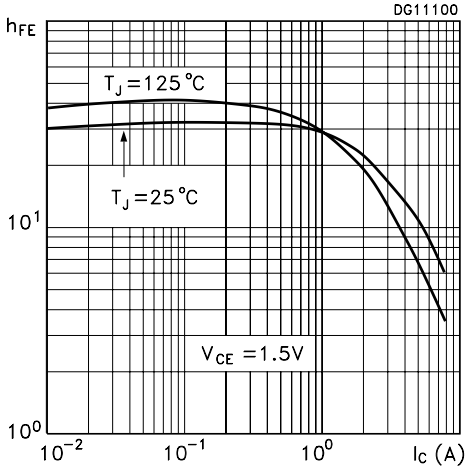
Safe Operating Area



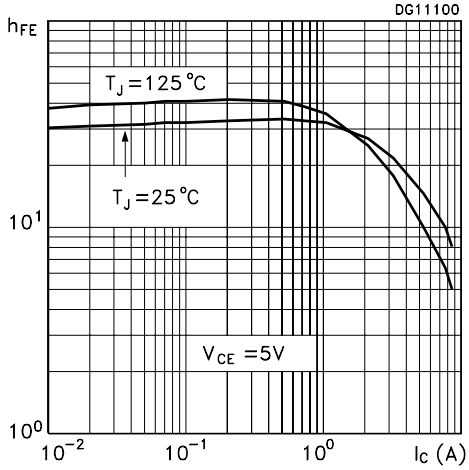
Derating Curve



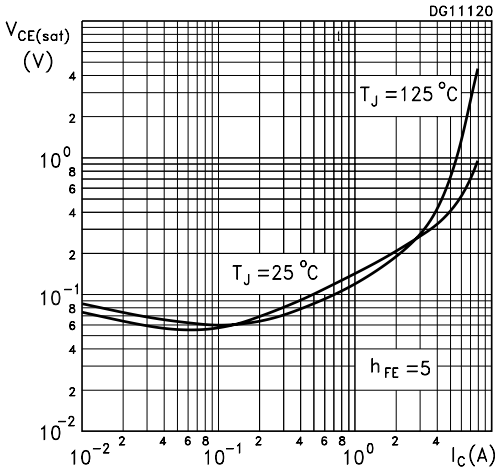
DC Current Gain



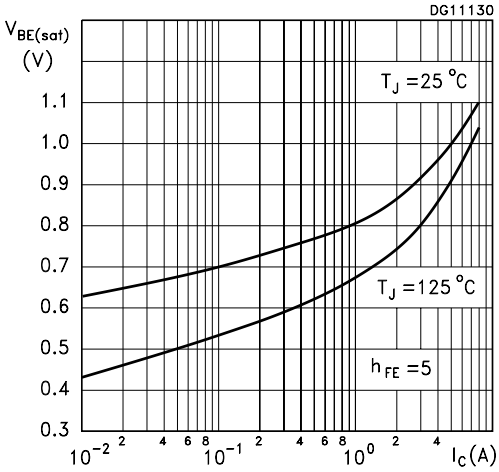
DC Current Gain



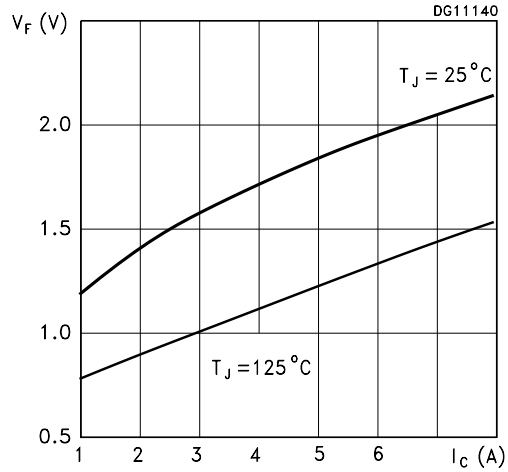
Collector Emitter Saturation Voltage



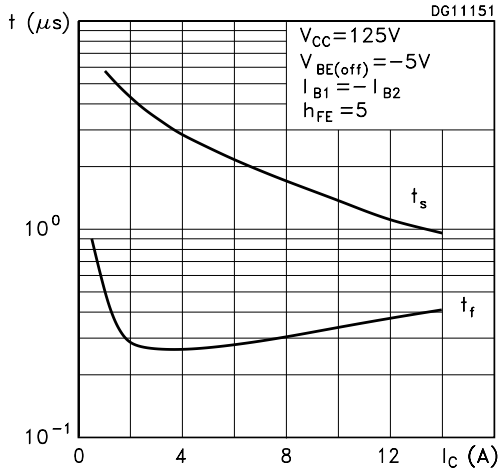
Base Emitter Saturation Voltage



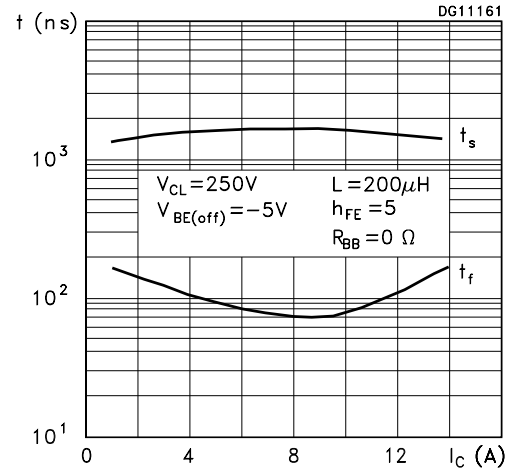
Diode Forward Voltage



Switching Time Resistive Load



Switching Time Inductive Load



Reverse Biased SOA

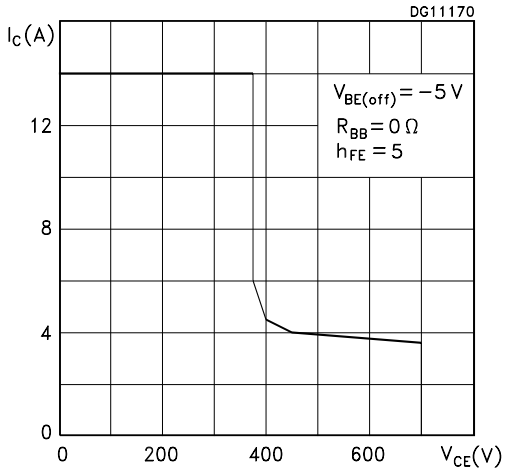


Figure 1: Inductive Load Switching Test Circuit.

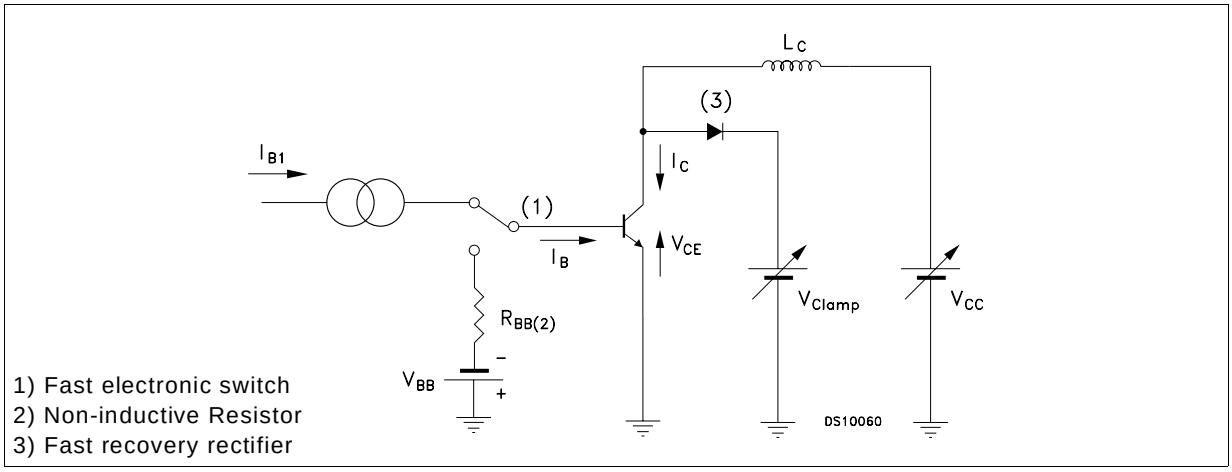
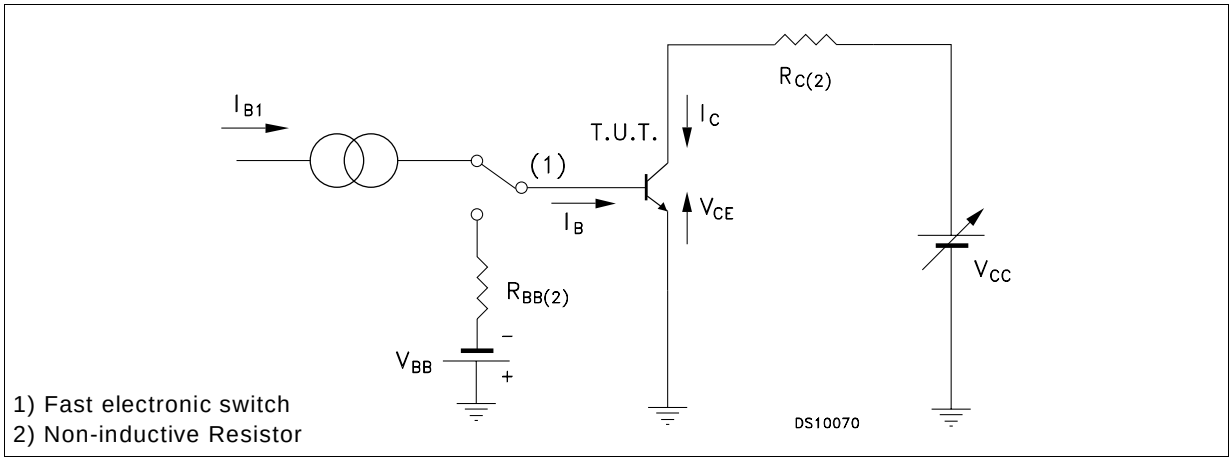


Figure 2: Resistive Load Switching Test Circuit.



TO-220FP MECHANICAL DATA

DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	4.4		4.6	0.173		0.181
B	2.5		2.7	0.098		0.106
D	2.5		2.75	0.098		0.108
E	0.45		0.7	0.017		0.027
F	0.75		1	0.030		0.039
F1	1.15		1.7	0.045		0.067
F2	1.15		1.7	0.045		0.067
G	4.95		5.2	0.195		0.204
G1	2.4		2.7	0.094		0.106
H	10		10.4	0.393		0.409
L2		16			0.630	
L3	28.6		30.6	1.126		1.204
L4	9.8		10.6	0.385		0.417
L6	15.9		16.4	0.626		0.645
L7	9		9.3	0.354		0.366
Ø	3		3.2	0.118		0.126