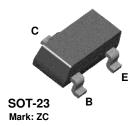


## 2N4124

## **MMBT4124**





## **NPN General Purpose Amplifier**

This device is designed as a general purpose amplifier and switch. The useful dynamic range extends to 100 mA as a switch and to 100 MHz as an amplifier.

#### **Absolute Maximum Ratings\***

TA = 25°C unless otherwise noted

| Symbol                            | Parameter  | Value       | Units |  |
|-----------------------------------|--|-------------|-------|--|
| V <sub>CEO</sub>                  | Collector-Emitter Voltage                        | 25          | V     |  |
| V <sub>CBO</sub>                  | Collector-Base Voltage                           | 30          | V     |  |
| V <sub>EBO</sub>                  | Emitter-Base Voltage 5.0                         |             | V     |  |
| I <sub>C</sub>                    | Collector Current - Continuous                   | 200         | mA    |  |
| T <sub>J</sub> , T <sub>stg</sub> | Operating and Storage Junction Temperature Range | -55 to +150 | °C    |  |

 $<sup>^{\</sup>star}$ These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

#### **Thermal Characteristics**

TA = 25°C unless otherwise noted

| Symbol          | Characteristic                          | Max    |           | Units |
|-----------------|---|--------|-----------|-------|
|                 |   | 2N4124 | *MMBT4124 |       |
| $P_{D}$         | Total Device Dissipation                | 625    | 350       | mW    |
|                 | Derate above 25°C                       | 5.0    | 2.8       | mW/°C |
| $R_{\theta JC}$ | Thermal Resistance, Junction to Case    | 83.3   |           | °C/W  |
| $R_{\theta JA}$ | Thermal Resistance, Junction to Ambient | 200    | 357       | °C/W  |

<sup>\*</sup>Device mounted on FR-4 PCB 1.6" X 1.6" X 0.06."

<sup>1)</sup> These ratings are based on a maximum junction temperature of 150 degrees C.
2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

# NPN General Purpose Amplifier (continued)

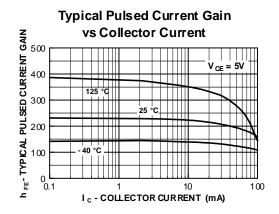
| lectrical | <br>nara | 1-1 OT | <br> |
|-----------|----------|--------|------|
|           |          |        |      |

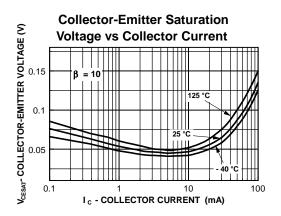
| Symbol               | Parameter  | Test Conditions  | Min       | Max  | Units |
|----------------------|--|--|-----------|------|-------|
|                      |  |  |           |      |       |
| OFF CHAI             | RACTERISTICS   |  |           |      |       |
| $V_{(BR)CEO}$        | Collector-Emitter Breakdown Voltage                    | $I_C = 1.0 \text{ mA}, I_B = 0$  | 25        |      | V     |
| V <sub>(BR)CBO</sub> | Collector-Base Breakdown Voltage                       | $I_C = 10  \mu A,  I_E = 0$  | 30        |      | V     |
| V <sub>(BR)EBO</sub> | Emitter-Base Breakdown Voltage                         | $I_C = 10  \mu A,  I_C = 0$  | 5.0       |      | V     |
| I <sub>CBO</sub>     | Collector Cutoff Current                               | $V_{CB} = 20 \text{ V}, I_{E} = 0$   |           | 50   | nA    |
| I <sub>EBO</sub>     | Emitter Cutoff Current                                 | $V_{EB} = 3.0 \text{ V}, I_{C} = 0$  |           | 50   | nA    |
| h <sub>FE</sub>      | DC Current Gain  | $I_C = 2.0 \text{ mA}, V_{CE} = 1.0 \text{ V}$   | 120<br>60 | 360  |       |
| ON CHAR              | ACTERISTICS*   |  |           |      |       |
| V <sub>CE(sat)</sub> | Collector-Emitter Saturation Voltage                   | $I_C = 50 \text{ mA}, V_{CE} = 1.0 \text{ V}$<br>$I_C = 50 \text{ mA}, I_R = 5.0 \text{ mA}$ | 60        | 0.3  | V     |
| V <sub>BE(sat)</sub> | Base-Emitter Saturation Voltage                        | $I_C = 50 \text{ mA}, I_B = 5.0 \text{ mA}$  |           | 0.95 | V     |
| SMALL SI             | GNAL CHARACTERISTICS  Current Gain - Bandwidth Product | I <sub>C</sub> = 10 mA, V <sub>CE</sub> = 20 V,<br>f = 100 MHz                               | 300       |      | MHz   |
| C <sub>obo</sub>     | Output Capacitance                                     | $V_{CB} = 5.0 \text{ V}, I_E = 0,$<br>f = 100  kHz   |           | 4.0  | pF    |
| C <sub>ibo</sub>     | Input Capacitance                                      | $V_{BE} = 0.5 \text{ V}, I_{C} = 0,$<br>f = 1.0 kHz  |           | 8.0  | pF    |
| C <sub>cb</sub>      | Collector-Base Capcitance                              | $V_{CB} = 5.0 \text{ V}, I_{E} = 0,$<br>f = 100 kHz  |           | 4.0  | pF    |
| h <sub>fe</sub>      | Small-Signal Current Gain                              | $V_{CE} = 10 \text{ V}, I_{C} = 2.0 \text{ mA},$<br>f = 1.0 kHz                              | 120       | 480  |       |
| NF                   | Noise Figure   | $I_C = 100 \mu A$ , $V_{CE} = 5.0 V$ ,<br>$R_S = 1.0 kΩ$ , $f = 10 Hz$ to 15.7 kHz           |           | 5.0  | dB    |

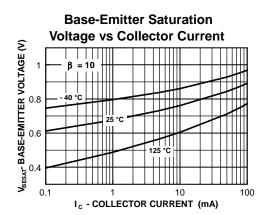
<sup>\*</sup>Pulse Test: Pulse Width  $\leq$  300  $\mu$ s, Duty Cycle  $\leq$  2.0%

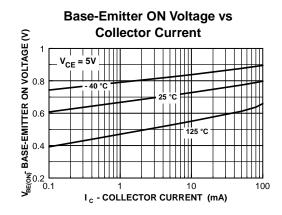
(continued)

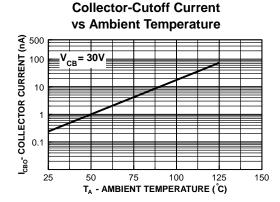
## **Typical Characteristics**

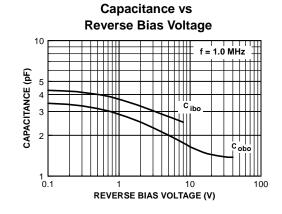






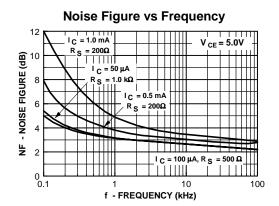


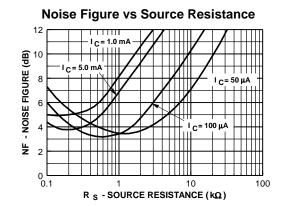


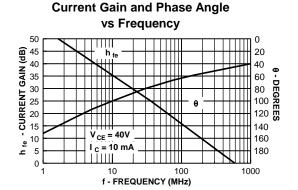


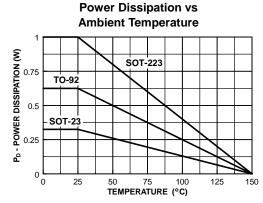
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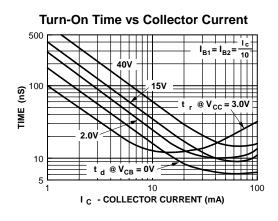
#### Typical Characteristics (continued)

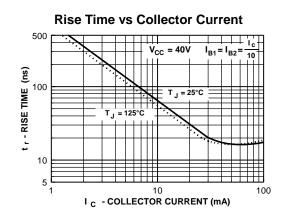








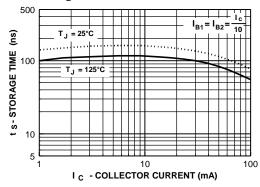




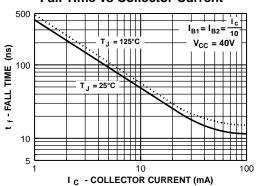
(continued)

## Typical Characteristics (continued)

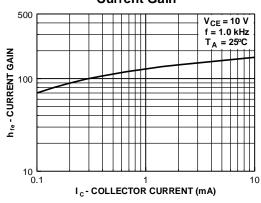




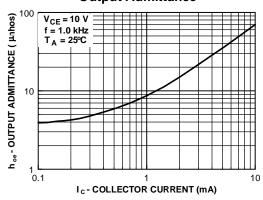
#### **Fall Time vs Collector Current**



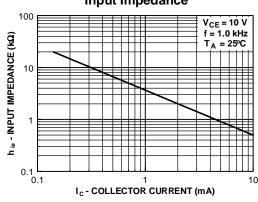
**Current Gain** 



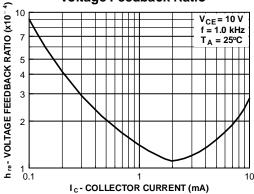
**Output Admittance** 



Input Impedance



Voltage Feedback Ratio



(continued)

## **Test Circuits**

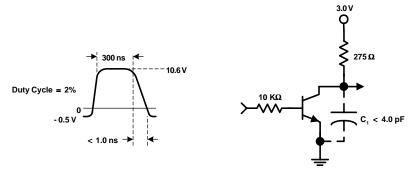


FIGURE 1: Delay and Rise Time Equivalent Test Circuit

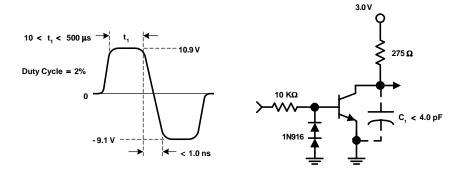


FIGURE 2: Storage and Fall Time Equivalent Test Circuit

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