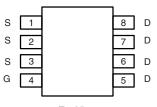


Vishay Siliconix

P-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)			
- 30	0.030 at V _{GS} = - 10 V	- 7.5			
	0.050 at V _{GS} = - 4.5 V	- 5.8			





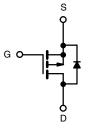
Top View

FEATURES

Halogen-free According to IEC 61249-2-21

Available • TrenchFET[®] Power MOSFETs





P-Channel MOSFET

Ordering Information: Si4431BDY-T1-E3 (Lead (Pb)-free) Si4431BDY-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS $T_A = 25 ^{\circ}C$, unless otherwise noted						
Parameter		Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	- 30		V	
Gate-Source Voltage		V _{GS}	± 20		v	
Continuous Drain Current /T 150 °C\a	T _A = 25 °C	– I _D	- 7.5	- 5.7		
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C		- 6.0	- 4.6		
Pulsed Drain Current		I _{DM}	- 30		A	
Continuous Source Current (Diode Conduction) ^a		۱ _s	- 2.1	- 1.2		
	T _A = 25 °C	P _D	2.5	1.5	W	
Maximum Power Dissipation ^a	T _A = 70 °C	- FD	1.6	0.9	vv	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Mauinum lunction to Anchienta	t ≤ 10 s	R _{thJA}	38	50	
Maximum Junction-to-Ambient ^a	Steady State	' 'thJA	70	85	°C/W
Maximum Junction-to-Foot	Steady State	R _{thJF}	22	28	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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SPECIFICATIONS $T_J = 25 \text{ °C}$, unless otherwise noted								
Parameter	Symbol	Test Conditions		Тур.	Max.	Unit		
Static								
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -250 \ \mu A$	- 1.0		- 3.0	V		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA		
	I _{DSS}	V _{DS} = - 30 V, V _{GS} = 0 V			- 1			
Zero Gate Voltage Drain Current		V_{DS} = - 30 V, V_{GS} = 0 V, T_{J} = 70 $^{\circ}C$			- 10	μΑ		
		V _{DS} = - 5 V, V _{GS} = - 10 V - 30				•		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = -5 V, V_{GS} = -4.5 V$	- 7			A		
	Б	V_{GS} = - 10 V, I _D = - 7.5 A		0.023	0.030	Ω		
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = -4.5 \text{ V}, \text{ I}_{D} = -5.8 \text{ A}$		0.036	0.050			
Forward Transconductance ^a	9 _{fs}	$V_{DS} = -15 \text{ V}, \text{ I}_{D} = -7.5 \text{ A}$		18		S		
Diode Forward Voltage ^a	V _{SD}	I _S = - 2.1 A, V _{GS} = 0 V		- 0.78	- 1.1	V		
Dynamic ^b								
Total Gate Charge	Qg			13	20			
Gate-Source Charge	Q _{gs}	V_{DS} = - 15 V, V_{GS} = - 5 V, I_{D} = - 7.5 A		3.6		nC		
Gate-Drain Charge	Q _{gd}			6				
Turn-On Delay Time	t _{d(on)}			10	20			
Rise Time	t _r	V_{DD} = - 15 V, R_L = 15 Ω		10	20			
Turn-Off Delay Time	t _{d(off)}	$\text{I}_{\text{D}}\cong$ - 1 A, V_{GEN} = - 10 V, R_{G} = 6 Ω		70	110	ns		
Fall Time	t _f			47	70	110		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 2.1 A, dl/dt = 100 A/μs		45	80			

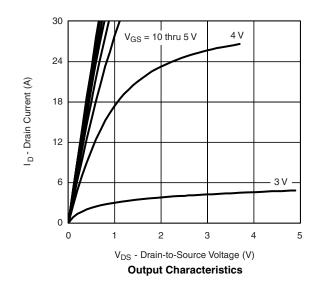
Notes:

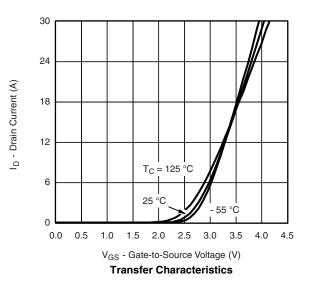
a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

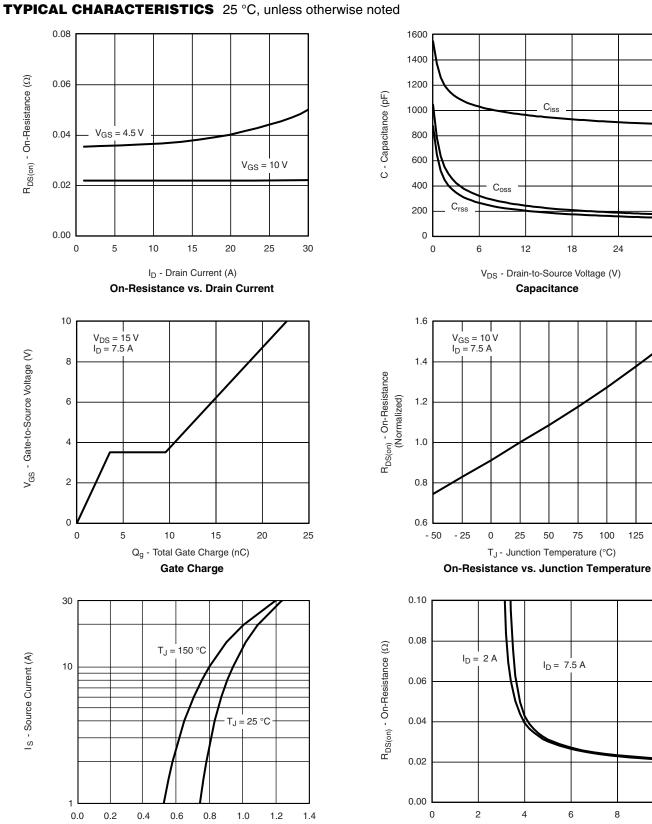
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted







V_{SD} - Source-to-Drain Voltage (V) Source-Drain Diode Forward Voltage

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V_{GS} - Gate-to-Source Voltage (V)

On-Resistance vs. Gate-to-Source Voltage

10

125

150

Si4431BDY

30

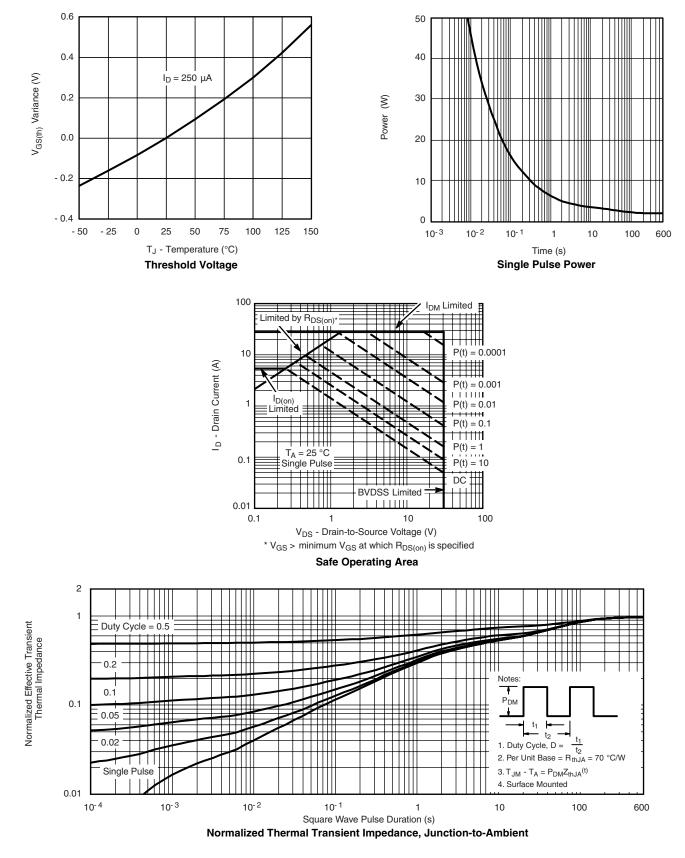
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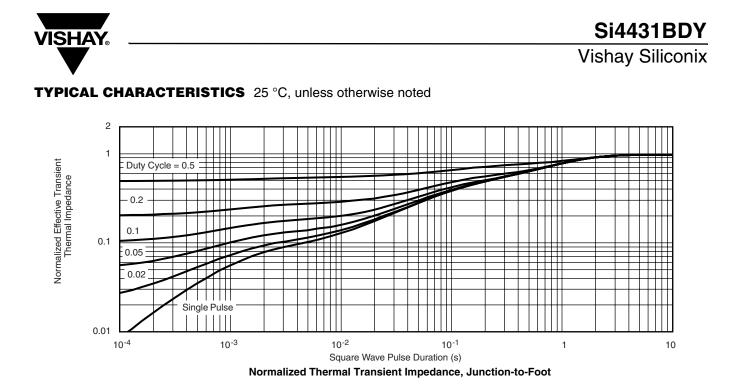
Si4431BDY

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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <u>www.vishay.com/ppg?72092</u>.

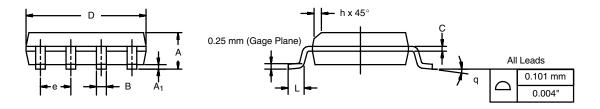


Package Information

Vishay Siliconix

SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012





	MILLIM	IETERS	INC	HES		
DIM	Min	Мах	Min	Max		
A	1.35	1.75	0.053	0.069		
A ₁	0.10	0.20	0.004	0.008		
В	0.35	0.51	0.014	0.020		
С	0.19	0.25	0.0075	0.010		
D	4.80	5.00	0.189	0.196		
E	3.80	4.00	0.150	0.157		
е	1.27	BSC	0.050 BSC			
н	5.80	6.20	0.228	0.244		
h	0.25	0.50	0.010	0.020		
L	0.50	0.93	0.020	0.037		
q	0°	8°	0°	8°		
S	0.44	0.64	0.018	0.026		
ECN: C-06527-Rev. I, 11-Sep-06 DWG: 5498						

Application Note 826

Vishay Siliconix



RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads Dimensions in Inches/(mm)

Return to Index



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