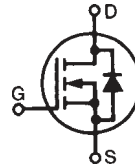


**Trench™ HiperFET™  
Power MOSFET**

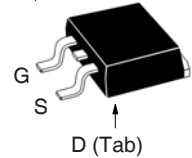
**IXFA130N10T  
IXFP130N10T**

**V<sub>DSS</sub> = 100V**  
**I<sub>D25</sub> = 130A**  
**R<sub>DS(on)</sub> ≤ 9.1mΩ**

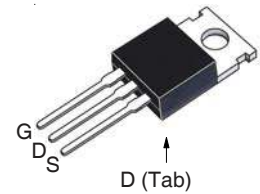
N-Channel Enhancement Mode  
 Avalanche Rated  
 Fast Intrinsic Diode



TO-263  
(IXFA)



TO-220  
(IXFP)



G = Gate      D = Drain  
 S = Source    Tab = Drain

Symbol	Test Conditions	Maximum Ratings	
V <sub>DSS</sub>	T <sub>J</sub> = 25°C to 175°C	100	V
V <sub>DGR</sub>	T <sub>J</sub> = 25°C to 175°C, R <sub>GS</sub> = 1MΩ	100	V
V <sub>GSS</sub>	Continuous	± 20	V
V <sub>GSM</sub>	Transient	± 30	V
I <sub>D25</sub>	T <sub>C</sub> = 25°C	130	A
I <sub>LRMS</sub>	Lead Current Limit, RMS	120	A
I <sub>DM</sub>	T <sub>C</sub> = 25°C, Pulse Width Limited by T <sub>JM</sub>	350	A
I <sub>A</sub>	T <sub>C</sub> = 25°C	65	A
E <sub>AS</sub>	T <sub>C</sub> = 25°C	750	mJ
P <sub>D</sub>	T <sub>C</sub> = 25°C	360	W
T <sub>J</sub>		-55 ... +175	°C
T <sub>JM</sub>		175	°C
T <sub>stg</sub>		-55 ... +175	°C
T <sub>L</sub>	Maximum Lead Temperature for Soldering	300	°C
T <sub>SOLD</sub>	1.6 mm (0.062in.) from Case for 10s	260	°C
F <sub>C</sub>	Mounting Force (TO-263)	10..65 / 2.2..14.6	N/lb
M <sub>d</sub>	Mounting Torque (TO-220)	1.13 / 10	Nm/lb.in
Weight	TO-263	2.5	g
	TO-220	3.0	g

**Features**

- Ultra-Low On Resistance
- Avalanche Rated
- Low Package Inductance
  - Easy to Drive and to Protect
- 175°C Operating Temperature
- Fast Intrinsic Diode

**Advantages**

- Easy to Mount
- Space Savings
- High Power Density

**Applications**

- Automotive
  - Motor Drives
  - 42V Power Bus
  - ABS Systems
- DC/DC Converters and Off-line UPS
- Primary Switch for 24V and 48V Systems
- Distributed Power Architectures and VRMs
- Electronic Valve Train Systems
- High Current Switching Applications
- High Voltage Synchronous Rectifier

Symbol	Test Conditions (T <sub>J</sub> = 25°C Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max.
BV <sub>DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	100		V
V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 1mA	2.5		V
I <sub>GSS</sub>	V <sub>GS</sub> = ± 20V, V <sub>DS</sub> = 0V			± 200 nA
I <sub>DSS</sub>	V <sub>DS</sub> = V <sub>DSS</sub> , V <sub>GS</sub> = 0V T <sub>J</sub> = 150°C			10 μA 500 μA
R <sub>DS(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 25A, Notes 1, 2			9.1 mΩ

Symbol	Test Conditions	Characteristic Values		
		Min.	Typ.	Max.
$g_{fs}$	$V_{DS} = 10V, I_D = 60A$ , Note 1	55	93	S
$C_{iss}$	$V_{GS} = 0V, V_{DS} = 25V, f = 1MHz$		5080	pF
$C_{oss}$			630	pF
$C_{rss}$			95	pF
$t_{d(on)}$	<b>Resistive Switching Times</b> $V_{GS} = 10V, V_{DS} = 0.5 \cdot V_{DSS}, I_D = 25A$ $R_G = 5\Omega$ (External)		30	ns
$t_r$			47	ns
$t_{d(off)}$			44	ns
$t_f$			28	ns
$Q_{g(on)}$	$V_{GS} = 10V, V_{DS} = 0.5 \cdot V_{DSS}, I_D = 25A$		104	nC
$Q_{gs}$			30	nC
$Q_{gd}$			29	nC
$R_{thJC}$			0.42	$^{\circ}C/W$
$R_{thCH}$	TO-220	0.50		$^{\circ}C/W$

**Source-Drain Diode**

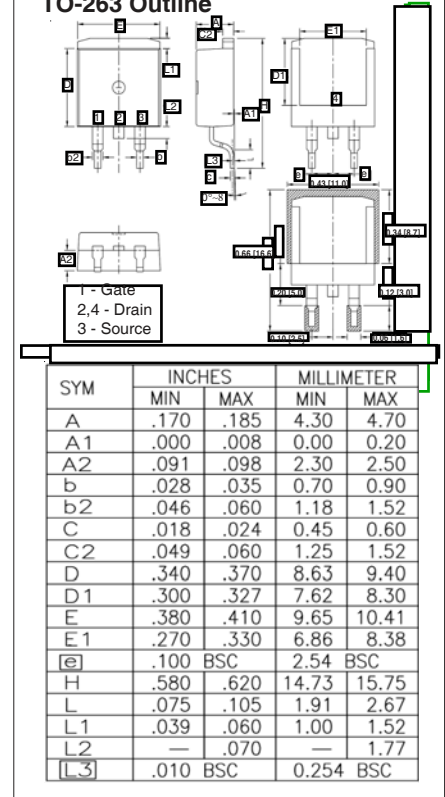
Symbol	Test Conditions	Characteristic Values		
		Min.	Typ.	Max.
$I_S$	$V_{GS} = 0V$			130 A
$I_{SM}$	Repetitive, Pulse Width Limited by $T_{JM}$			350 A
$V_{SD}$	$I_F = 25A, V_{GS} = 0V$ , Note 1			1.0 V
$t_{rr}$	$I_F = 65A, -di/dt = 100A/\mu s$ $V_R = 0.5 \cdot V_{DSS}, V_{GS} = 0V$		67	ns
$I_{RM}$			4.7	A
$Q_{rr}$			160	nC

- Notes: 1. Pulse test,  $t \leq 300 \mu s$ ; duty cycle,  $d \leq 2\%$ .  
2. On through-hole packages,  $R_{DS(on)}$  Kelvin test contact location must be 5 mm or less from the package body.

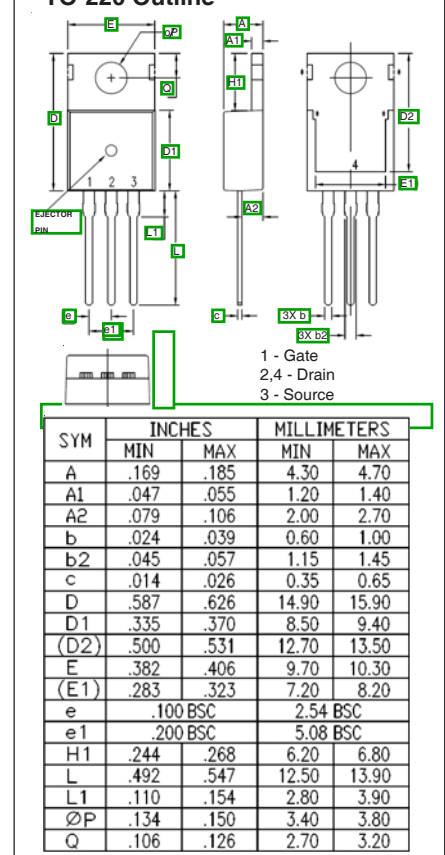
**PRELIMINARY TECHNICAL INFORMATION**

The product presented herein is under development. The Technical Specifications offered are derived from data gathered during objective characterizations of preliminary engineering lots; but also may yet contain some information supplied during a pre-production design evaluation. IXYS reserves the right to change limits, test conditions, and dimensions without notice.

**TO-263 Outline**



**TO-220 Outline**



IXYS reserves the right to change limits, test conditions, and dimensions.

IXYS MOSFETs and IGBTs are covered by one or more of the following U.S. patents:	4,835,592	4,931,844	5,049,961	5,237,481	6,162,665	6,404,065 B1	6,683,344	6,727,585	7,005,734 B2	7,157,338B2
	4,860,072	5,017,508	5,063,307	5,381,025	6,259,123 B1	6,534,343	6,710,405 B2	6,759,692	7,063,975 B2	
	4,881,106	5,034,796	5,187,117	5,486,715	6,306,728 B1	6,583,505	6,710,463	6,771,478 B2	7,071,537	

Fig. 1. Output Characteristics @  $T_J = 25^\circ\text{C}$

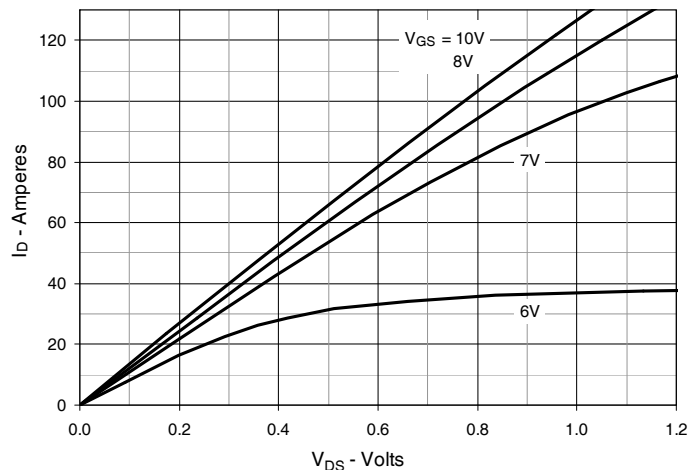


Fig. 2. Extended Output Characteristics @  $T_J = 25^\circ\text{C}$

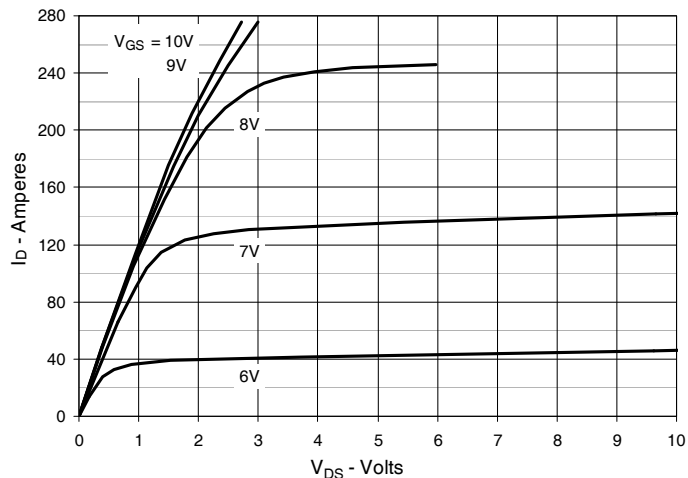


Fig. 3. Output Characteristics @  $T_J = 150^\circ\text{C}$

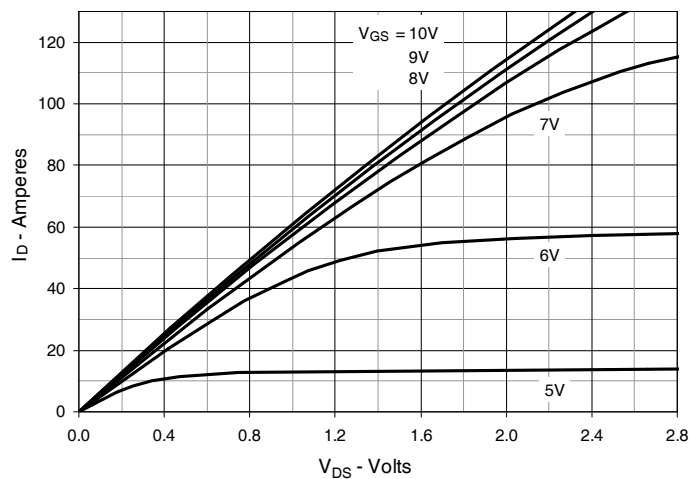


Fig. 4.  $R_{DS(on)}$  Normalized to  $I_D = 65\text{A}$  Value vs. Junction Temperature

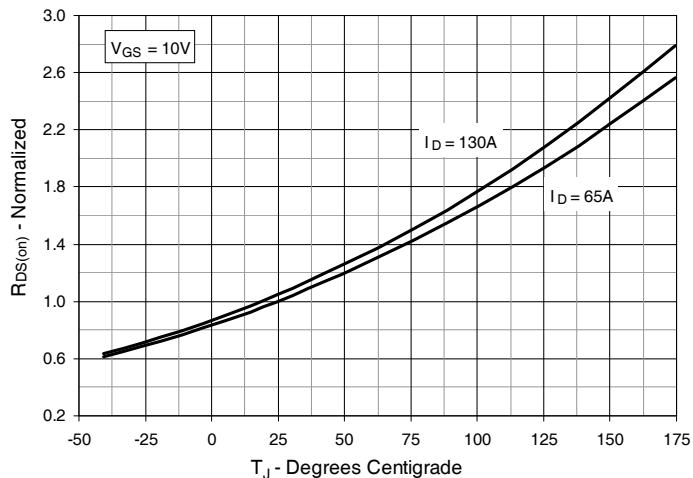


Fig. 5.  $R_{DS(on)}$  Normalized to  $I_D = 65\text{A}$  Value vs. Drain Current

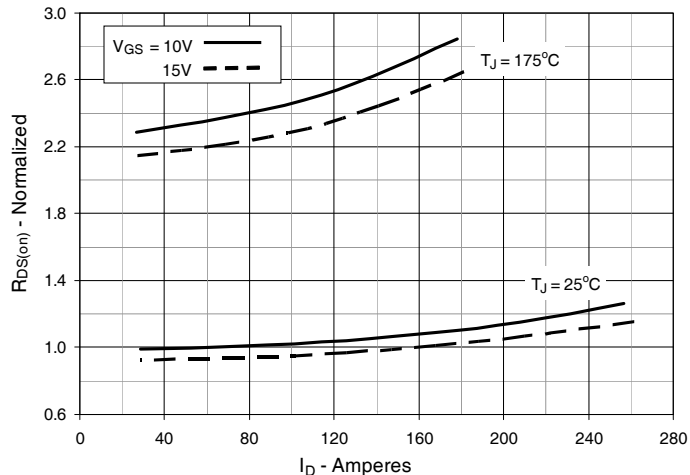


Fig. 6. Drain Current vs. Case Temperature

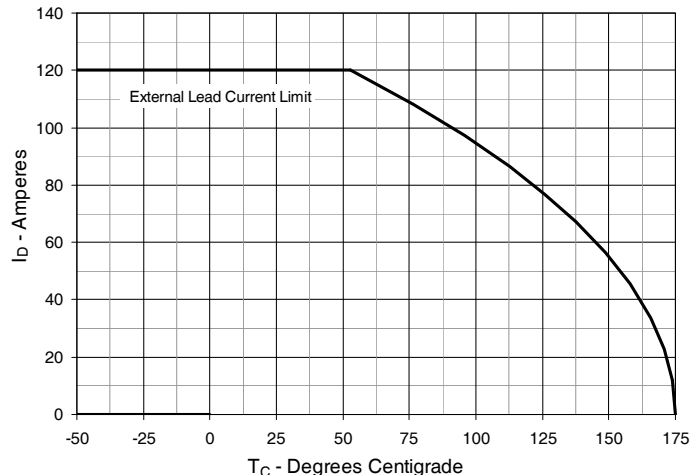


Fig. 7. Input Admittance

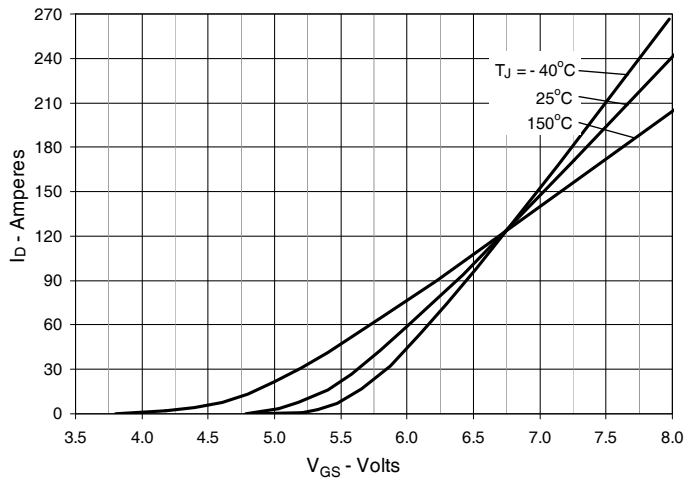


Fig. 8. Transconductance

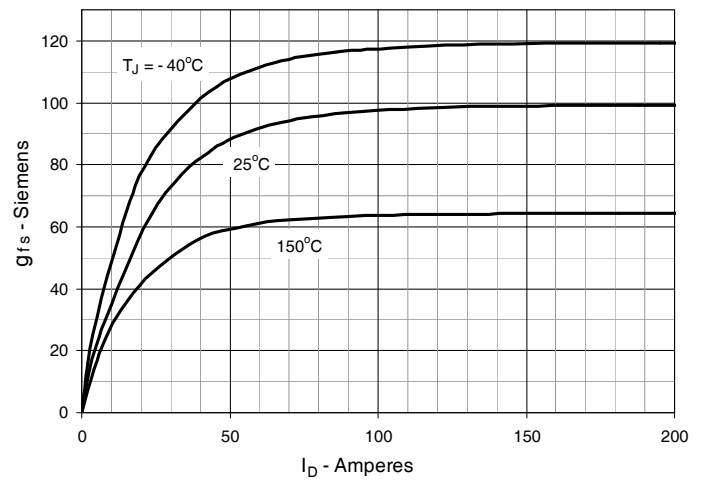


Fig. 9. Forward Voltage Drop of Intrinsic Diode

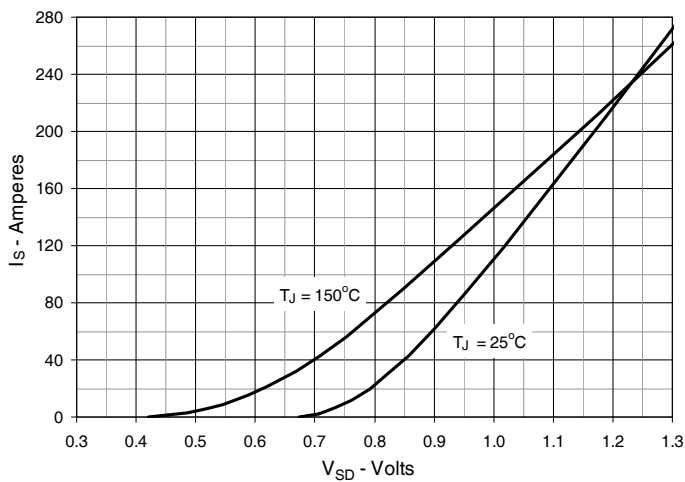


Fig. 10. Gate Charge

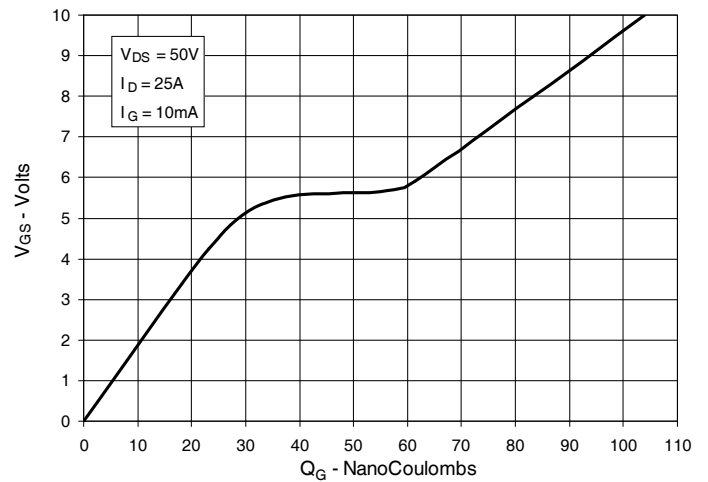


Fig. 11. Capacitance

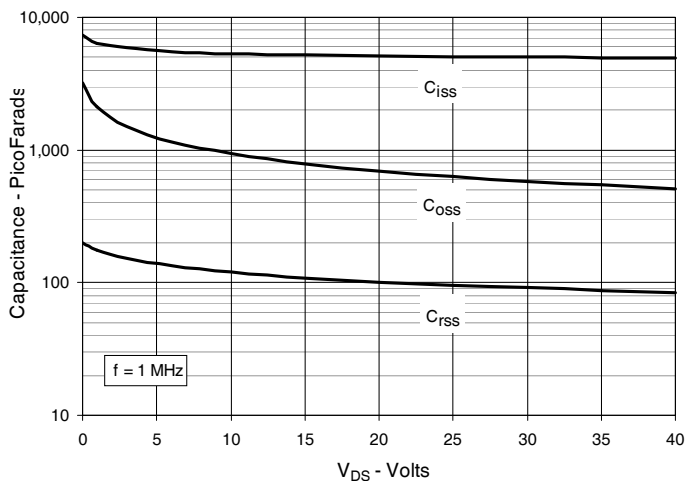
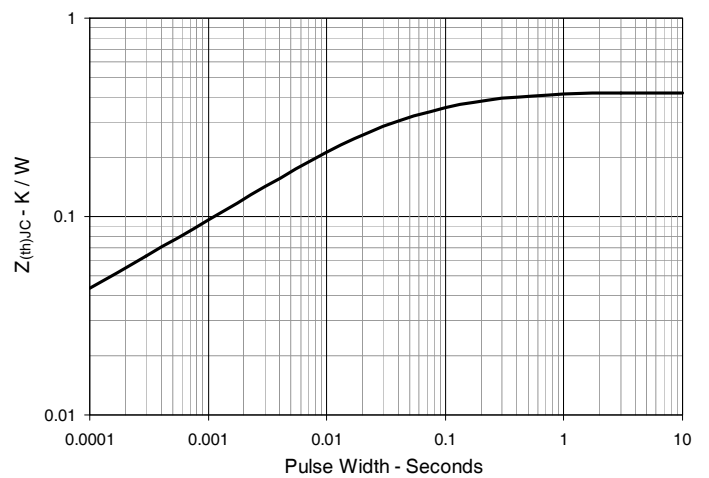
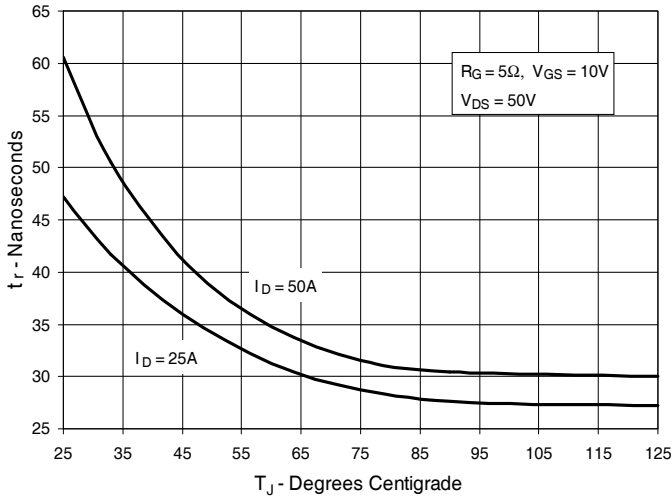


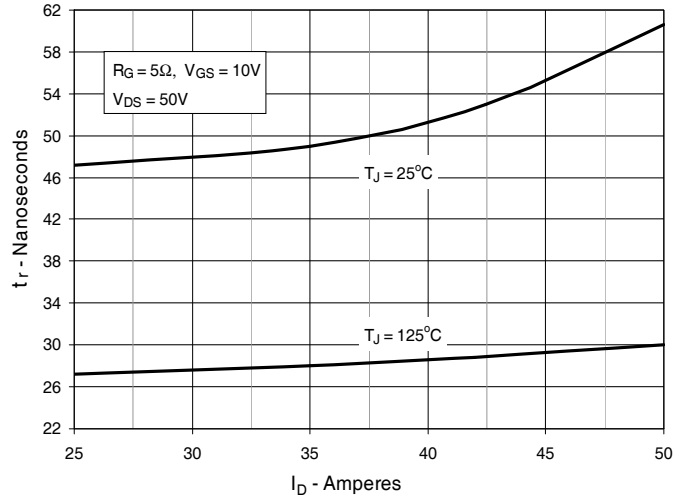
Fig. 12. Maximum Transient Thermal Impedance



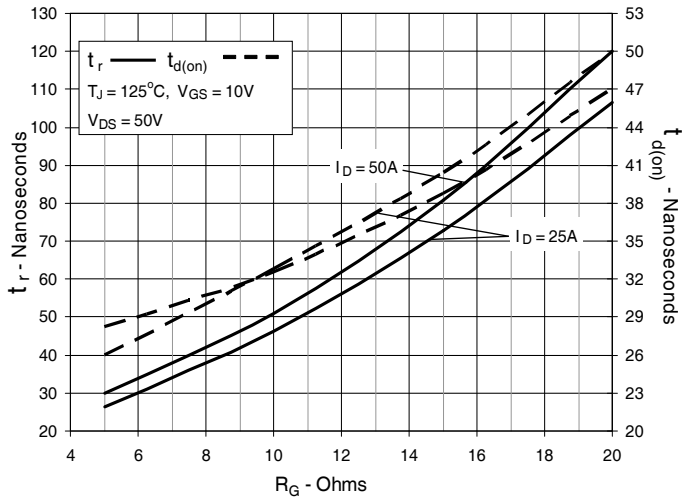
**Fig. 13. Resistive Turn-on**  
Rise Time vs. Junction Temperature



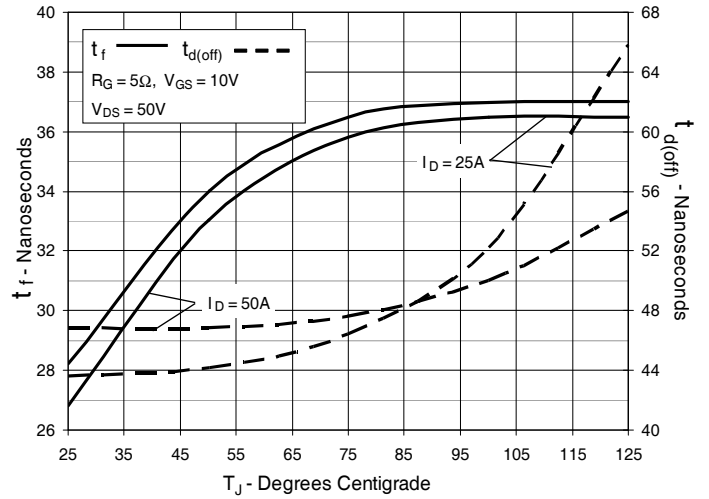
**Fig. 14. Resistive Turn-on**  
Rise Time vs. Drain Current



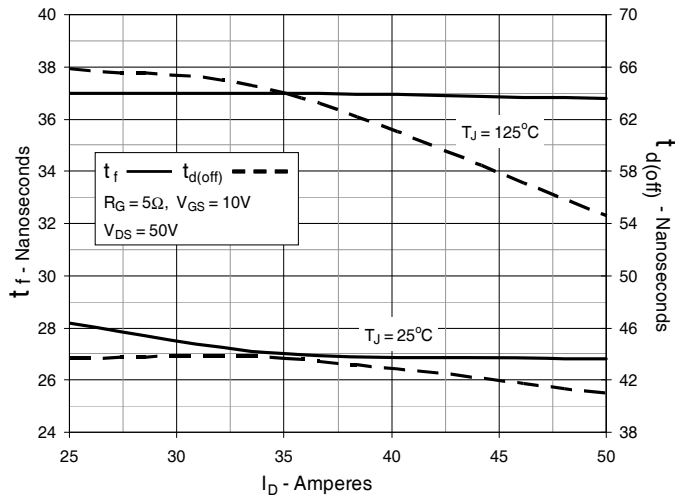
**Fig. 15. Resistive Turn-on**  
Switching Times vs. Gate Resistance



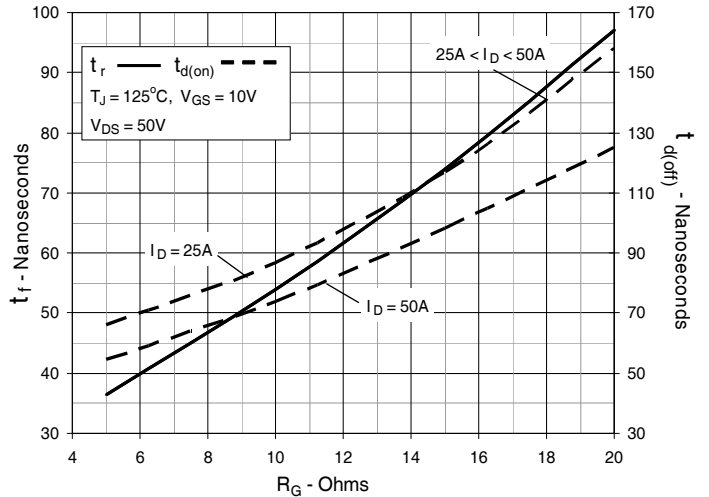
**Fig. 16. Resistive Turn-off**  
Switching Times vs. Junction Temperature



**Fig. 17. Resistive Turn-off**  
Switching Times vs. Drain Current



**Fig. 18. Resistive Turn-off**  
Switching Times vs. Gate Resistance





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