



PJD45N06A-AU

60V N-Channel Enhancement Mode MOSFET

Voltage

60 V

Current

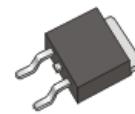
45 A

Features

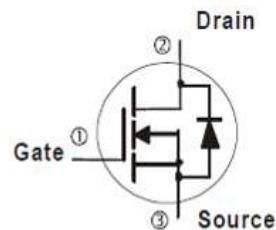
- $R_{DS(ON)}$, $V_{GS} @ 10V$, $I_D @ 20A < 12m\Omega$
- $R_{DS(ON)}$, $V_{GS} @ 4.5V$, $I_D @ 15A < 15m\Omega$
- High switching speed
- Improved dv/dt capability
- Low reverse transfer capacitance
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 Standard

Mechanical Data

- Case : TO-252AA Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0104 ounces, 0.297grams



TO-252AA



Maximum Ratings and Thermal Characteristics ($T_A=25^\circ C$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ^(Note 4)	I_D	45	A
$T_C=100^\circ C$		29	
Pulsed Drain Current ^(Note 1)	I_{DM}	180	
Power Dissipation	P_D	75	W
$T_C=100^\circ C$		37	
Single Pulse Avalanche Energy ^(Note 6)	E_{AS}	61	mJ
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55~175	°C
Typical Thermal Resistance ^(Note 4,5)	Junction to Case	$R_{\theta JC}$	2.0
	Junction to Ambient	$R_{\theta JA}$	110

- Limited only by Maximum Junction Temperature



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Electrical Characteristics ($T_A=25^\circ C$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	60	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1	1.7	2.5	
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=20A$	-	10.5	12	$m\Omega$
		$V_{GS}=4.5V, I_D=15A$	-	12	15	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=60V, V_{GS}=0V$	-	-	1	μA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 100	nA
Dynamic <small>(Note 7)</small>						
Total Gate Charge	Q_g	$V_{DS}=30V, I_D=10A,$ $V_{GS}=10V$ <small>(Note 2,3)</small>	-	39	-	nC
Gate-Source Charge	Q_{gs}		-	6.1	-	
Gate-Drain Charge	Q_{gd}		-	6.7	-	
Input Capacitance	C_{iss}	$V_{DS}=25V, V_{GS}=0V,$ $f=1.0MHz$	-	2256	-	pF
Output Capacitance	C_{oss}		-	145	-	
Reverse Transfer Capacitance	C_{rss}		-	93	-	
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=15V, I_D=10A,$ $V_{GS}=10V, R_G=6\Omega$ <small>(Note 2,3)</small>	-	7.5	-	ns
Turn-On Rise Time	t_r		-	36	-	
Turn-Off Delay Time	$t_{d(off)}$		-	49	-	
Turn-Off Fall Time	t_f		-	12	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_S	---	-	-	45	A
Diode Forward Voltage	V_{SD}	$I_S=1A, V_{GS}=0V$	-	0.67	1	V

NOTES :

1. Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$.
2. Essentially independent of operating temperature typical characteristics.
3. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}=150^\circ C$. Ratings are based on low frequency and duty cycles to keep initial $T_J=25^\circ C$.
4. The maximum current rating is package limited.
5. $R_{Theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
6. The test condition is $L=0.1mH, I_{AS}=35A, V_{DD}=25V, V_{GS}=10V$
7. Guaranteed by design, not subject to production testing.



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TYPICAL CHARACTERISTIC CURVES

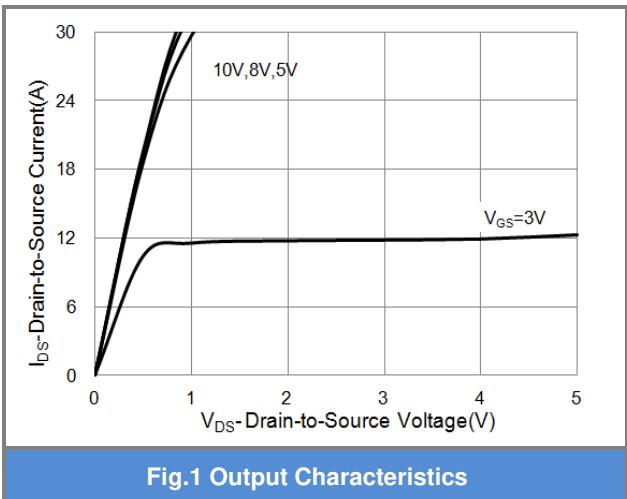


Fig.1 Output Characteristics

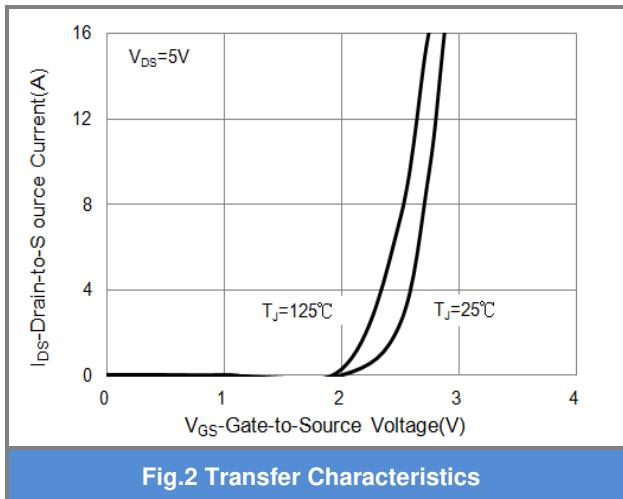


Fig.2 Transfer Characteristics

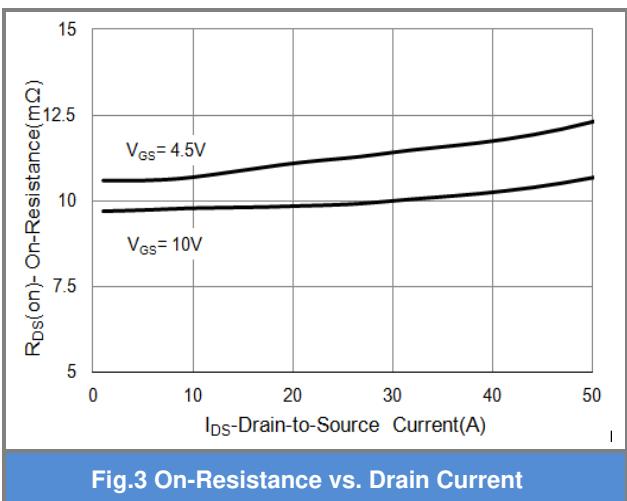


Fig.3 On-Resistance vs. Drain Current

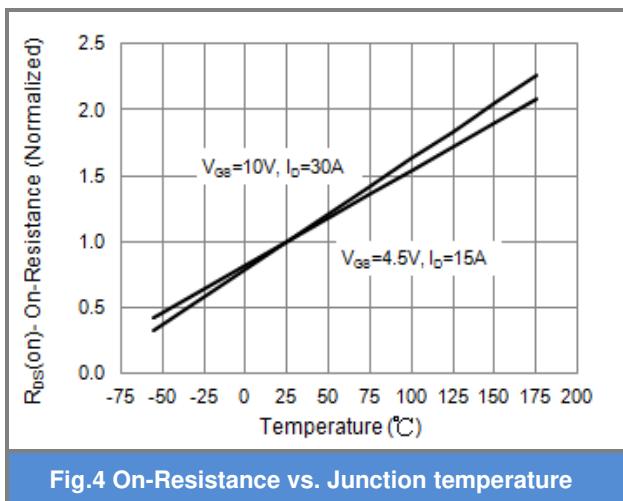


Fig.4 On-Resistance vs. Junction temperature

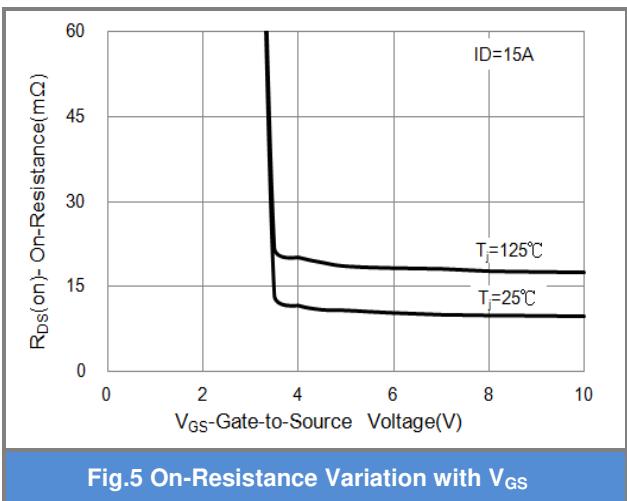


Fig.5 On-Resistance Variation with V_{GS}

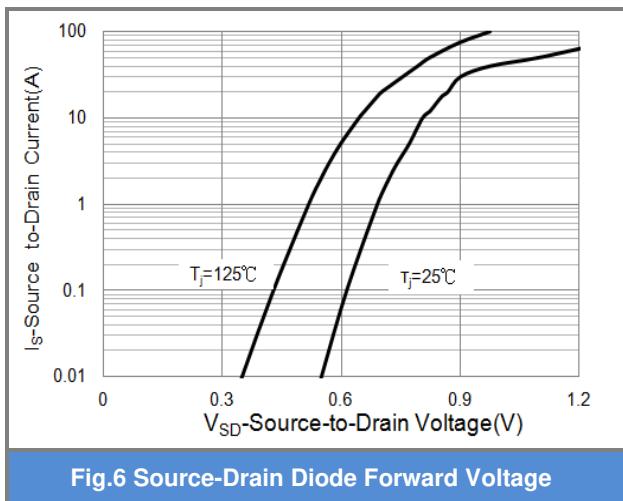
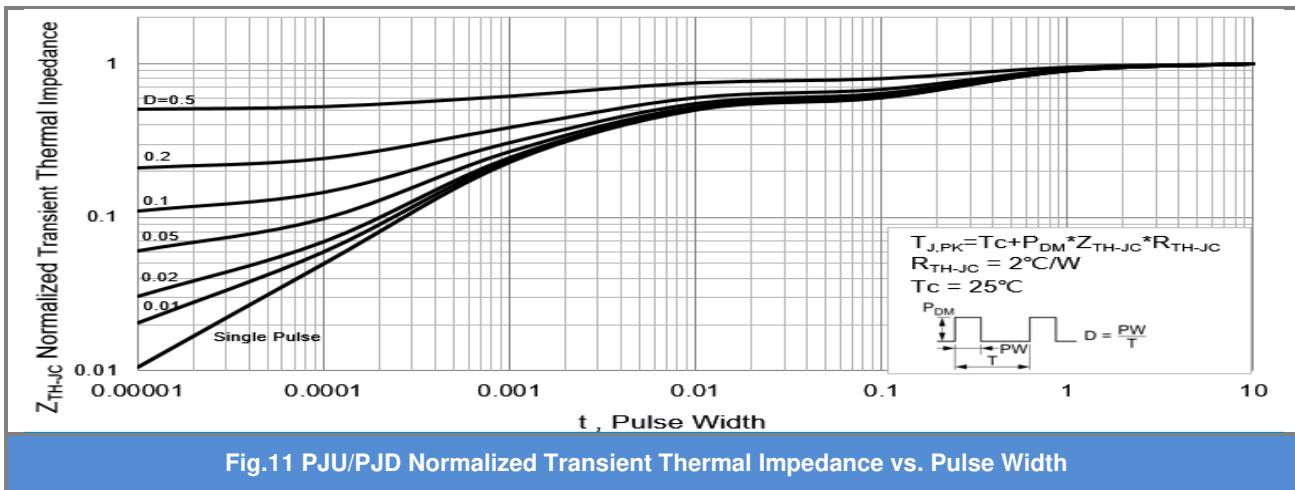
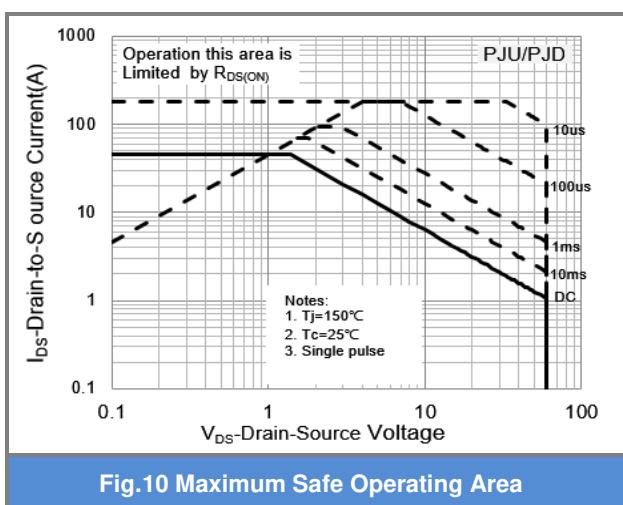
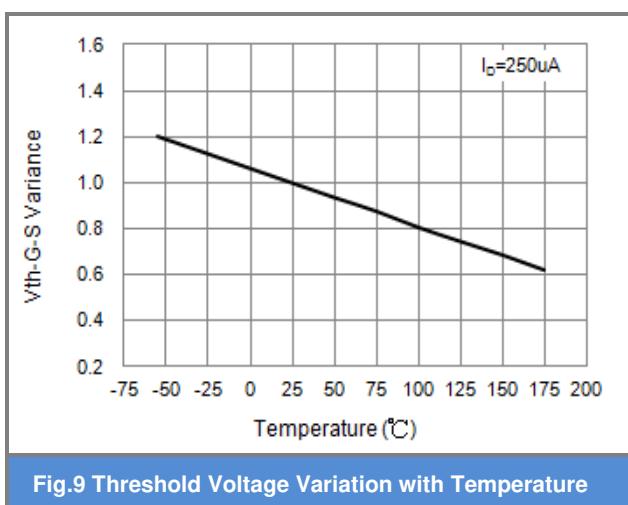
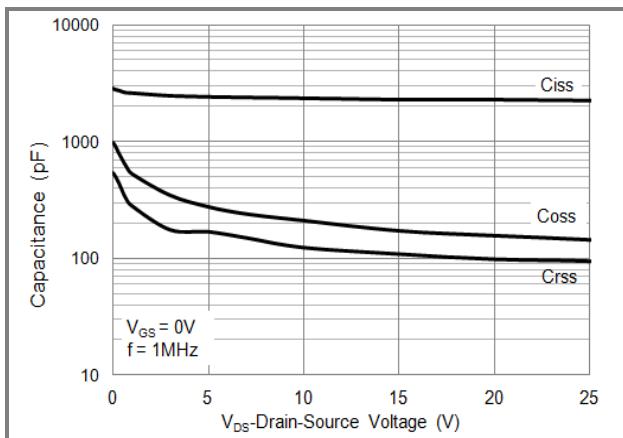
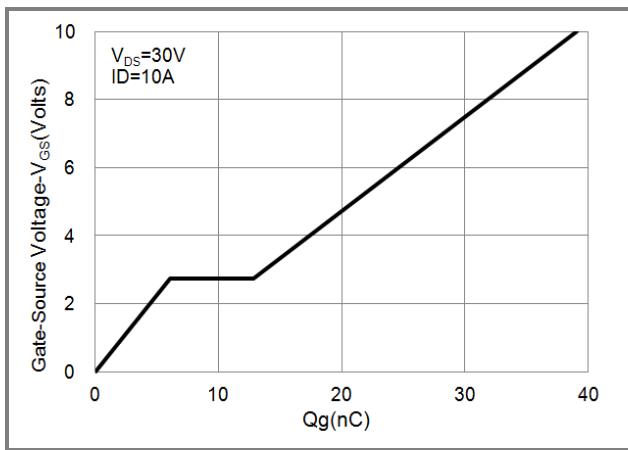


Fig.6 Source-Drain Diode Forward Voltage



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TYPICAL CHARACTERISTIC CURVES



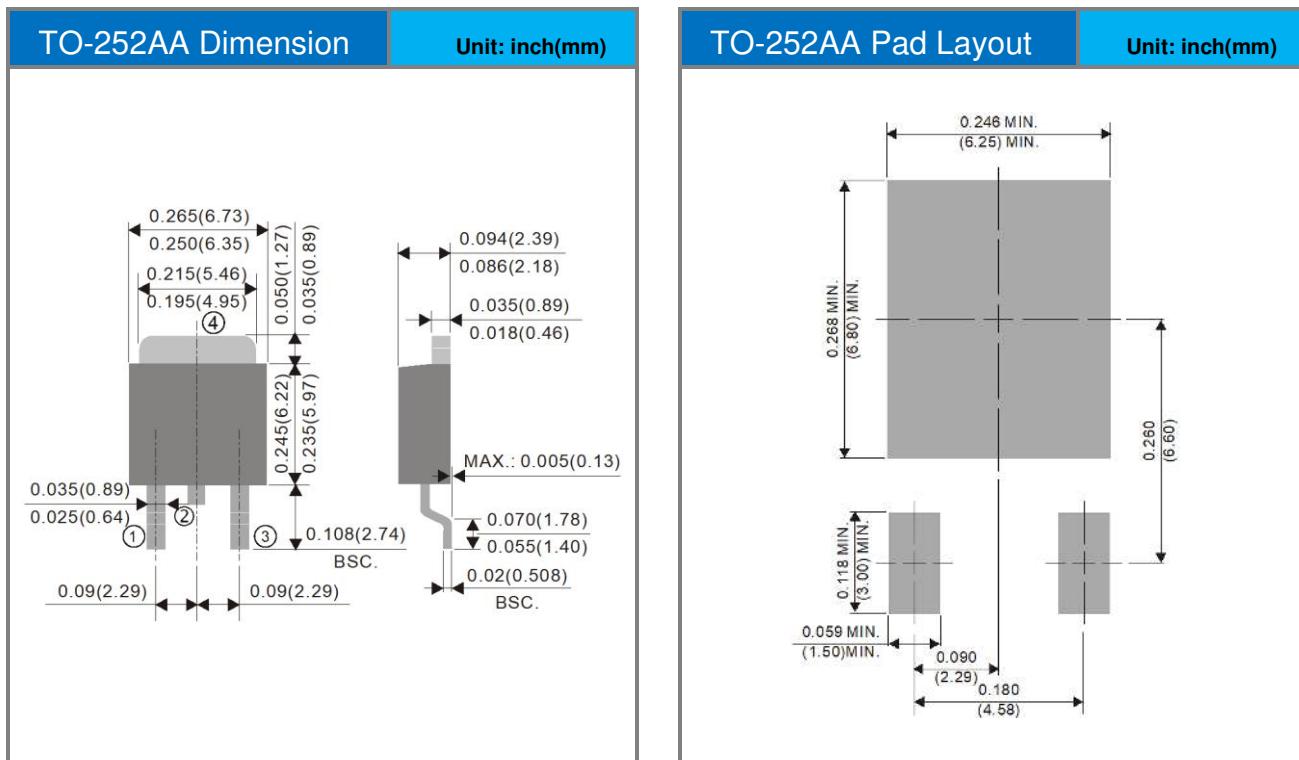


PJD45N06A-AU

Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJD45N06A-AU_L2_000A1	TO-252AA	3,000pcs / 13" reel	D45N06A	Halogen free

Mounting Pad Layout





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