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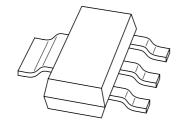
If you have any questions related to the data sheet, please contact our nearest sales office via e-mail or telephone (details via **salesaddresses@nexperia.com**). Thank you for your cooperation and understanding,

Kind regards,

Team Nexperia

# DISCRETE SEMICONDUCTORS

# DATA SHEET



# **PZT4401**NPN switching transistor

Product data sheet 1999 May 10



# **NPN** switching transistor

**PZT4401** 

#### **FEATURES**

- High current (max. 600 mA)
- Low voltage.

#### **APPLICATIONS**

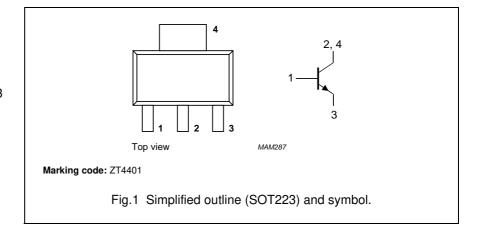
 Switching and linear amplification in industrial and consumer applications.

#### **DESCRIPTION**

NPN switching transistor in a SOT223 plastic package. PNP complement: PZT4403.

#### **PINNING**

PIN	DESCRIPTION
1	base
2, 4	collector
3	emitter



#### **LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	PARAMETER CONDITIONS			
V <sub>CBO</sub>	collector-base voltage	open emitter	-	60	V
V <sub>CEO</sub>	collector-emitter voltage	open base	_	40	V
V <sub>EBO</sub>	emitter-base voltage	open collector	_	6	V
I <sub>C</sub>	collector current (DC)		_	600	mA
I <sub>CM</sub>	peak collector current		_	800	mA
I <sub>BM</sub>	peak base current		_	200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C; note 1	_	1150	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T <sub>amb</sub>	operating ambient temperature		-65	+150	°C

#### Note

1. Device mounted on a printed-circuit board, single-sided copper, tinplated, mounting pad for collector 1 cm<sup>2</sup>. For other mounting conditions, see *"Thermal considerations for SOT223 in the General Part of associated Handbook"*.

# NPN switching transistor

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#### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th j-a</sub>	thermal resistance from junction to ambient	note 1	109	K/W
R <sub>th j-s</sub>	thermal resistance from junction to soldering point		28	K/W

#### Note

1. Device mounted on a printed-circuit board, single-sided copper, tinplated, mounting pad for collector 1 cm<sup>2</sup>. For other mounting conditions, see *"Thermal considerations for SOT223 in the General Part of associated Handbook"*.

#### **CHARACTERISTICS**

 $T_j = 25$  °C unless otherwise specified.

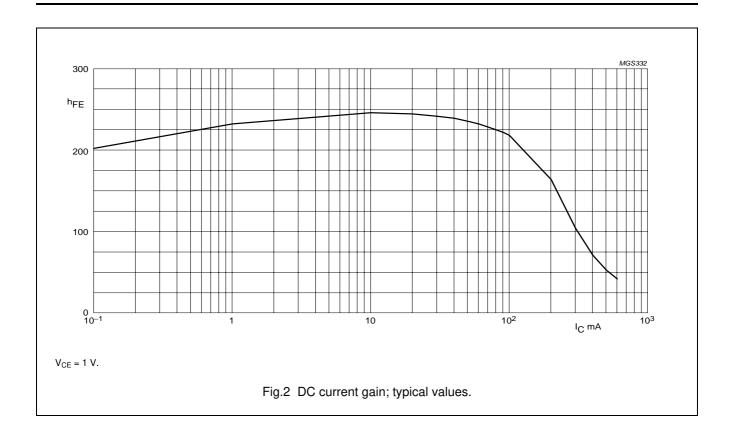
SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I <sub>CBO</sub>	collector cut-off current	I <sub>E</sub> = 0; V <sub>CB</sub> = 60 V	_	50	nA
I <sub>EBO</sub>	emitter cut-off current	I <sub>C</sub> = 0; V <sub>EB</sub> = 6 V	_	50	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 1 V; see Fig.2			
		I <sub>C</sub> = 0.1 mA	20	_	
		I <sub>C</sub> = 1 mA	40	_	
		I <sub>C</sub> = 10 mA	80	_	
		IC = 150 mA; note 1	100	300	
		V <sub>CE</sub> = 2 V; I <sub>C</sub> = 500 mA; note 1	40	_	
V <sub>CEsat</sub>	collector-emitter saturation	I <sub>C</sub> = 150 mA; I <sub>B</sub> = 15 mA; note 1	_	400	mV
	voltage	$I_C = 500 \text{ mA}$ ; $I_B = 50 \text{ mA}$ ; note 1	_	750	mV
V <sub>BEsat</sub>	base-emitter saturation voltage	$I_C = 150 \text{ mA}$ ; $I_B = 15 \text{ mA}$ ; note 1	_	950	mV
		IC = 500 mA; IB = 50 mA; note 1	_	1200	mV
C <sub>c</sub>	collector capacitance	$I_E = i_e = 0$ ; $V_{CB} = 5$ V; $f = 1$ MHz	_	8	pF
C <sub>e</sub>	emitter capacitance	$I_C = i_c = 0$ ; $V_{EB} = 500 \text{ mV}$ ; $f = 1 \text{ MHz}$	_	30	pF
f <sub>T</sub>	transition frequency	$I_C = 20 \text{ mA}; V_{CE} = 10 \text{ V}; f = 100 \text{ MHz}$	250	_	MHz
Switching ti	mes (between 10% and 90% lev	els); see Fig.3			
t <sub>on</sub>	turn-on time	I <sub>Con</sub> = 150 mA; I <sub>Bon</sub> = 15 mA;	_	35	ns
t <sub>d</sub>	delay time	$I_{Boff} = -15 \text{ mA}; V_{BB} = -3.5 \text{ V};$	_	15	ns
t <sub>r</sub>	rise time	V <sub>CC</sub> = 29.5 V	_	20	ns
t <sub>off</sub>	turn-off time		_	250	ns
ts	storage time		_	200	ns
t <sub>f</sub>	fall time		_	60	ns

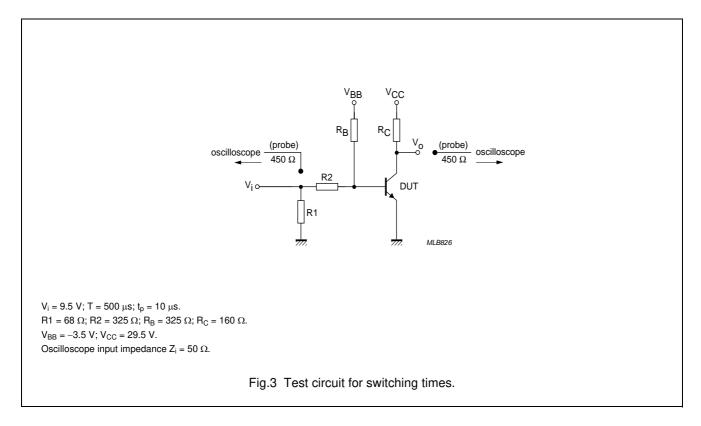
#### Note

1. Pulse test:  $t_p \le 300~\mu s;~\delta \le 0.02.$ 

# NPN switching transistor

PZT4401





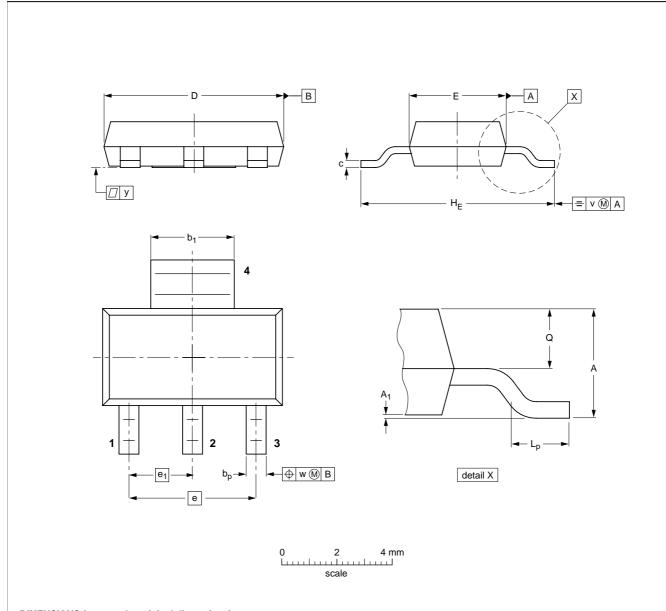
# NPN switching transistor

PZT4401

#### **PACKAGE OUTLINE**

Plastic surface mounted package; collector pad for good heat transfer; 4 leads

**SOT223** 



#### DIMENSIONS (mm are the original dimensions)

UNIT	Α	A <sub>1</sub>	bp	b <sub>1</sub>	С	D	E	е	e <sub>1</sub>	HE	L <sub>p</sub>	Q	v	w	у
mm	1.8 1.5	0.10 0.01	0.80 0.60	3.1 2.9	0.32 0.22	6.7 6.3	3.7 3.3	4.6	2.3	7.3 6.7	1.1 0.7	0.95 0.85	0.2	0.1	0.1

OUTLINE		REFER	EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT223			SC-73			<del>97-02-28</del> 99-09-13

### NPN switching transistor

PZT4401

#### **DATA SHEET STATUS**

DOCUMENT STATUS <sup>(1)</sup>	PRODUCT STATUS <sup>(2)</sup>	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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#### **Customer notification**

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

#### **Contact information**

For additional information please visit: http://www.nxp.com

For sales offices addresses send e-mail to: salesaddresses@nxp.com

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