RB751S40T1

Schottky Barrier Diode

These Schottky barrier diodes are designed for high-speed switching applications, circuit protection, and voltage clamping. Extremely low forward voltage reduces conduction loss. Miniature surface mount package is excellent for hand-held and portable applications where space is limited.

Features

- Extremely Fast Switching Speed
- Extremely Low Forward Voltage 0.28 V (Typ) @ I_F = 1.0 mAdc
- Low Reverse Current
- Lead-Free Plating
- Pb-Free Package is Available

http://onsemi.com

BARRIER DIODE

ON Semiconductor®





SOD-523 CASE 502 PLASTIC

MARKING DIAGRAM



5E = Specific Device Code

M = Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

MAXIMUM RATINGS

Rating	Symbol	Value	Unit	
Peak Reverse Voltage	V _{RM}	40	V	
Reverse Voltage	V _R	30	V	
Forward Continuous Current (DC)	IF	30	mA	
Peak Forward Surge Current	I _{FSM}	500	mA	
ESD Rating: Class 1C per Human Body Model Class A per Machine Model				

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board, (Note 1) T _A = 25°C Derate above 25°C	P _D	200 1.57	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	635	°C/W
Junction and Storage Temperature Range	T _J , T _{stg}	-55 to +150	°C

1

ORDERING INFORMATION

Device	Package	Shipping [†]
RB751S40T1	SOD-523	3000/Tape & Reel
RB751S40T1G	SOD-523 (Pb-Free)	3000/Tape & Reel

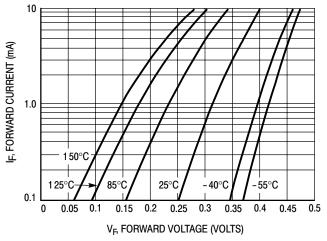
[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

^{1.} FR-5 Minimum Pad.

RB751S40T1

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
Reverse Breakdown Voltage $(I_R = 10 \mu A)$	V _{(BR)R}	30	-	-	V
Total Capacitance (V _R = 1.0 V, f = 1.0 MHz)	СТ	-	2.0	2.5	pF
Reverse Leakage (V _R = 30 V)	I _R	-	300	500	nAdc
Forward Voltage (I _F = 1.0 mAdc)	V _F	-	0.28	0.37	Vdc



1000

(Y 100

T_A = 150°C

125°C

125°C

0.01

0.001

0 5 10 15 20 25 30 35

V_R, REVERSE VOLTAGE (VOLTS)

Figure 1. Typical Forward Voltage

Figure 2. Reverse Current versus Reverse Voltage

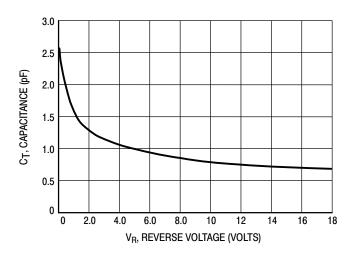
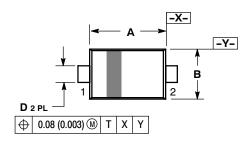


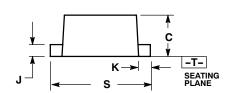
Figure 3. Typical Capacitance

RB751S40T1

PACKAGE DIMENSIONS

SOD-523 CASE 502-01 ISSUE C





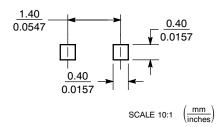
NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M,
 1982
- 2. CONTROLLING DIMENSION: MILLIMETER.
- 2. GONTHOLING BININGTON: MILEUMET LET.

 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH
 THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM
 THICKNESS OF BASE MATERIAL.

	MILLIMETERS			INCHES			
DIM	MIN	NOM	MAX	MIN	NOM	MAX	
Α	1.10	1.20	1.30	0.043	0.047	0.051	
В	0.70	0.80	0.90	0.028	0.032	0.035	
С	0.50	0.60	0.70	0.020	0.024	0.028	
D	0.25	0.30	0.35	0.010	0.012	0.014	
J	0.07	0.14	0.20	0.0028	0.0055	0.0079	
K	0.15	0.20	0.25	0.006	0.008	0.010	
S	1.50	1.60	1.70	0.059	0.063	0.067	

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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