User's Guide SLVU966–October 2013



The Texas Instruments TPS61158EVM-565 evaluation module contains a TPS61158 integrated circuit (IC), helping designers evaluate the operation and performance of the TPS61158, which is a WLED driver providing integrated solutions for single-cell Li-ion battery powered backlight for small and media form factor LCD Display.

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1 Introduction

The EVM contains one DC / DC converter (See Table 1).

Table 1. Device and Package Configurations

Converter	IC	Package		
U1	TPS61158	QFN 2 x 2 6L - DRV		

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Jumper and Connector Setup

1.1 Performance Specification Summary

The EVM is designed to operate from an input voltage source ranging from 2.7 V - 5.5 V, and provides a 30 mA maximum output current for the LED string. For each single channel, there can be 6 to 8 LEDs in series due to customer's application.

Table 2 provides a summary of the TPS61158EVM-565 performance specifications. All specifications are given for an ambient temperature of 25°C.

Specification	CONDITION	MIN	ТҮР	МАХ	UNITS
VIN supply		2.7		5.5	V
Ι _{ουτ}			20		mA
	JP3 shorted		6		
Number of LEDs in series as the load	JP4 shorted		7		
	JP5 shorted		8		

2 Jumper and Connector Setup

This section describes the jumpers and connectors on the EVM and how to properly connect, set up and use the TPS61158EVM-565.

2.1 Input/Output Connector Description

J1,J2 – **Input** are the power input terminals for the converter. The terminal blocks provide a power (VIN) and ground (GND) connection to allow the user to attach the EVM to a cable harness.

J3 – **USB-TO-GPIO** is for the 10-pin ribbon cable that connects the EVM to the USB-TO-GPIO interface box. It is only used when the software is used to EasyScale dimming.

JP1 – Output is the output terminal for the converter. The terminal block provides a connection for LED load and it allows the user to add a current meter between its two pins to measure the output current.

JP2-CTRL is the jumper used to enable the device and do the dimming. Connecting pin1 and pin 2 will toggle the EN high and enable the device. Connecting pin 2 and pin 3 will toggle the EN low and disable the device. A PWM signal or a controlled digital signal to pin 2 can set the device in dimming mode.

JP3, JP4, JP5 – Function has been described in Table 2.

2.2 Hardware Requirements

This EVM requires an external power supply capable of providing 2.7 V to 5.5 V at 0.5 A. To change the default current value (that is, implement dimming), the user can apply either a PWM signal to JP2-pin2 or digital control signal to JP2-pin2.

2.2.1 Normal Operation Wthout Dimming Control

No additional hardware is required.

2.2.2 PWM dimming

A function generator capable of driving the PWM pin with 1.2 V to V_{IN} amplitude and 20-kHz to 100-kHz PWM signal is required for PWM-controlled dimming.

2.2.2.1 One-wire Digital EasyScale

The user also can implement EasyScale dimming by using a digital control signal. The EVM kit includes a PC software compact disk(CD) and USB-TO-GPIO interface box which, when installed on a personal computer (PC) and connected to the EVM, allows the user to communicate with the EVM via a GUI interface. The minimum PC requirements are:

- Windows[®] 2000 or Windows[™] XP operating system
- USB port
- Minimum of 30 MB of free hard disk space (100 MB recommended)
- Minimum of 256 MB of RAM

2.3 Test Setup

The input voltage range for the converter is 2.7 volts to 5.5 volts. A load should be applied to the output terminal for proper operation.

3 Operation

3.1 Non-Dimming Operation (default configuration)

For non-dimming operation of the TPS61158EVM-565, JP1, JP2 should be properly configured. The recommended setting using shorting blocks are shown in Table 3. The configuration for JP3 to JP5 is determined by the specific application.

Table 3. Final Jumper Settings

Reference Designator	Setting on Board		
JP1	Short pin1 and pin2		
JP2	Short pin1 and pin2		

In this default configuration, the device will power up when power is applied.

3.2 PWM Dimming Operation

Remove the jumper on JP2 of default configuration, connect the appropriately configured function generator output between pin 2 and pin 3 (for GND connection) of JP2. The device powers up when VIN power the PWM signal are applied. The recommended PWM signal frequency is from 20 kHz to 100 kHz, and the PWM duty is from 1% to 100%. The regulated output current is directly proportional to the PWM signal duty cycle.

3.3 One-wire Digital EasyScale Dimming

Remove the jumper on JP2, prepare a PC running the TPS61158EVM-565 Controller software and USB-TO-GPIO interface box, and perform the following steps in any sequence:

- Remove the jumper on JP2 of the default configuration.
- Connect one end of the USB-TO-GPIO box to the PC using the USB cable and the other end to J3 of the TPS61158EVM-565 using the supplied 10-pin ribbon cable as shown in the following illustration. The connectors on the ribbon cable are keyed to prevent incorrect installation.
- Connect the power supply between J1 and J2 and turn on the power supply.
- Run the software as explained in the Software Installation and Operation section.



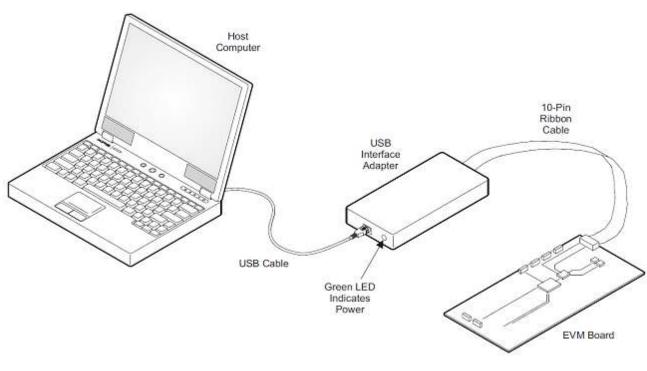


Figure 1. USB Interface Adapter



3.4 Software Installation and Operation

If installing from a CD, insert the CD and run Setup.exe; follow all the prompts to install the software.

If installing from the TI Web site, go to the URL - http://www.ti.com/product/tps61158

Click on the install button; the PC gives a security warning and asks the user to install this application. Select Install to proceed. If a pre-release or Beta version is currently installed the your PC, then uninstall this version of the software before installing the final version from either the CD or the TI Web site.

With both types of installation, the software attempts to install the Microsoft Dot Net Framework 2.0 (if it is not already installed). This framework is required for the software to run. Immediately following installation, the software automatically runs.

To run the software after installation, go to Start \rightarrow all programs \rightarrow Texas Instruments, Inc. \rightarrow TPS61158 Controller EVM Software. At start-up, the software first checks the firmware version of the USB-TO-GPIO adapter box. If an incorrect firmware version is installed, the software automatically searches on the Internet (if connected) for updates. If a new update is available, the software notifies the user of the update, and downloads and installs the software. Note that after the firmware is updated, the user must disconnect and then reconnect the USB cable between the adapter and PC, as instructed during the installation process. The host PC software also automatically searches on the Internet (if connected) for updates. If a new update is available, the software notifies the user of the update, downloads and installs the update.

NOTE: VeriSign[™] Code Signing is used to prevent any malicious code from changing this application. If at any time in the future the binaries are modified, the code will no longer attempt to run.

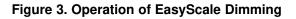
The TPS61158 IC has a 5-bit register that stores the feedback voltage to which the error amplifier will regulate the FB pin. In EasyScale dimming mode, a digital command should be sent to the IC via the CTRL pin to change this register to one of 32 discrete settings; thereby, changing the FB voltage. The software provides a GUI interface in Figure 2 after the software start-up.

File Help	and the second second	TPS61158 EVM	-
00000	0 mV	•	
E	nable IC		
Di	sable IC		
Bit Rate	100 kbps	•	
📄 ACK	1		
	dge Detec	1021	Software Version 1.0.0

Figure 2. GUI Interface of EasyScale Dimming

The user clicks on the "Enable IC" button to enable the IC and enter the EasyScale dimming mode. If the hardware has already been connected and powered on, moving the slider runs the dimming operation. See Figure 3.

Texas Instrum <mark>ents - T</mark> PS(File Help		
10000 62 mV 🔻		
Disable IC		
Bit Rate 100 kbps →		
USB Bridge Version 24.0.0 I	Detected Software Version 1.0.0.0	0





Operation

www.ti.com

The default bit transmission rate is 100 kbps, but the software also supports other bit rate options between 10 kbps to 100 kbps. The user can easily change the bit rate directly by a drop-down box. See a screen shot of the software in Figure 4.

File Help	
10011 80 mV 🔻	 0
Enable IC	
Disable IC	
Bit Rate 10 kbps 👻	
ACK	

Figure 4. Bit Rate Change



3.5 **Test Results**

This section provides typical efficiency for the TPS611158EVM-565 board.

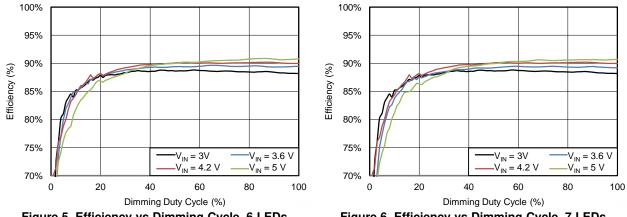
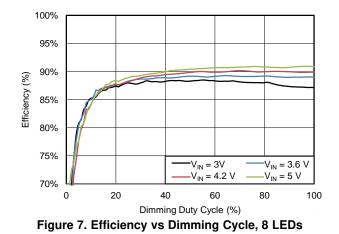




Figure 6. Efficiency vs Dimming Cycle, 7 LEDs





4 Board Layout

Figure 8, Figure 9 and Figure 10 show the board layout for the TPS61158EVM-565. The EVM offers resistors, capacitors and jumpers. Jumpers are provided to configure the device.

The PCB provides 1 oz copper planes on the top and bottom to dissipate heat.

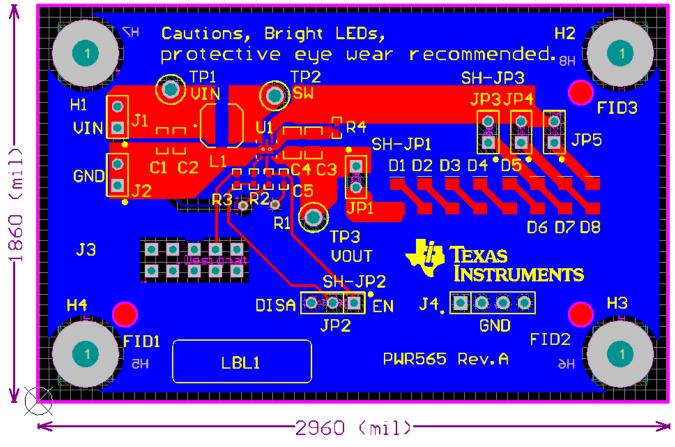


Figure 8. Top Assembly



Board Layout

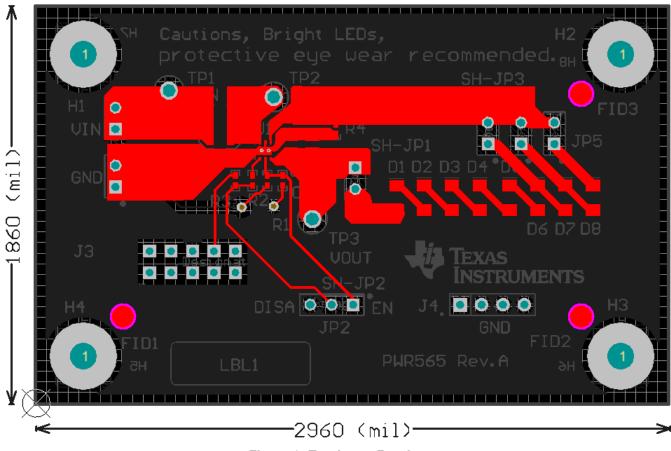


Figure 9. Top Layer Routing



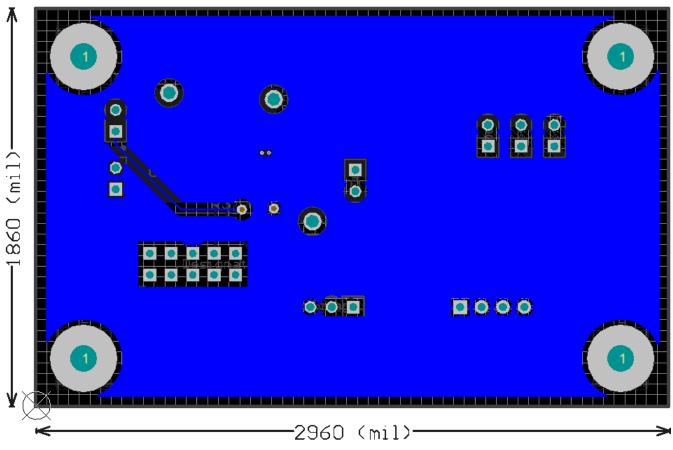


Figure 10. Bottom Layer Routing



Schematics

5 Schematics

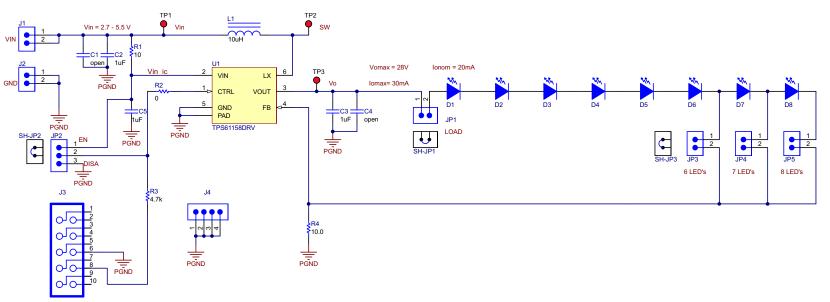


Figure 11. TPS61158EVM-565 Schematic



Bill of Materials

6 Bill of Materials

Ref Des	Qty	Value	Description	Package Reference	Part Number	Manufacturer
J3	1	N2510-6002RB	Connector, Male Straight 2x5 pin, 100mil spacing, 4 Wall	0.338 x 0.788 inch	N2510-6002RB	3М
J4	1		Header, TH, 100mil, 4x1, Gold plated, 230 mil above insulator	4x1 Header	TSW-104-07-G-S	Samtec, Inc.
JP2	1		Header, 100mil, 3x1, Tin plated, TH	Header, 3 PIN, 100mil, Tin	PEC03SAAN	Sullins Connector Solutions
L1	1	10uH	Inductor, Shielded Drum Core, Ferrite, 10uH, 1.25A, 0.2 ohm, SMD	LPS4018	LPS4018-103MLB	Coilcraft
LBL1	1		Thermal Transfer Printable Labels, 0.650" W x 0.200" H - 10,000 per roll	PCB Label 0.650"H x 0.200"W	THT-14-423-10	Brady
R1	1	10	RES, 10 ohm, 5%, 0.063W, 0402	0402	CRCW040210R0JNED	Vishay-Dale
R2	1	0	RES, 0 ohm, 5%, 0.063W, 0402	0402	RC0402JR-070RL	Yageo America
R3	1	4.7k	RES, 4.7k ohm, 5%, 0.063W, 0402	0402	CRCW04024K70JNED	Vishay-Dale
R4	1	10.0	RES, 10.0 ohm, 1%, 0.063W, 0402	0402	CRCW040210R0FKED	Vishay-Dale
SH-JP1, SH-JP2, SH- JP3	3	1x2	Shunt, 100mil, Gold plated, Black	Shunt	969102-0000-DA	ЗМ
TP1, TP2, TP3	3	Red	Test Point, TH, Multipurpose, Red	Keystone5010	5010	Keystone
U1	1		30V WLED Driver with Integrated Power Diode, DRV0006A	DRV0006A	TPS61158DRV	Texas Instruments
C1	1	open	CAP, CERM, 1uF, 10V, +/-10%, X5R, 0603	0603	C1608X5R1A105K	ТDК
C4	1	open	CAP, CERM, 1uF, 50V, +/-10%, X7R, 0805	0805	GRM21BR71H105KA12L	MuRata
PCB	1					Any

7 Related Documentation From Texas Instruments

30V WLED Driver with Integrated Power Diode data sheet (SLVSBR3)

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- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- · Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

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- 2. Use this product only after you obtained the license of Test Radio Station as provided in Radio Law of Japan with respect to this product, or
- 3. Use of this product only after you obtained the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to this product. Also, please do not transfer this product, unless you give the same notice above to the transferee. Please note that if you could not follow the instructions above, you will be subject to penalties of Radio Law of Japan.

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