

3-Terminal 500mA Positive Voltage Regulator

DESCRIPTION

The TS78M00 Series positive voltage regulators are identical to the popular TS7800 Series devices, except that they are specified for only half the output current. Like the TS7800 devices, the TS78M00 Series 3-Terminal regulators are intended for local, on-card voltage regulation. Internal current limiting, thermal shutdown circuitry and safe-area compensation for the internal pass transistor combine to make these devices remarkably rugged under most operating conditions. Maximum output current with adequate heatsink is 500mA

FEATURES

- Output Voltage Range 5V & 12V
- Output current up to 500mA .
- No external components required .
- Internal thermal overload protection
- . Internal short-circuit current limiting
- Output transistor safe-area compensation •
- Output voltage offered in 4% tolerance
- Compliant to RoHS Directive 2011/65/EU and WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21

APPLICATION

- Switching power supply
- Home appliance



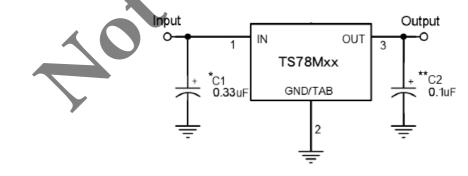




1. Input 2. Ground (Tab)

Notes: MSL 3 (Moisture Sensitivity Level) per J-STD-020

TYPICAL APPLICATION CIRCUIT



A common ground is required between the input and the output voltages. The input voltage must remain typically 2.0V above the output voltage even during the low point on the Input ripple voltage.

XX = these two digits of the type number indicate voltage.

* = Cin is required if regulator is located an appreciable distance from power supply filter.

** = Co is not needed for stability; however, it does improve transient response.



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ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	LIMIT	UNIT				
DC Input Voltage	V _{IN}	35	V				
Power Dissipation	P _D	Internally Limited	W				
Operating Junction Temperature Range	TJ	0 ~ +150	°C				
Storage Temperature Range	T _{STG}	-65~+150	°C				

PARAMETER	SYMBOL	LIMIT	UNIT			
Junction to Case Thermal Resistance	$R_{\Theta JC}$	10	°C/W			
Junction to Ambient Thermal Resistance	$R_{\Theta JA}$	100	°C/W			

Notes: $R_{\Theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistances. The case thermal reference is defined at the solder mounting surface of the drain pins. $R_{\Theta JA}$ is guaranteed by design while $R_{\Theta CA}$ is determined by the user's board design. $R_{\Theta JA}$ shown below for single device operation on FR-4 PCB in still air.

ELECTRICAL SPECIFICATIONS TS78M05

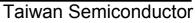
 $(V_{IN}=10V, I_{OUT}=350 \text{ mA}, 0^{\circ}\text{C} \le T_{J} \le 125^{\circ}\text{C}, C_{IN}=0.33 \mu\text{F}, C_{OUT}=0.1 \mu\text{F}, unless otherwise noted})$

PARAMETER	SYMBOL	CONDITION		MIN	ТҮР	MAX	UNIT
		TJ=25°C		4.80	5	5.20	
		7.5V≤V _{IN} ≤ 5mA≤I _{OUT}		4.75	5	5.25	V
Line Degulation	DEO	Teore	7.5V≤V _{IN} ≤25V		3	100	
Line Regulation	REG _{LINE}	Т <i>ј</i> =25°С	8V≤V _{IN} ≤12V		1	50	
	ad Regulation REGLOAD THE		5mA≤I _{OUT} ≤500mA		15	100	mV
Load Regulation		T,=25°C	5mA≤I _{OUT} ≤200mA		5	50	
Quiescent Current	Iq	I _{OUT} =0, T _J =25°C			3	6	
Quiescent Current Change		7.5V≤V _{IN} ≤25V				0.8	mA
	Δl _Q	5mA≤I _{OUT}	≤350mA			0.5	
Output Noise Voltage	V _N	10Hz≤f≤100KHz, TJ=25°C			40		μV
Ripple Rejection Ratio	RR	f=120Hz, 8V≤V _{IN} ≤18V		62	78		dB
Voltage Drop	V _{DROP}	I _{OUT} =500mA, T _J =25°C			2		V
Output Resistance	R _{OUT}	f=1kHz	f=1kHz		17		mΩ
Output Short Circuit Current	I _{OS}	T _J =25°C			50		mA
Peak Output Current	I ₀ peak	TJ=25°C			0.7		Α
Temperature Coefficient of Output Voltage	$\Delta V_{OUT} / \Delta T_J$	I _{OUT} = 5mA, 0°C≤T _J ≤125°C			-0.2		mV/°C

Note:

1. Pulse testing techniques are used to maintain the junction temperature as close to the ambient temperature as possible, and thermal effects must be taken into account separately

2. This specification applies only for DC power dissipation permitted by absolute maximum ratings.



ELECTRICAL SPECIFICATIONS TS78M05							
$(V_{IN}=19V, I_{OUT}=350mA, 0^{\circ}C \le T_{J} \le 125^{\circ}C, C_{IN}=0.33\mu$ F, $C_{OUT}=0.1\mu$ F, unless otherwise noted)							
PARAMETER	SYMBOL	CONDITION		MIN	ТҮР	MAX	UNIT
		T _J =25°C		11.53	12	12.48	
Output voltage	V _{OUT}	14.5V≤V _{IN} ≤27V 5mA≤I _{OUT} ≤350mA		11.42	12	12.60	V
Line Regulation	550	T _J =25°C	14.5V≤V _{IN} ≤30V		10	240	
	REG _{LINE}		15V≤V _{IN} ≤19V		3	120	
			5mA≤I _{OUT} ≤500mA		12	240	mV
Load Regulation		T _J =25°C	5mA≤I _{OUT} ≤200mA		4	120	
Quiescent Current	Ι _Q	I _{OUT} =0, T _J =25°C			3	6	
	Δl _Q	14.5V≤V _{IN} ≤27V		7		0.8	mA
Quiescent Current Change		5mA≤I _{OUT} ≤350mA				0.5	
Output Noise Voltage	V _N	10Hz≤f≤100KHz, TJ=25°C			75		μV
Ripple Rejection Ratio	RR	f=120Hz, 15V≤V _{IN} ≤25V			80		dB
Voltage Drop	V _{DROP}	I _{оит} =500mA, Т _J =25°С			2		V
Output Resistance	R _{OUT}	f=1kHz			18		mΩ
Output Short Circuit Current	I _{os}	Tj=25°C			50		mA
Peak Output Current	I _O peak	TJ=25°C			0.7		А
Temperature Coefficient of Output Voltage	$\Delta V_{OUT} / \Delta T_J$	Ι _{ουτ} = 5mA, 0°C≤Τ _J ≤125°C			-0.3		mV/°C

Note:

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2. This specification applies only for DC power dissipation permitted by absolute maximum ratings.

ORDERING INFORMATION

DUCTOR

OUTPYT VOLTAGE	AGE PART NO. PACKAGE		PACKING	
-5V	TS78M05CP ROG	TO-252 (DPAK)	2,500pcs / 13" Reel	
12V	TS78M12CP ROG	TO-252 (DPAK)	2,500pcs / 13" Reel	





ELECTRICAL CHARACTERISTIC CURVE

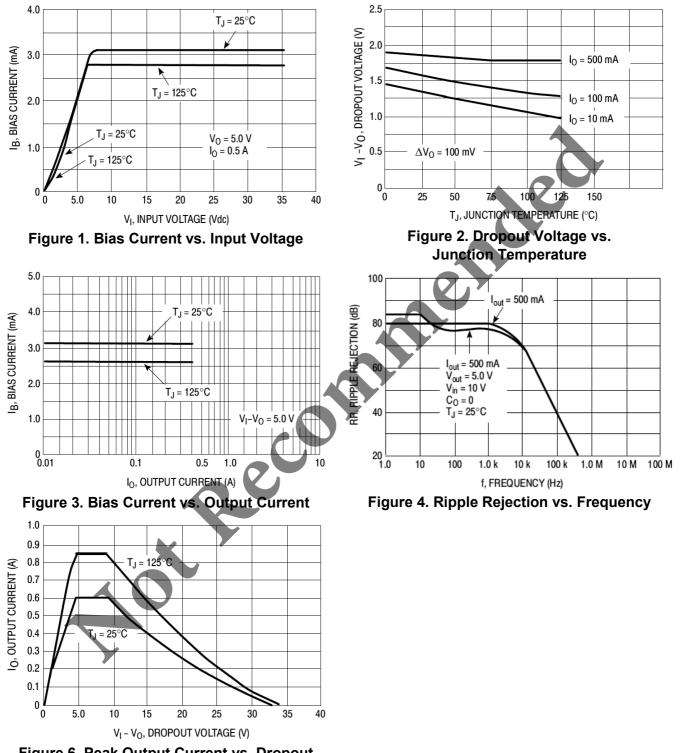
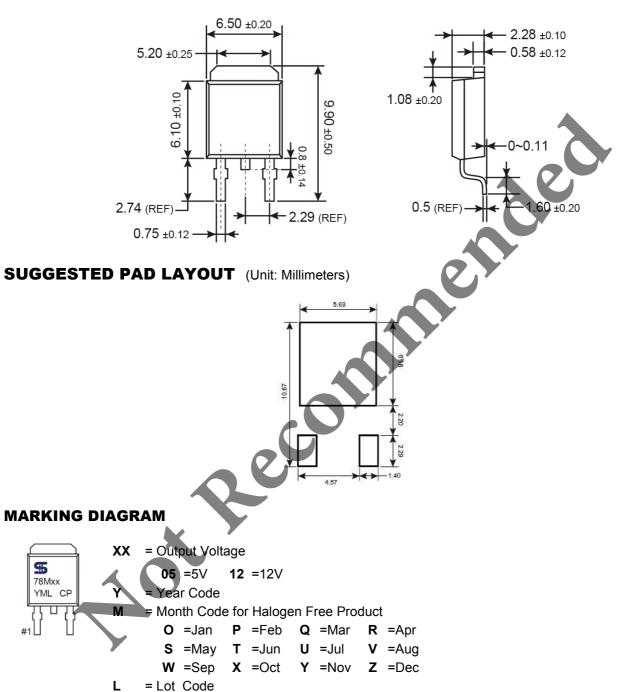


Figure 6. Peak Output Current vs. Dropout Voltage



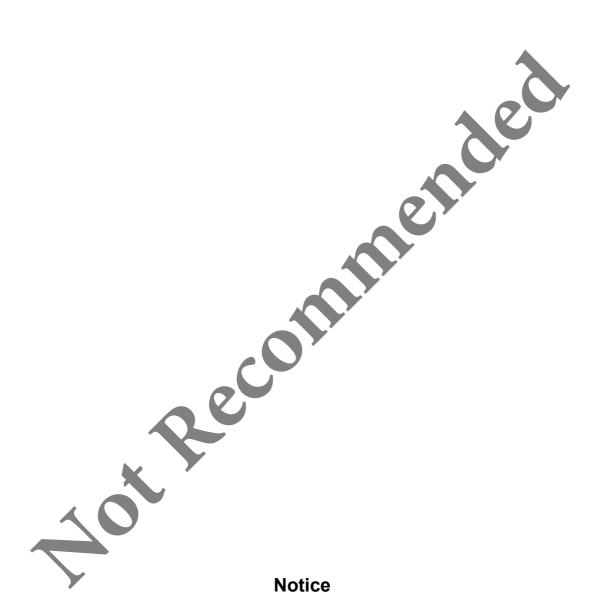
PACKAGE OUTLINE DIMENSIONS (Unit: Millimeters)



TO-252 (DPAK)



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