



20V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(ON)} max	I _D max T _A = +25°C
20V	0.99Ω @ V _{GS} = 4.5V	250mA
	1.2Ω @ V _{GS} = 2.5V	230mA
	1.8Ω @ V _{GS} = 1.8V	180mA
	2.4Ω @ V _{GS} = 1.5V	150mA

Description

This MOSFET is designed to minimize the on-state resistance $(R_{DS(ON)})$ and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- General Purpose Interfacing Switch
- Power Management Functions
- Analog Switch

Features and Benefits

- Low Package Profile, 0.42mm Maximum Package Height
- 0.62mm x 0.62mm Package Footprint
- Low On-Resistance
- Very Low Gate Threshold Voltage, 1.0V Max
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 standards for High Reliability

Mechanical Data

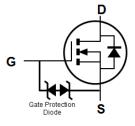
- Case: X2-DFN0606-3
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe Solderable per MIL-STD-202, Method 208 4
- Weight: 0.001 grams (Approximate)

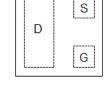
X2-DFN0606-3





Bottom View





Equivalent Circuit

Top View Package Pin Configuration

Ordering Information (Note 4)

Ī	Part Number	Case	Packaging		
	DMN2990UFZ-7B	X2-DFN0606-3	10K/Tape & Reel		

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information

X2-DFN0606-3



Top View Bar Denotes Gate and Source Side 6N = Product Type Marking Code



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V_{DSS}	20	V
Gate-Source Voltage			V _{GSS}	±8	V
Continuous Drain Current (Note 5) V _{GS} = 4.5V	I _D	250 170	mA		
Pulsed Drain Current (Note 6)			I _{DM}	800	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

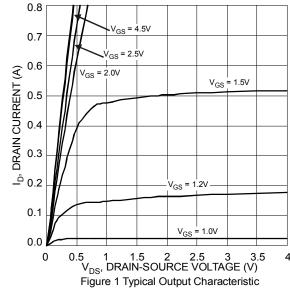
Characteristic	Symbol	Value	Units	
Total Power Dissipation (Note 5)	Steady state	P_{D}	320	mW
Thermal Resistance, Junction to Ambient (Note 5)	Steady state	$R_{\theta JA}$	402	°C/W
Operating and Storage Temperature Range		$T_{J_i} T_{STG}$	-55 to +150	°C

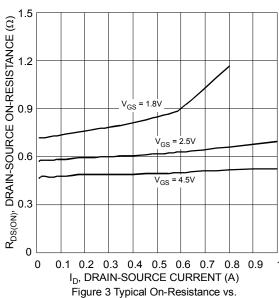
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)								
Drain-Source Breakdown Voltage		BV _{DSS}	20	_	_	V	V _{GS} = 0V, I _D = 250μA	
Zero Gate Voltage Drain Current	@T _C = +25°C	I _{DSS}	_	_	100	nA	V _{DS} = 16V, V _{GS} = 0V	
Gate-Source Leakage		I _{GSS}	_	_	±100	nA	V_{GS} = ±5V, V_{DS} = 0V	
ON CHARACTERISTICS (Note 7)								
Gate Threshold Voltage		V _{GS(th)}	0.4		1.0	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
			_	0.60	0.99		V_{GS} = 4.5V, I_D = 100mA	
				0.75	1.2		V_{GS} = 2.5V, I_{D} = 50mA	
Static Drain-Source On-Resistance	R _{DS(ON}	R _{DS(ON)}	_	0.90	1.8	Ω	V_{GS} = 1.8V, I_{D} = 20mA	
		` ,	_	1.2	2.4		V_{GS} = 1.5V, I_{D} = 10mA	
			_	2.0	_		V_{GS} = 1.2V, I_{D} = 1mA	
Forward Transfer Admittance		Y _{fs}	180	_	_	mS	V _{DS} = 10V, I _D = 400mA	
Diode Forward Voltage		V_{SD}	_	0.6	1.0	V	$V_{GS} = 0V, I_{S} = 150mA$	
DYNAMIC CHARACTERISTICS (Note 8)								
Input Capacitance		Ciss	_	28.2	55.2	pF		
Output Capacitance		Coss	_	4.0	8.0	pF	$V_{DS} = 16V, V_{GS} = 0V,$ f = 1.0MHz	
Reverse Transfer Capacitance		C _{rss}	_	2.8	5.6	pF	1 - 1.000112	
Total Gate Charge		Qg	_	0.5	1.0	nC) / 4 E) /) / 40) /	
Gate-Source Charge		Q _{gs}	_	0.07	0.14	nC	$V_{GS} = 4.5V, V_{DS} = 10V,$ $I_{D} = 250mA$	
Gate-Drain Charge		Q_{gd}	_	0.07	0.14	nC	1D - 20011A	
Turn-On Delay Time		t _{D(on)}	_	3.5	10	ns		
Turn-On Rise Time			_	2.1	10	ns	$V_{DD} = 10V, V_{GS} = 4.5V,$	
Turn-Off Delay Time		t _{D(off)}	_	22	35	ns	$R_L = 47\Omega, R_G = 10\Omega,$ $I_D = 200 \text{mA}$	
Turn-Off Fall Time		t _f	_	7.7	15	ns		

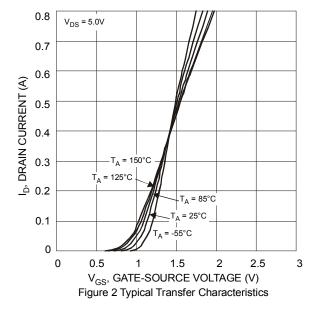
- 5. Device mounted on FR-4 PCB, with minimum recommended pad layout.
 6. Device mounted on minimum recommended pad layout test board, 10µs pulse duty cycle = 1%.
 7. Short duration pulse test used to minimize self-heating effect.
 8. Guaranteed by design. Not subject to product testing.

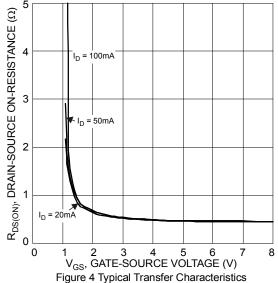




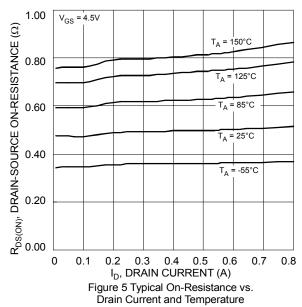


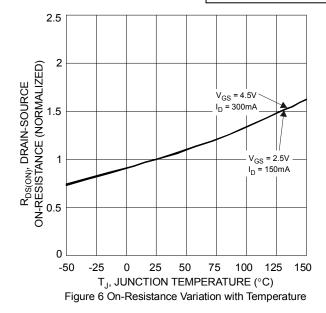
Drain Current and Gate Voltage











1.5 $R_{DS(ON)}$, DRAIN-SOURCE ON-RESISTANCE (Ω) V_{GS} = 4.5V I_D = 300mA V_{GS} = 2.5V I_D = 150mA -50 -25 25 50 75 100 125 150

T_J, JUNCTION TEMPERATURE (°C)

Figure 7 On-Resistance Variation with Temperature

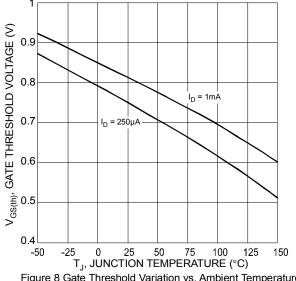
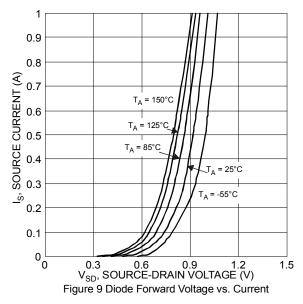
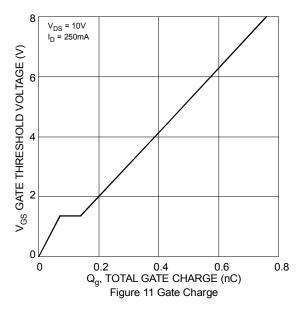
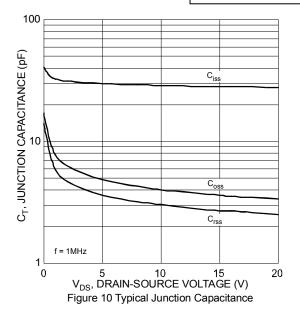


Figure 8 Gate Threshold Variation vs. Ambient Temperature





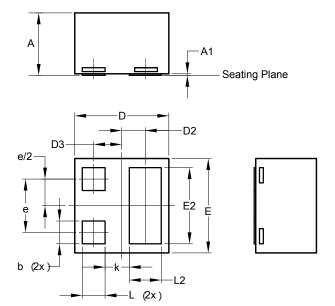






Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

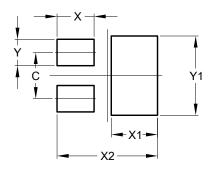


X2-DFN0606-3						
Dim	Min	Max	Тур			
Α	0.36	0.40	0.39			
A1	0	0.05	0.02			
b	0.10	0.20	0.15			
D	0.57	0.57 0.67 0.62				
D2	0.155 BSC					
D3	0	.185 BS	С			
Е	0.57	0.67	0.62			
E2	0.40 0.60 0.50					
е	0.35 BSC					
k	0.16 REF					
L	0.09	0.21	0.15			
L2	0.11	0.31	0.21			
All	All Dimensions in mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

X2-DFN0606-3



Dimensions	Value (in mm)		
С	0.350		
Х	0.280		
X1	0.350		
X2	0.760		
Υ	0.200		
Y1	0.600		



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