



Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
	14mΩ @ V _{GS} = -20V	-10A
-30V	18mΩ @ V _{GS} = -10V	-8.8A
	36mΩ @ V _{GS} = -4.5V	-6.2A

Description

This MOSFET is designed to minimize the on-state resistance $(R_{DS(ON)})$, yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Backlighting
- Power Management Functions
- DC-DC Converters

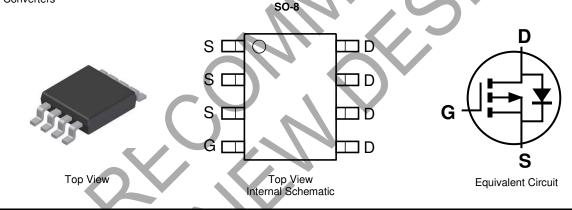
SINGLE P-CHANNEL ENHANCEMENT MODE MOSFET

Features and Benefits

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.074 grams (Approximate)



Ordering Information (Note 4)

Part Number	Case	Packaging	
DMP3035LSS-13	SO-8	2,500/Tape & Reel	

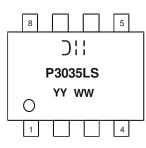
Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



)|| = Manufacturer's Marking P3035LS = Product Type Marking Code YYWW = Date Code Marking YY or YY = Year (ex: 17 = 2017) WW = Week (01 to 53)



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteris		Symbol	Value	Unit	
Drain-Source Voltage			V _{DSS}	-30	V
Gate-Source Voltage			V _{GSS}	±25	V
Drain Current (Note 5) (V _{GS} = -20V)	Steady State	T _A = +25°C T _A = +70°C	I _D	-10 -8	А
Pulsed Drain Current (Note 6)			I _{DM}	-80	А

Thermal Characteristics

Characteristic	Symbol	Value	Unit			
Total Power Dissipation (Note 5)	PD	2.0	W			
Thermal Resistance, Junction to Ambient	R _{eja}	60	°C/W			
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C			

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

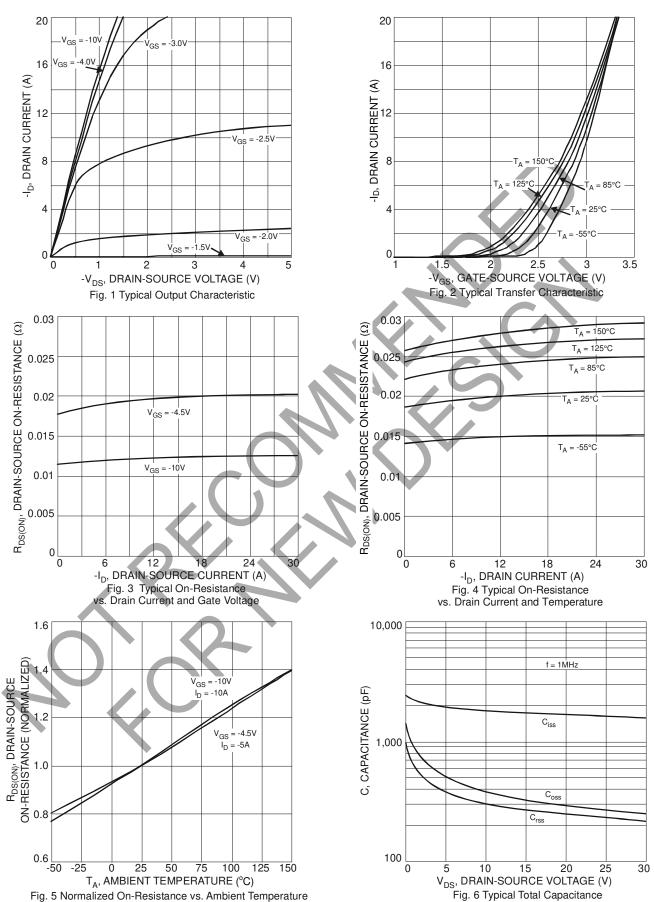
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	-30		-	V	$V_{GS} = 0V, I_D = -250 \mu A$
Zero Gate Voltage Drain Current	I _{DSS}	+		-1	μA	$V_{DS} = -30V, V_{GS} = 0V$
Gate-Source Leakage	IGSS	T	_	±100 ±800	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$ $V_{GS} = \pm 25V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						·
Gate Threshold Voltage	V _{GS(TH)}	-1 (—	-2	V	$V_{DS} = V_{GS}, I_D = -250 \mu A$
Static Drain-Source On-Resistance	Rds(on)		11 15 27	14 18 36	mΩ	
Forward Transconductance	G _{fs}		12	—	S	$V_{DS} = -10V, I_D = -12A$
Diode Forward Voltage (Note 7)	V _{SD}	-0.5	_	-1.1	V	$V_{GS} = 0V, I_S = -2A$
DYNAMIC CHARACTERISTICS						
Input Capacitance	Ciss	—	1,655	—	pF	V _{DS} = -20V, V _{GS} = 0V f = 1.0MHz
Output Capacitance	C _{oss}		286	—	pF	
Reverse Transfer Capacitance	Crss		240	—	pF	
Gate Resistance	R _G	_	2.3	_	Ω	$V_{GS} = 0V, V_{DS} = 0V, f = 1MHz$
SWITCHING CHARACTERISTICS	~			_		
Total Gate Charge	Qg	_	15.3 30.7	—		
Gate-Source Charge	Q _{gs}		3.5	_	nC	$V_{DS} = -15V, V_{GS} = -10V, I_D = -8A$
Gate-Drain Charge	Q _{gd}		7.9	_		$V_{DS} = -15V, V_{GS} = -10V, I_D = -8A$
Turn-On Delay Time	t _{D(ON)}	_	5.1	_		
Rise Time	t _R	—	8	_		$V_{GS} = -10V, V_{DS} = -15V,$
Turn-Off Delay Time	t _{D(OFF)}		46	_	ns	$R_D = 15\Omega, R_G = 6\Omega$
Fall Time	tF		30	_		

5. Device mounted on 1 inch² FR-4 board with 2 oz. copper, in a still-air environment with $T_A = +25^{\circ}C$. 6. Repetitive rating, pulse width limited by junction temperature. 7. Short duration pulse test used to minimize self-heating effect. Notes:



NOT RECOMMENDED FOR NEW DESIGN USE <u>DMP3036SSS</u>

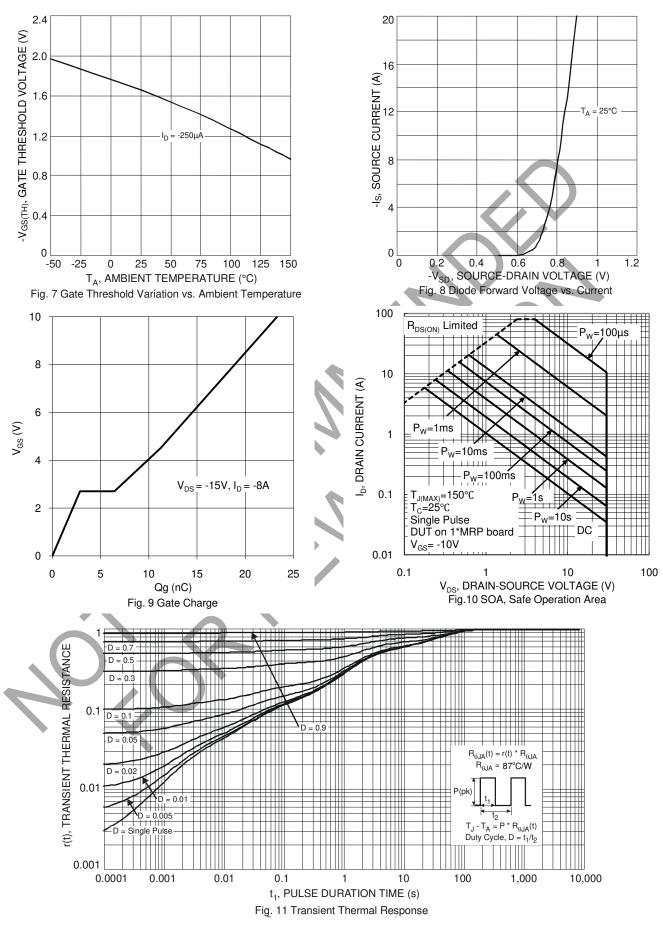
DMP3035LSS





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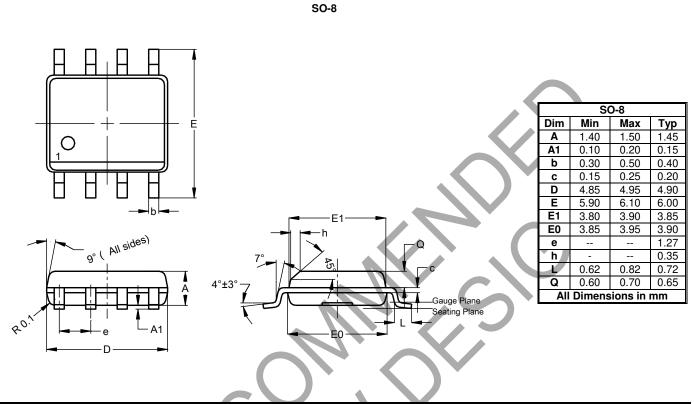
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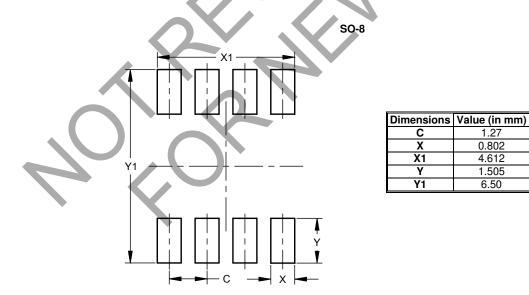
Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.





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