

# 6th Generation CoolSiC™

### 650V SiC Schottky Diode

The CoolSiC<sup>™</sup> generation 6 (G6) is the leading edge technology from Infineon for the SiC Schottky barrier diodes. The Infineon proprietary innovative G5 technology was enhanced in G6 by introducing further advancements like a novel Schottky metal system. The result is a family of products with improved efficiency over all load conditions, resulting from a lower figure of merit (Q<sub>c</sub> x V<sub>f</sub>). The CoolSiC<sup>™</sup> Schottky diode 650 V G6 has been designed to complement our 600 V and 650 V CoolMOS™ 7 families, meeting the most stringent application requirements in this voltage range.

Table 1 **Key performance parameters** 

Parameter	Value	Unit						
$V_{RRM}$	650	V						
$\overline{Q_C (V_R = 400 \text{ V})}$	26.8	nC						
$E_C (V_R = 400 \text{ V})$	5.3	μЈ						
$I_F (T_C \le 135  ^{\circ}\text{C}, D = 1)$	20	А						
$V_F (I_F = 20 \text{ A}, T_j = 25 \text{ °C})$	1.25	V						

Table 2 **Package information** 

Type / ordering Code	Package	Marking		
IDH20G65C6	PG-TO220-2	D2065C6		

# PG-TO220-2 **CASE** 1) Cathode 2) Anode o CASE

### **Features**

- Best in class forward voltage (1.25 V)
- Best in class figure of merit ( $Q_c \times V_F$ )
- High dv/dt ruggedness (150 V/ns)

### **Benefits**

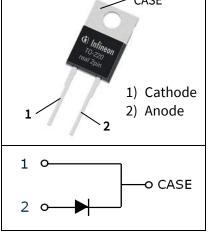
- System efficiency improvement
- System cost and size savings due to the reduced cooling requirements
- Enabling higher frequency and increased power density

### **Potential Applications**

- Power factor correction in SMPS
- Solar inverter
- Uninterruptible power supply

### **Product Validation**

Qualified for industrial applications according to the relevant tests of JEDEC (J-STD20 and JESD22)









# 6<sup>th</sup> Generation CoolSiC<sup>™</sup>

### IDH20G65C6



### **Table of Content**

1	Maximum ratings	3
2		
3	Electrical characteristics	
3.1	Static characteristics	
3.2	AC characteristics	
4	Diagrams	5
5	Simplified forward characteristic	
6	Package outlines	



## 1 Maximum ratings

Table 3 Maximum ratings

Damamatan	Complete	Values			l lmit		
Parameter	Symbol	Min.	Тур.	Max.	Unit	Note/Test condition	
	I <sub>F</sub>	-	_	20		$T_C \le 135 ^{\circ}\text{C}, D = 1$	
Continuous forward current		-	_	22		$T_C \le 125  ^{\circ}\text{C}, D = 1$	
		-	_	41		$T_C \le 25 ^{\circ}\text{C}, D = 1$	
Surge-repetitive forward current, sine halfwave <sup>1</sup>	$I_{F,RM}$	-	_	87	A	$T_C = 25  ^{\circ}\text{C}, t_p = 10  \text{ms}$	
Surge non-repetitive forward		-	_	99		$T_C = 25 ^{\circ}\text{C}, t_p = 10 \text{ms}$	
current, sine halfwave	$I_{F,SM}$	-	_	79		$T_C = 150  ^{\circ}\text{C}, t_p = 10  \text{ms}$	
Non-repetitive peak forward current	I <sub>F,max</sub>	-	-	780		$T_C = 25  ^{\circ}\text{C},  t_p = 10  \mu\text{s}$	
:24	( :2d+	_	_	49	۸2-	$T_C = 25 ^{\circ}\text{C}, t_p = 10 \text{ms}$	
i <sup>2</sup> t value	∫i²dt	_	_	31	A <sup>2</sup> s	$T_C = 150 ^{\circ}\text{C}, t_P = 10 \text{ms}$	
Repetitive peak reverse voltage	$V_{RRM}$	_	_	650	٧	<i>T<sub>C</sub></i> = 25 °C	
Diode dv/dt ruggedness	dv/dt	_	_	150	V/ns	V <sub>R</sub> = 0480 V	
Power dissipation	P <sub>tot</sub>	_	_	108	W	$T_C = 25$ °C, $R_{thJC,max}$	
Operating and storage temperature	$T_j$ $T_{stg}$	-55	-	175	°C	-	
Mounting torque	_	_	_	70	Ncm	M3 screw	

### 2 Thermal characteristics

Table 4 Thermal characteristics (PG-TO-220-2)

Doromotor	Cymahal		Values		l lm!s	Note/Test condition	
Parameter	Symbol	Min.	Тур.	Max.	Unit		
Thermal resistance, junction- case	$R_{thJC}$	-	0.8	1.3	12 /\\	-	
Thermal resistance, junctionambient	$R_{thJA}$	_	-	62	K/W	leaded	
Soldering temperature, wavesoldering only allowed at leads	$T_{sold}$	_	_	260	°C	1.6 mm (0.063 in.) from case for 10 s	

Final Datasheet 3 Rev. 2.0, 2017-05-23

<sup>&</sup>lt;sup>1</sup> The surge-repetitive forward current test was performed with 1000 pulses (half-wave rectified sine with the 10 ms period).





#### **Electrical characteristics** 3

#### 3.1 **Static characteristics**

Table 5 **Static characteristics** 

Dava marka m	Symbol	Values			11	Nata/Task sandiki sa	
Parameter		Min.	Тур.	Max.	Unit	Note/Test condition	
DC blocking voltage	$V_{DC}$	650	_	_		<i>T<sub>j</sub></i> = 25 °C	
Diode forward voltage	$V_F$	_	1.25	1.35	V	$I_F = 20 \text{ A}, T_j = 25 \text{ °C}$	
		_	1.5	_		$I_F = 20 \text{ A}, T_j = 150 \text{ °C}$	
Reverse current	$I_R$	_	2.0	67		$V_R = 420 \text{ V}, T_j = 25 ^{\circ}\text{C}$	
		_	66	_	μΑ	$V_R$ = 420 V, $T_j$ = 125 °C	
		_	153	_		$V_R$ = 420 V, $T_j$ = 150 °C	

#### **AC characteristics** 3.2

Table 6 **AC characteristics** 

Devementer	Cumbal	Values			I I mit	Note /Took Consider	
Parameter	Symbol	Min.	Typ.	Max.	Unit	Note/Test Condition	
Total capacitive charge	$Q_c$	_	26.8	-	nC	$V_R$ = 400 V, $T_j$ = 150 °C, di/dt = 200 A/ $\mu$ s, $I_F \le I_{F,MAX}$	
Total capacitance		_	970	-		$V_R = 1 \text{ V, } f = 1 \text{ MHz,}$ $T_j = 25 \text{ °C}$	
	С	_	57	-	pF	$V_R = 300 \text{ V}, f = 1 \text{ MHz},$ $T_j = 25 \text{ °C}$	
		_	55	-		$V_R$ = 600 V, $f$ = 1 MHz, $T_j$ = 25 °C	



### 4 Diagrams

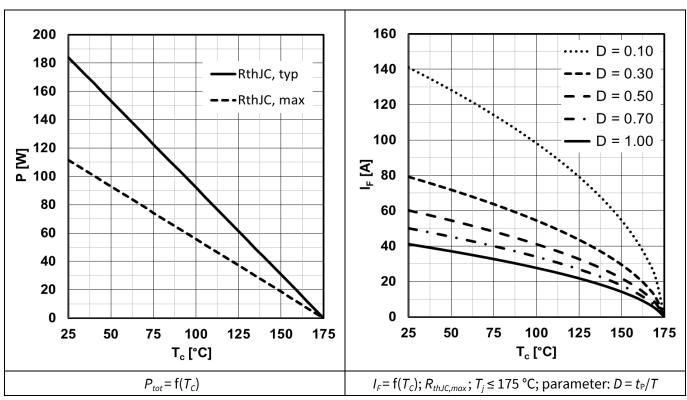


Figure 1 Power dissipation

Figure 2 Max. forward current

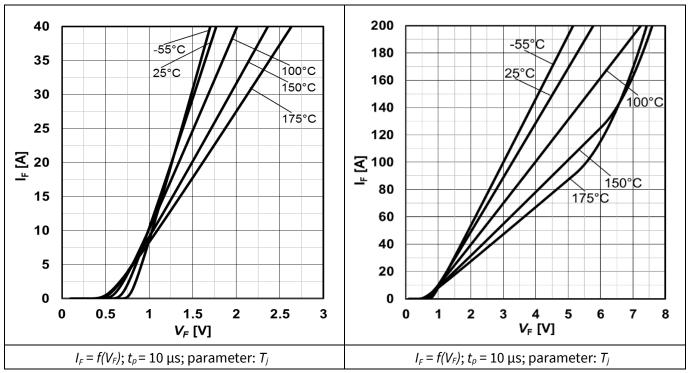


Figure 3 Typ. forward characteristics

Figure 4 Typ. forward characteristics in surge current



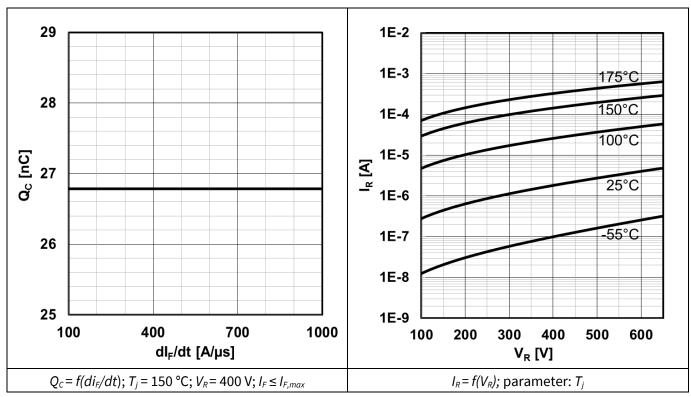


Figure 5 Typ. cap. charge vs. current slope

Figure 6 Typ. reverse current vs. reverse voltage

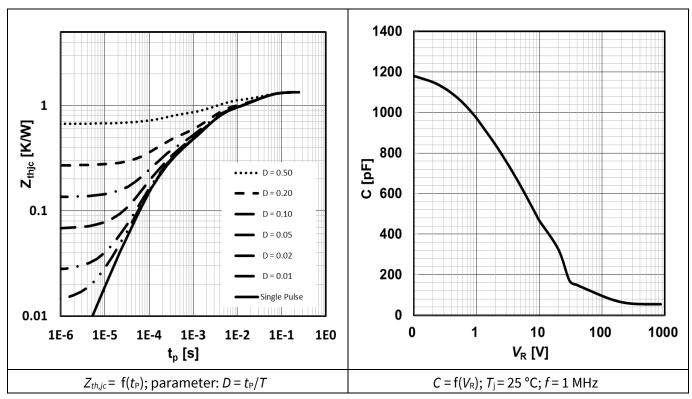


Figure 7 Max. transient thermal impedance

Figure 8 Typ. capacitance vs. reverse voltage



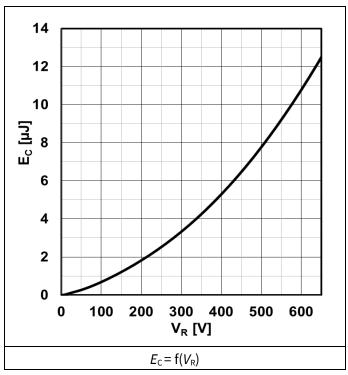


Figure 9 Typ. capacitance stored energy

### 5 Simplified forward characteristic

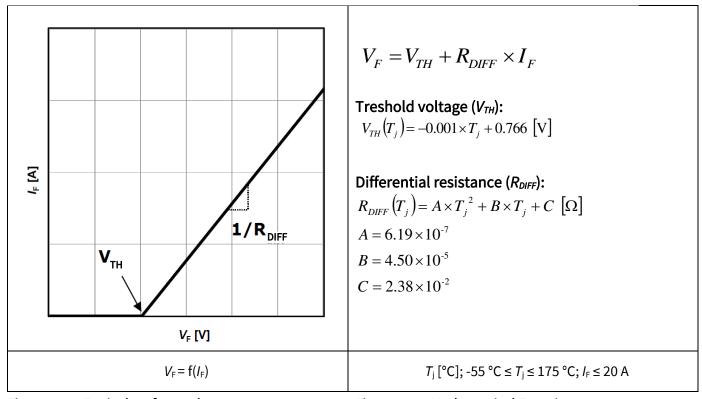


Figure 10 Equivalent forward current curve

Figure 11 Mathematical Equation



### 6 Package outlines

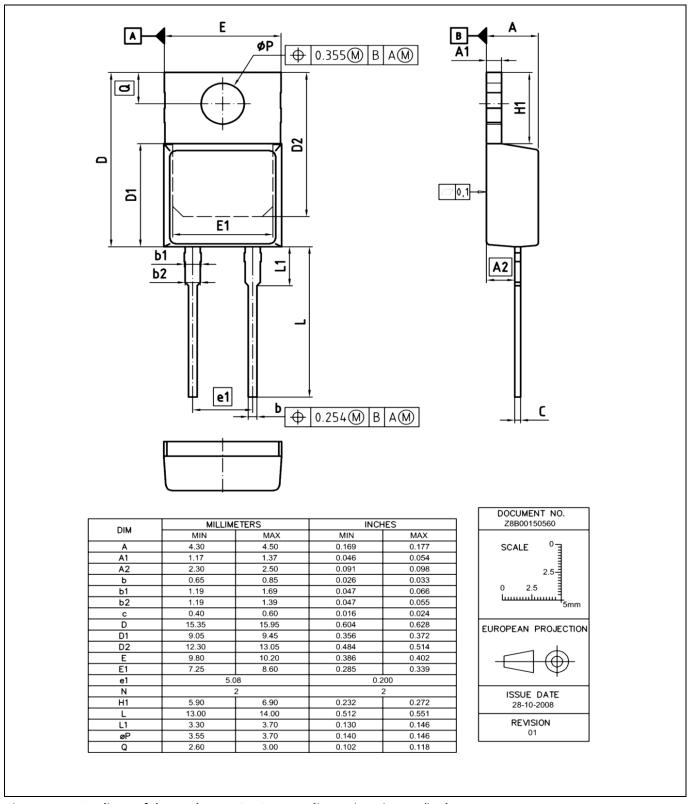


Figure 12 Outlines of the package PG-TO220-2, dimensions in mm/inches

# 6<sup>th</sup> Generation CoolSiC<sup>™</sup>

IDH20G65C6



### **Revision History**

### Major changes since the last revision

Revision	Date	Subject (major changes since last revision)						
2.0	2017-05-23	Release of final version						

#### Other Trademarks

All referenced product or service names and trademarks are the property of their respective owners.

Edition 2017-05-23 Published by Infineon Technologies AG 81726 München, Germany

© 2017 Infineon Technologies AG. All Rights Reserved.

Do you have a question about this document?

Email: erratum@infineon.com

Document reference

#### IMPORTANT NOTICE

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics ("Beschaffenheitsgarantie").

With respect to any examples, hints or any typical values stated herein and/or any information regarding the application of the product, Infineon Technologies hereby disclaims any and all warranties and liabilities of any kind, including without limitation warranties of non-infringement of intellectual property rights of any third party.

In addition, any information given in this document is subject to customer's compliance with its obligations stated in this document and any applicable legal requirements, norms and standards concerning customer's products and any use of the product of Infineon Technologies in customer's applications.

The data contained in this document is exclusively intended for technically trained staff. It is the responsibility of customer's technical departments to evaluate the suitability of the product for the intended application and the completeness of the product information given in this document with respect to such application.

For further information on the product, technology, delivery terms and conditions and prices please contact your nearest Infineon Technologies office (www.infineon.com).

#### WARNINGS

Due to technical requirements products may contain dangerous substances. For information on the types in question please contact your nearest Infineon Technologies office.

Except as otherwise explicitly approved by Infineon Technologies in a written document signed by authorized representatives of Infineon Technologies, Infineon Technologies' products may not be used in any applications where a failure of the product or any consequences of the use thereof can reasonably be expected to result in personal iniury.