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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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Not recommended
for new design

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2SK1626, 2SK1627

Silicon N Channel MOS FET

REJ03G0959-0200
(Previous: ADE-208-1302)
Rev.2.00
Sep 07, 2005

Application

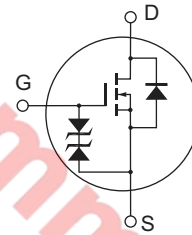
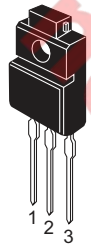
High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator and DC-DC converter

Outline

RENESAS Package code: PRSS0003AD-A
(Package name: TO-220FM)



1. Gate
2. Drain
3. Source

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Ratings	Unit	
Drain to source voltage	V _{DSS}	2SK1626	450	V
		2SK1627	500	
Gate to source voltage	V _{GSS}	±30	V	
Drain current	I _D	5	A	
Drain peak current	I _{D(pulse)} * ¹	20	A	
Body to drain diode reverse drain current	I _{DR}	5	A	
Channel dissipation	P _{ch} * ²	35	W	
Channel temperature	T _{ch}	150	°C	
Storage temperature	T _{stg}	-55 to +150	°C	

Notes: 1. PW ≤ 10 ∞s, duty cycle ≤ 1%

2. Value at T_C = 25°C

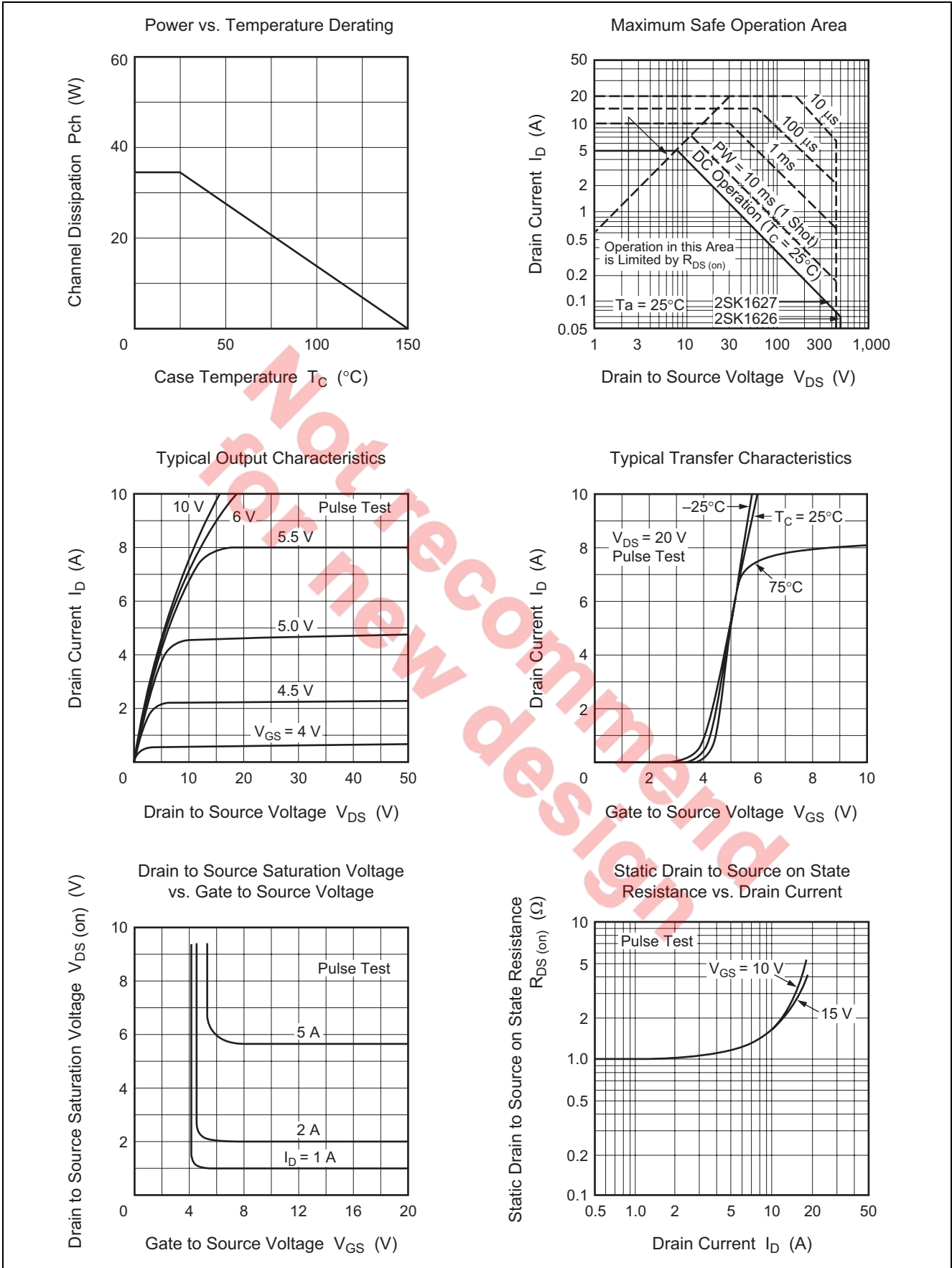
Electrical Characteristics

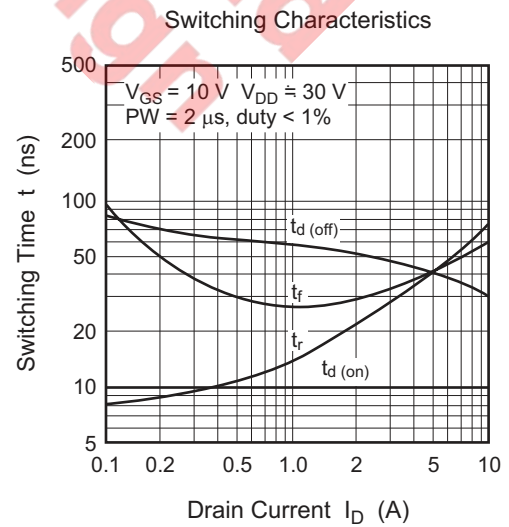
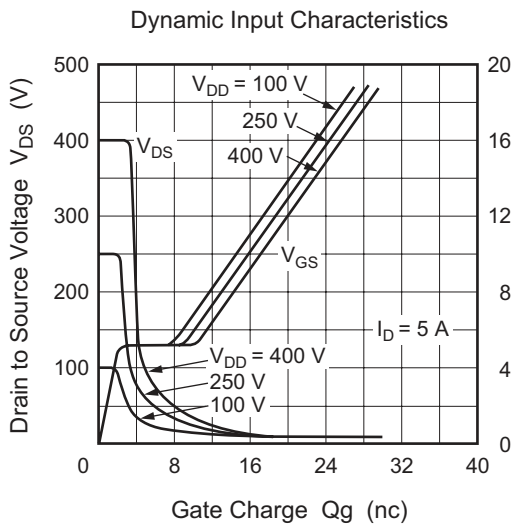
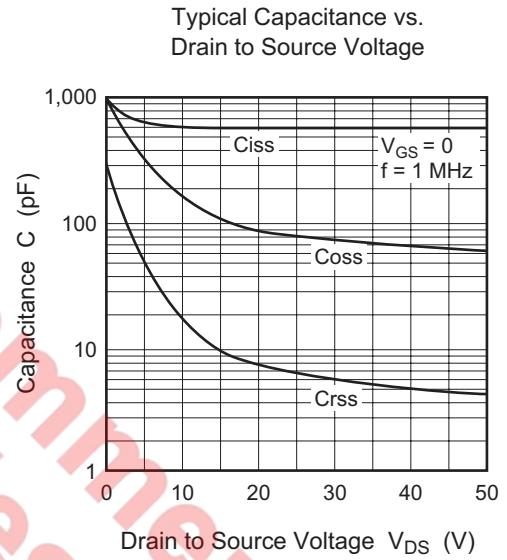
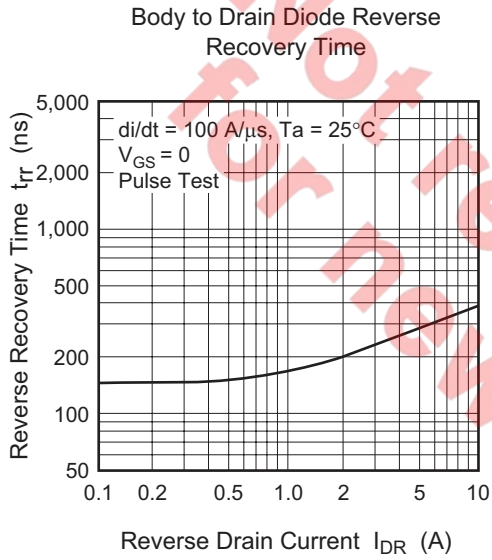
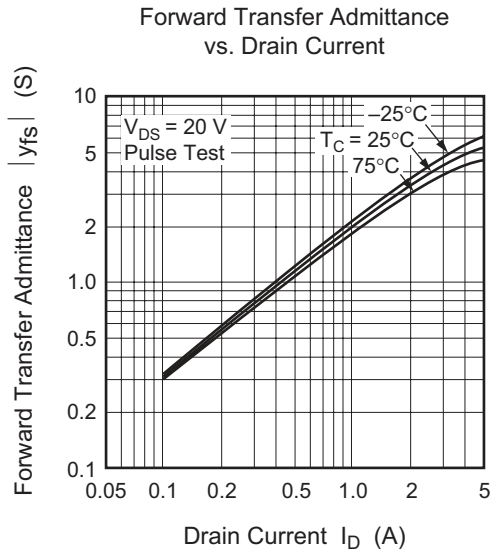
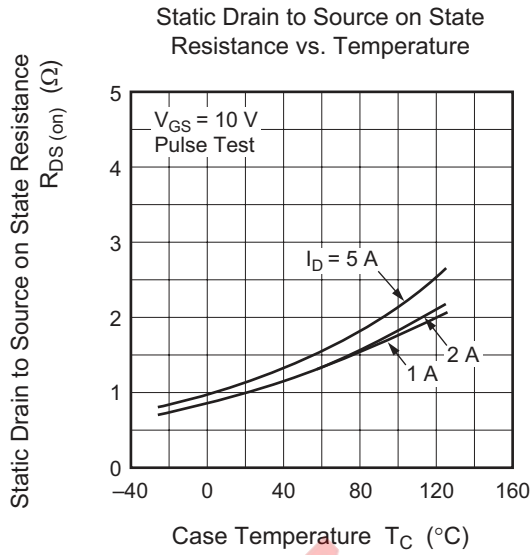
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test conditions	
Drain to source breakdown voltage	V _{(BR)DSS}	2SK1626	450	—	—	V	I _D = 10 mA, V _{GS} = 0
		2SK1627	500	—	—		
Gate to source breakdown voltage	V _{(BR)GSS}	±30	—	—	V	I _G = ±100 ∞A, V _{DS} = 0	
Gate to source leak current	I _{GSS}	—	—	±10	∞A	V _{GS} = ±25 V, V _{DS} = 0	
Zero gate voltage drain current	I _{DSS}	2SK1626	—	—	250	∞A	V _{DS} = 360 V, V _{GS} = 0
		2SK1627	—	—	—		V _{DS} = 400 V, V _{GS} = 0
Gate to source cutoff voltage	V _{GS(off)}	2.0	—	3.0	V	I _D = 1 mA, V _{DS} = 10 V	
Static drain to source on state resistance	R _{DS(on)}	2SK1626	—	1.0	1.4	Ω	I _D = 2.5 A, V _{GS} = 10 V * ³
		2SK1627	—	1.2	1.5		
Forward transfer admittance	y _{fs}	2.5	4.0	—	S	I _D = 2.5 A, V _{DS} = 10 V * ³	
Input capacitance	C _{iss}	—	640	—	pF	V _{DS} = 10 V, V _{GS} = 0,	
Output capacitance	C _{oss}	—	160	—	pF	f = 1 MHz	
Reverse transfer capacitance	C _{rss}	—	20	—	pF		
Turn-on delay time	t _{d(on)}	—	10	—	ns	I _D = 2.5 A, V _{GS} = 10 V, R _L = 12 Ω	
Rise time	t _r	—	25	—	ns		
Turn-off delay time	t _{d(off)}	—	50	—	ns		
Fall time	t _f	—	30	—	ns		
Body to drain diode forward voltage	V _{DF}	—	0.95	—	V	I _F = 5 A, V _{GS} = 0	
Body to drain diode reverse recovery time	t _{rr}	—	300	—	ns	I _F = 5 A, V _{GS} = 0, di _F /dt = 100 A/∞s	

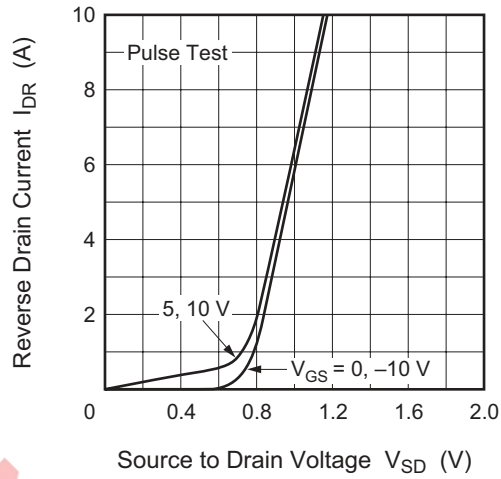
Note: 3. Pulse test

Main Characteristics

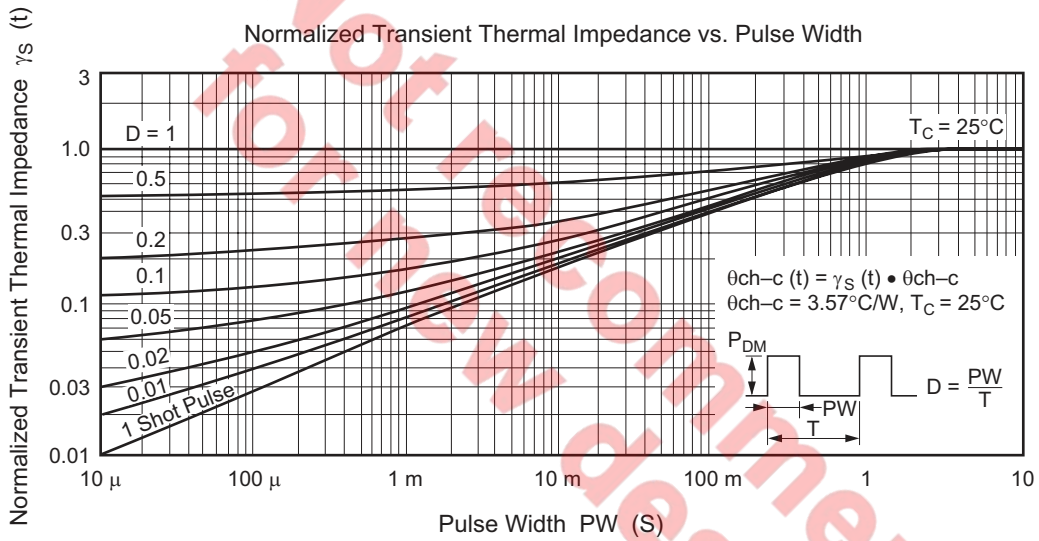




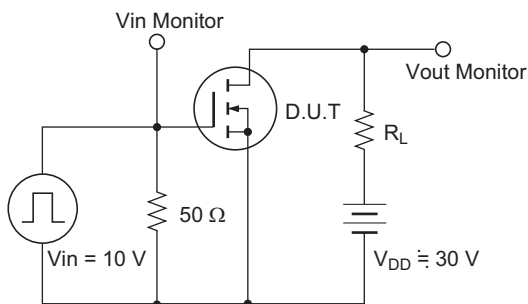
Reverse Drain Current vs. Source to Drain Voltage



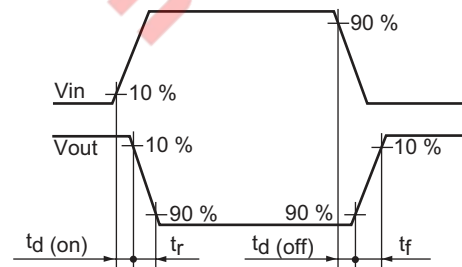
Normalized Transient Thermal Impedance vs. Pulse Width



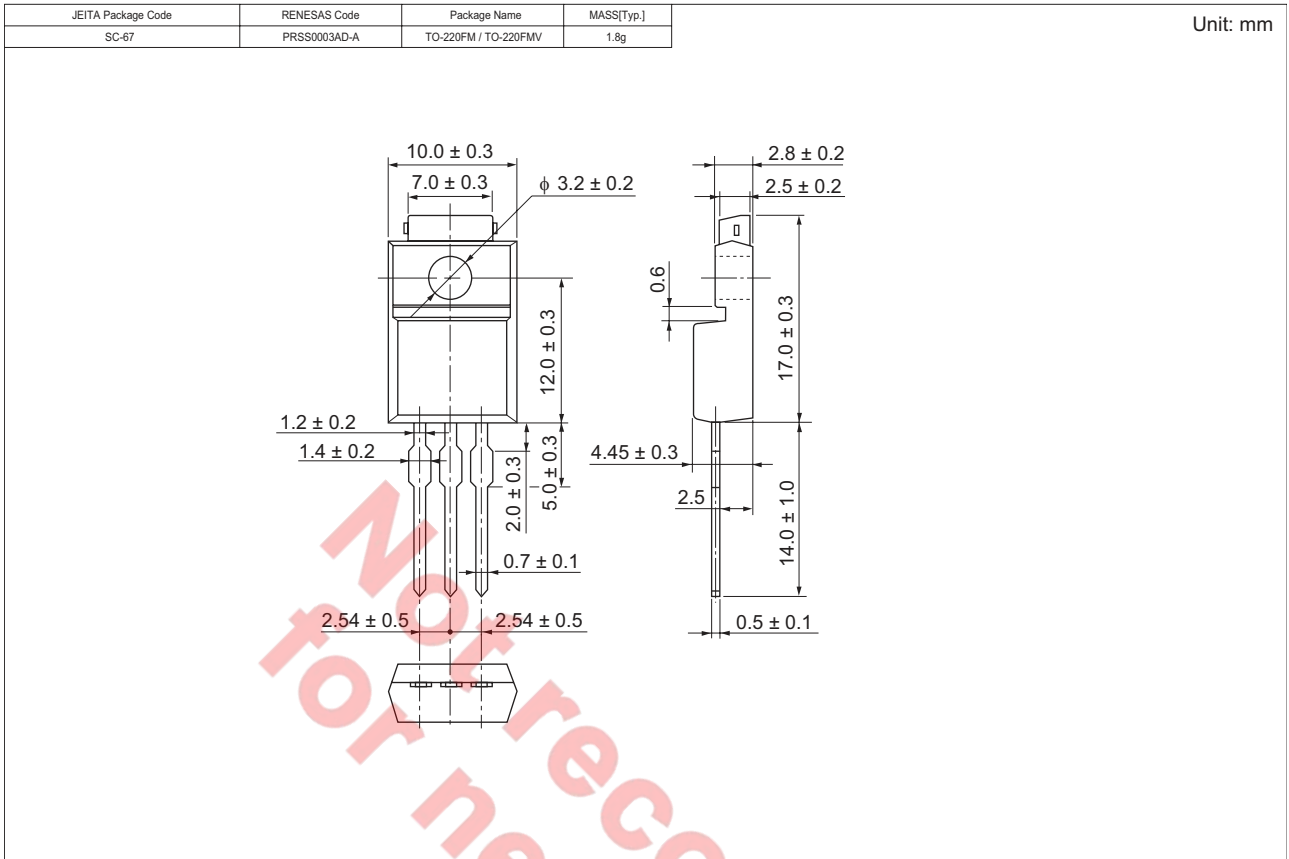
Switching Time Test Circuit



Waveforms



Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
2SK1626-E	500 pcs	Box (Sack)
2SK1627-E	500 pcs	Box (Sack)

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