

40V PNP SMALL SIGNAL TRANSISTOR IN SOT523

Features

- BV_{CEO} > -40V
- I_C = -200mA Collector Current
- Epitaxial Planar Die Construction
- Ultra-Small Surface Mount Package
- Complementary NPN Type: MMBT3904T
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

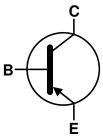
Mechanical Data

- Case: SOT523
- Case Material: Molded Plastic. "Green" Molding Compound.
- UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.002 grams (Approximate)

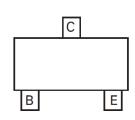




Top View



Device Symbol



Pin-out Top View

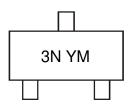
Ordering Information (Note 4)

Product	Status	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
MMBT3906T-7-F	Active	AEC-Q101	3N	7	8	3,000
MMBT3906T-13-F	Active	AEC-Q101	3N	13	8	10,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



3N = Product Type Marking Code YM = Date Code Marking Y or \overline{Y} = Year (ex: D = 2016) M or \overline{M} = Month (ex: 9 = September)

Date Code Kev

Year	201	5	2016		2017	2018	2019	2020	202	1 20	22 2	2023	2024	2025
Code	С		D		E	F	G	Н		,	J	K	L	М
Montl	h	Ja	n F	eb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	,	1		2	3	4	5	6	7	8	9	0	N	D



Absolute Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V_{CBO}	-40	V
Collector-Emitter Voltage	V _{CEO}	-40	V
Emitter-Base Voltage	V_{EBO}	-5	V
Collector Current	Ic	-200	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

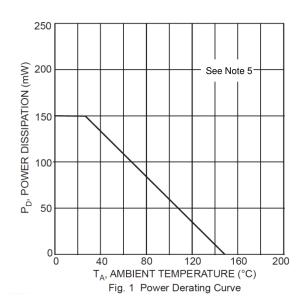
Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P _D	150	mW
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	833	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

ESD Ratings (Note 6)

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	400	V	С

Notes:

Thermal Characteristics and Derating Information



^{5.} For a device mounted with the collector lead on minimum recommended pad layout 1oz copper that is on a single-sided 1.6mm FR-4 PCB; device is measured under still air conditions whilst operating in a steady-state.

6. Refer to JEDEC specification JESD22-A114 and JESD22-A115.



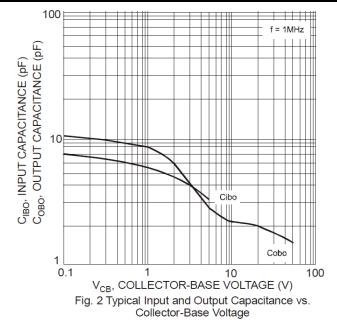
Electrical Characteristics (@T_A = +25°C unless otherwise specified.)

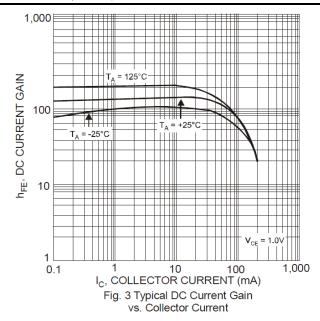
Characteristic	Symbol	Min	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)					
Collector-Base Breakdown Voltage	BV _{CBO}	-40	_	V	$I_C = -10\mu A, I_E = 0$
Collector-Emitter Breakdown Voltage	BV _{CEO}	-40	_	V	$I_C = -1 \text{mA}, I_B = 0$
Emitter-Base Breakdown Voltage	BV _{EBO}	-5	_	V	$I_E = -10\mu A, I_C = 0$
Collector Cutoff Current	I _{CEX}		-50	nA	$V_{CE} = -30V$, $V_{EB(OFF)} = -3V$
Base Cutoff Current	I_{BL}		-50	nA	$V_{CE} = -30V$, $V_{EB(OFF)} = -3V$
ON CHARACTERISTICS (Note 7)					
DC Current Gain	h _{FE}	60 80 100 60 30	300 — —	_	$I_{C} = -100\mu A, V_{CE} = -1V$ $I_{C} = -1mA, V_{CE} = -1V$ $I_{C} = -10mA, V_{CE} = -1V$ $I_{C} = -50mA, V_{CE} = -1V$ $I_{C} = -100mA, V_{CE} = -1V$
Collector-Emitter Saturation Voltage	V _{CE(SAT)}	_	-0.25 -0.40	V	$I_C = -10mA$, $I_B = -1mA$ $I_C = -50mA$, $I_B = -5mA$
Base-Emitter Saturation Voltage	$V_{BE(SAT)}$	-0.65 —	-0.85 -0.95	V	$I_C = -10\text{mA}, I_B = -1\text{mA}$ $I_C = -50\text{mA}, I_B = -5\text{mA}$
SMALL SIGNAL CHARACTERISTICS					
Output Capacitance	C_{obo}	_	4.5	pF	$V_{CB} = -5V$, $f = 1.0MHz$, $I_E = 0$
Input Capacitance	Cibo	_	10	pF	$V_{EB} = -0.5V$, $f = 1.0MHz$, $I_{C} = 0$
Input Impedance	h _{ie}	2	12	kΩ	
Voltage Feedback Ratio	h _{re}	0.1	10	x 10 ⁻⁴	$V_{CE} = -10V, I_{C} = -10mA,$
Small Signal Current Gain	h _{fe}	100	400	_	f = 1.0MHz
Output Admittance	h _{oe}	3	60	μS	
Current Gain-Bandwidth Product	f_{T}	250	_	MHz	$V_{CE} = -20V, I_{C} = -10mA,$ f = 100MHz
Noise Figure	NF	_	5	dB	$V_{CC} = 5V$, $I_C = 100\mu A$, $R_S = 1k\Omega$, $f = 1MHz$
SWITCHING CHARACTERISTICS					
Delay Time	t₀	_	35	ns	$V_{CC} = -3V, I_{C} = -10mA,$
Rise Time	t _R	1	35	ns	$V_{BE(OFF)} = -0.5V, I_{B1} = -1mA$
Storage Time	ts		225	ns	V _{CC} = -3.0V, I _C = -10mA
Fall Time	t _F	_	75	ns	$I_{B1} = -I_{B2} = -1.0 \text{mA}$

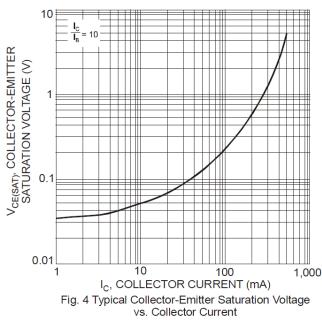
Note: 7. Measured under pulsed conditions. Pulse width \leq 300 μ s. Duty cycle \leq 2%.

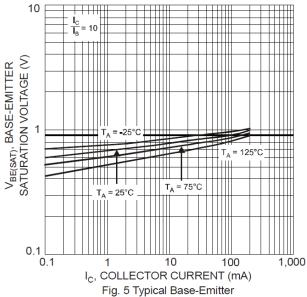


Typical Electrical Characteristics (@TA = +25°C, unless otherwise specified.)





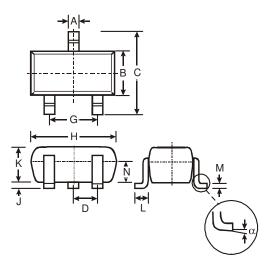






Package Outline Dimensions

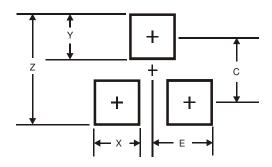
Please see http://www.diodes.com/package-outlines.html for the latest version.



	SOT523							
Dim	Min	Max	Тур					
Α	0.15	0.30	0.22					
В	0.75	0.85	0.80					
С	1.45	1.75	1.60					
D	_	_	0.50					
G	0.90	1.10	1.00					
Н	1.50	1.70	1.60					
J	J 0.00		0.05					
K	K 0.60		0.75					
L	0.10	0.30	0.22					
M	0.10	0.20	0.12					
N	0.45	0.65	0.50					
α	0°	8°						
All Dimensions in mm								

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
Z	1.8
Х	0.4
Υ	0.51
С	1.3
E	0.7



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