

PMDXB290UNE

20 V, dual N-channel Trench MOSFET

19 January 2023

Product data sheet

1. General description

Dual N-channel enhancement mode Field-Effect Transistor (FET) in a leadless ultra small DFN1010B-6 (SOT1216) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- Low threshold voltage
- Very fast switching
- Trench MOSFET technology
- ElectroStatic Discharge (ESD) protection > 2 kV HBM

3. Applications

- Relay driver
- High-speed line driver
- Low-side load switch
- Switching circuits

4. Quick reference data

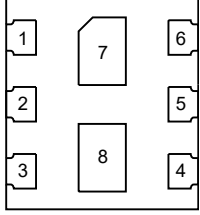
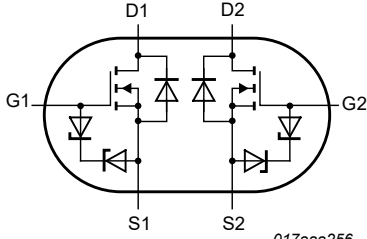
Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{DS}	drain-source voltage	$T_j = 25\text{ }^\circ\text{C}$	-	-	20	V
V_{GS}	gate-source voltage		-8	-	8	V
I_D	drain current	$V_{GS} = 4.5\text{ V}; T_{amb} = 25\text{ }^\circ\text{C}$	[1]	-	930	mA
Static characteristics						
R_{DSon}	drain-source on-state resistance	$V_{GS} = 4.5\text{ V}; I_D = 1.2\text{ A}; T_j = 25\text{ }^\circ\text{C}$	-	270	320	m Ω

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 1 cm².

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	S1	source TR1	 <p>Transparent top view DFN1010B-6 (SOT1216)</p>	 <p>017aaa256</p>
2	G1	gate TR1		
3	D2	drain TR2		
4	S2	source TR2		
5	G2	gate TR2		
6	D1	drain TR1		
7	D1	drain TR1		
8	D2	drain TR2		

6. Ordering information

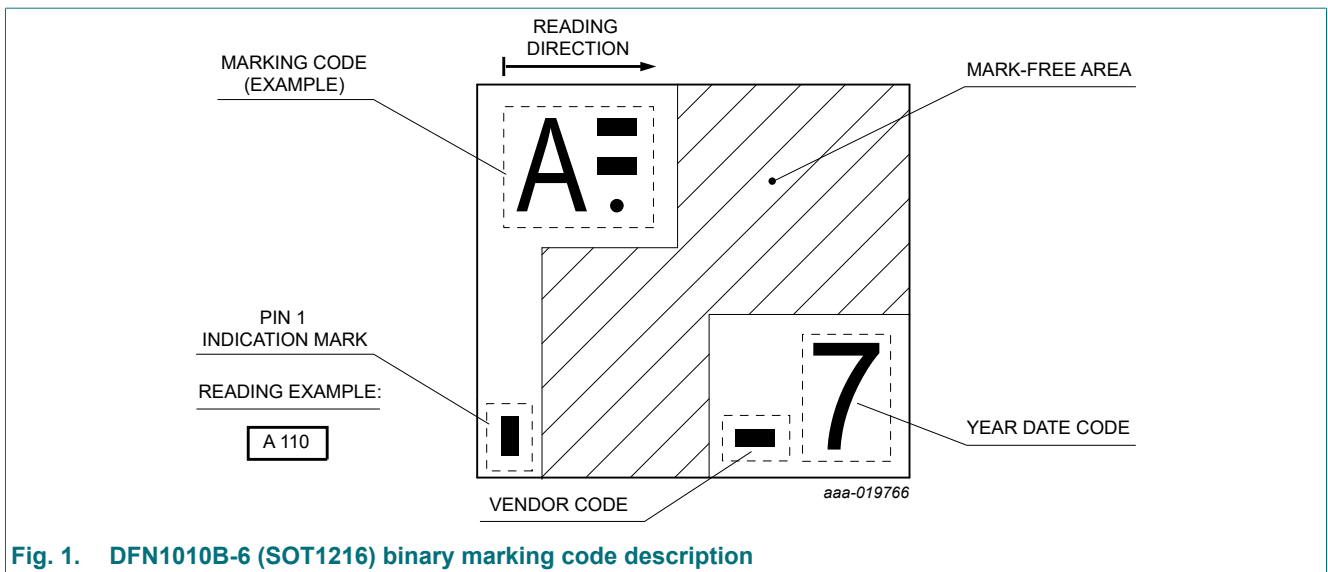
Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PMDXB290UNE	DFN1010B-6	plastic, leadless thermal enhanced ultra thin small outline package; 6 terminals; 0.35 mm pitch; 1.1 mm x 1 mm x 0.37 mm body	SOT1216

7. Marking

Table 4. Marking codes

Type number	Marking code
PMDXB290UNE	D 001



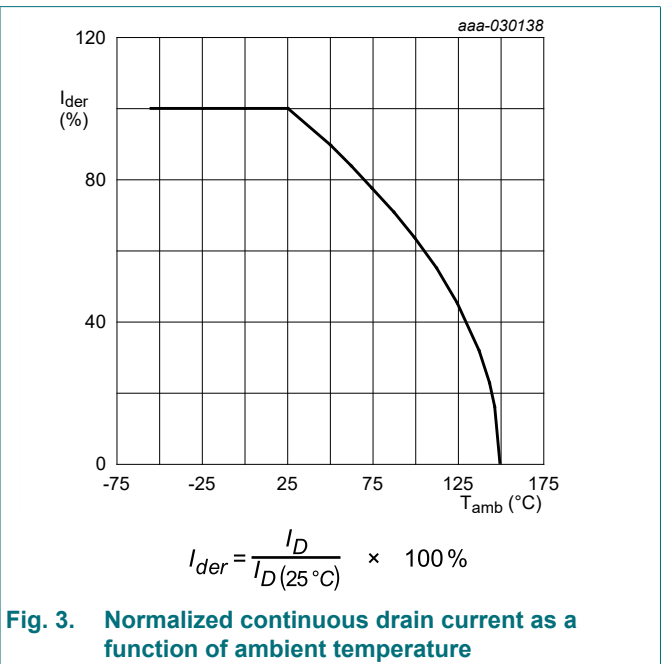
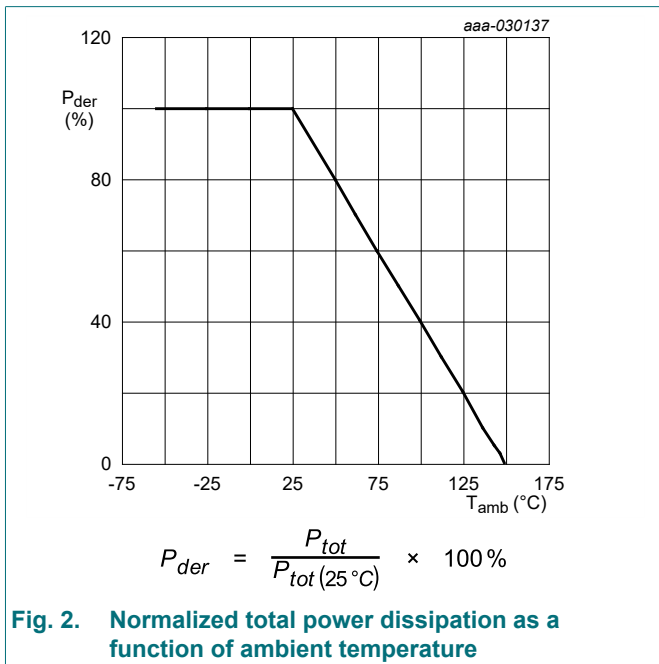
8. Limiting values

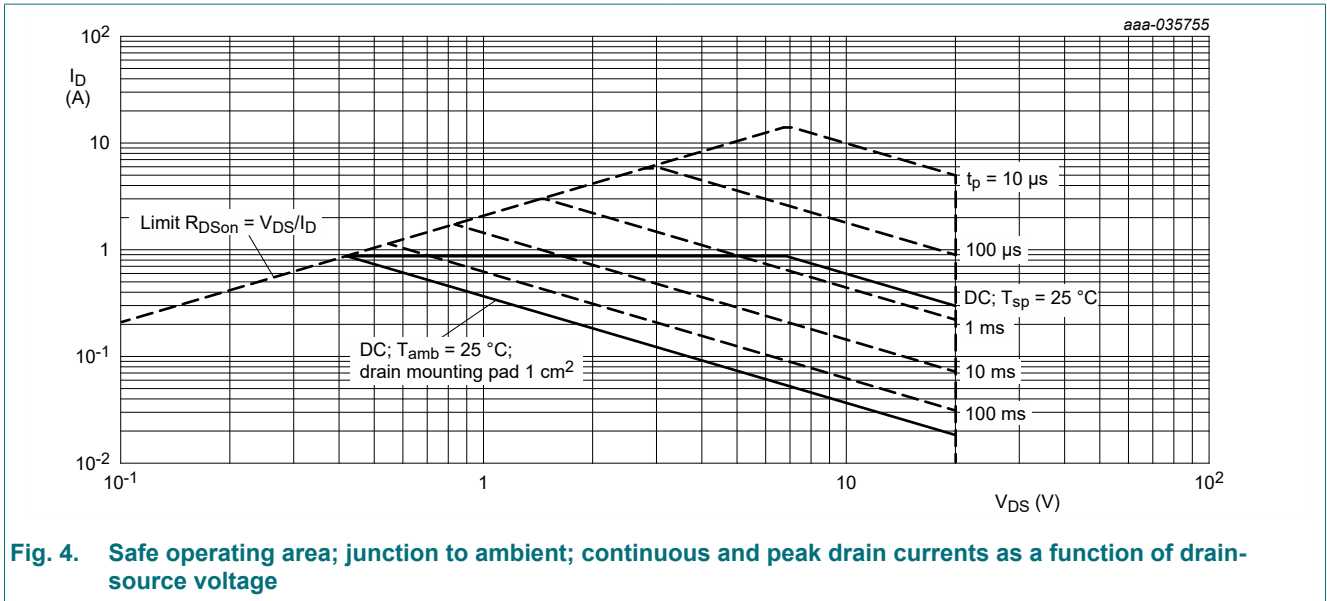
Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	20	V
V _{GS}	gate-source voltage			-8	8	V
I _D	drain current	V _{GS} = 4.5 V; T _{amb} = 25 °C	[1]	-	930	mA
		V _{GS} = 4.5 V; T _{sp} = 25 °C		-	3.5	A
		V _{GS} = 4.5 V; T _{amb} = 100 °C	[1]	-	590	mA
		V _{GS} = 4.5 V; T _{sp} = 100 °C		-	2.2	A
I _{DM}	peak drain current	T _{amb} = 25 °C; single pulse; t _p ≤ 10 μs		-	14	A
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	280	mW
			[1]	-	370	mW
		T _{sp} = 25 °C		-	6	W
T _j	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
Source-drain diode						
I _S	source current	T _{amb} = 25 °C	[1]	-	0.3	A
ESD maximum rating						
V _{ESD}	electrostatic discharge voltage	HBM		-	2000	V

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 1 cm².
- [2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.





9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	386	444	K/W
			[2]	-	297	342	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	18	21	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 1 cm².

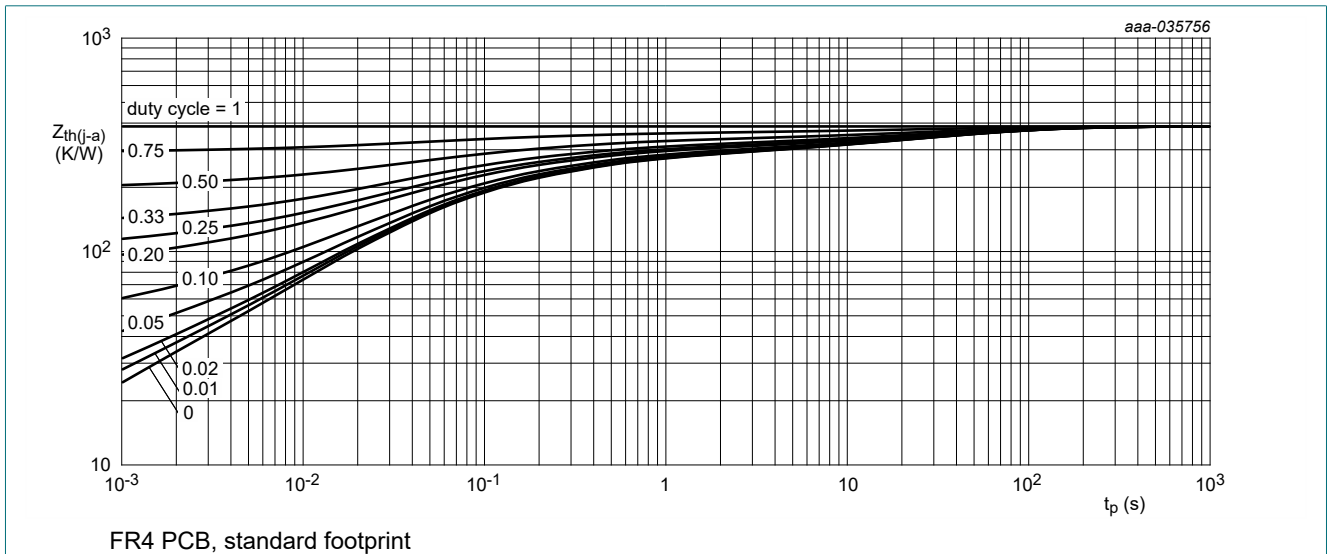


Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

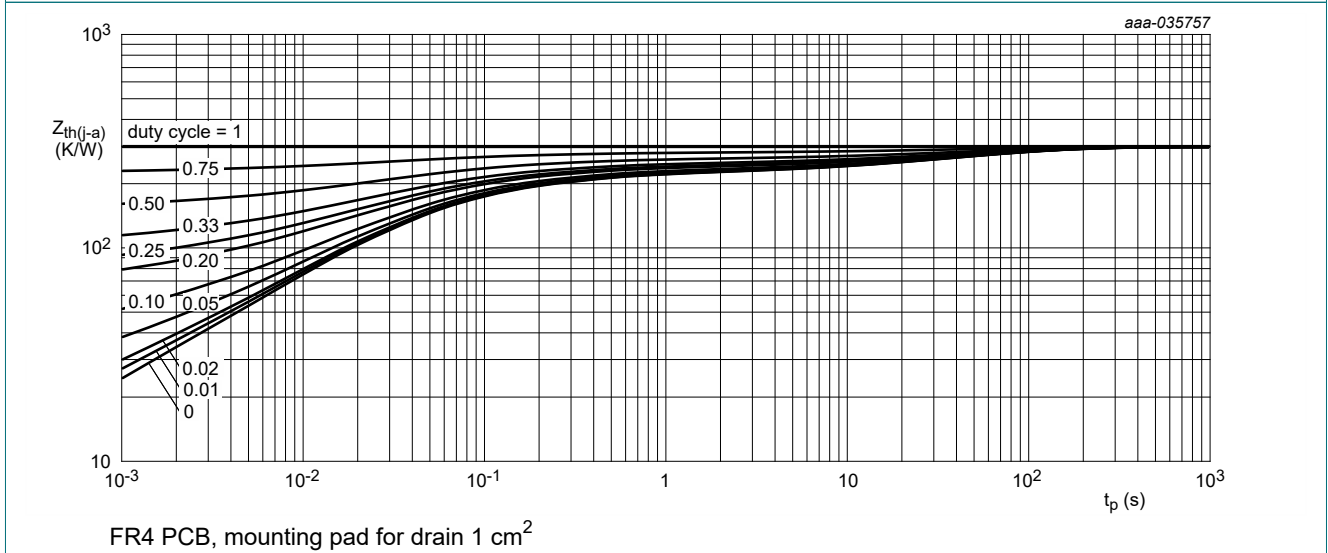


Fig. 6. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static characteristics						
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 250 \mu A$; $V_{GS} = 0 V$; $T_j = 25 \text{ }^\circ\text{C}$	20	-	-	V
V_{GSth}	gate-source threshold voltage	$I_D = 250 \mu A$; $V_{DS} = V_{GS}$; $T_j = 25 \text{ }^\circ\text{C}$	0.5	0.7	1	V
I_{DSS}	drain leakage current	$V_{DS} = 20 V$; $V_{GS} = 0 V$; $T_j = 25 \text{ }^\circ\text{C}$	-	-	1	μA
		$V_{DS} = 20 V$; $V_{GS} = 0 V$; $T_j = 150 \text{ }^\circ\text{C}$	-	-	20	μA
I_{GSS}	gate leakage current	$V_{GS} = 8 V$; $V_{DS} = 0 V$; $T_j = 25 \text{ }^\circ\text{C}$	-	-	10	μA
		$V_{GS} = -8 V$; $V_{DS} = 0 V$; $T_j = 25 \text{ }^\circ\text{C}$	-	-	-10	μA
		$V_{GS} = 4.5 V$; $V_{DS} = 0 V$; $T_j = 25 \text{ }^\circ\text{C}$	-	-	1	μA
		$V_{GS} = -4.5 V$; $V_{DS} = 0 V$; $T_j = 25 \text{ }^\circ\text{C}$	-	-	-1	μA
		$V_{GS} = 2.5 V$; $V_{DS} = 0 V$; $T_j = 25 \text{ }^\circ\text{C}$	-	-	500	nA
		$V_{GS} = -2.5 V$; $V_{DS} = 0 V$; $T_j = 25 \text{ }^\circ\text{C}$	-	-	-500	nA
R_{DSon}	drain-source on-state resistance	$V_{GS} = 4.5 V$; $I_D = 1.2 A$; $T_j = 25 \text{ }^\circ\text{C}$	-	270	320	m Ω
		$V_{GS} = 4.5 V$; $I_D = 1.2 A$; $T_j = 150 \text{ }^\circ\text{C}$	-	400	480	m Ω
		$V_{GS} = 2.5 V$; $I_D = 1 A$; $T_j = 25 \text{ }^\circ\text{C}$	-	360	480	m Ω
		$V_{GS} = 1.8 V$; $I_D = 120 \text{ mA}$; $T_j = 25 \text{ }^\circ\text{C}$	-	470	680	m Ω
		$V_{GS} = 1.5 V$; $I_D = 10 \text{ mA}$; $T_j = 25 \text{ }^\circ\text{C}$	-	600	1190	m Ω
g_{fs}	forward transconductance	$V_{DS} = 5 V$; $I_D = 1.2 A$; $T_j = 25 \text{ }^\circ\text{C}$	-	1.9	-	S
Dynamic characteristics						
$Q_{G(tot)}$	total gate charge	$V_{DS} = 10 V$; $I_D = 1.2 A$; $V_{GS} = 4.5 V$; $T_j = 25 \text{ }^\circ\text{C}$	-	0.6	0.9	nC
Q_{GS}	gate-source charge		-	0.1	-	nC
Q_{GD}	gate-drain charge		-	0.2	-	nC
C_{iss}	input capacitance	$V_{DS} = 10 V$; $f = 1 \text{ MHz}$; $V_{GS} = 0 V$; $T_j = 25 \text{ }^\circ\text{C}$	-	43.6	-	pF
C_{oss}	output capacitance		-	10.1	-	pF
C_{rss}	reverse transfer capacitance		-	8.2	-	pF
$t_{d(on)}$	turn-on delay time	$V_{DS} = 10 V$; $I_D = 1.2 A$; $V_{GS} = 4.5 V$; $R_{G(ext)} = 6 \Omega$; $T_j = 25 \text{ }^\circ\text{C}$	-	1	-	ns
t_r	rise time		-	3	-	ns
$t_{d(off)}$	turn-off delay time		-	5	-	ns
t_f	fall time		-	3	-	ns
Source-drain diode						
V_{SD}	source-drain voltage	$I_S = 0.7 A$; $V_{GS} = 0 V$; $T_j = 25 \text{ }^\circ\text{C}$	-	0.9	1.2	V

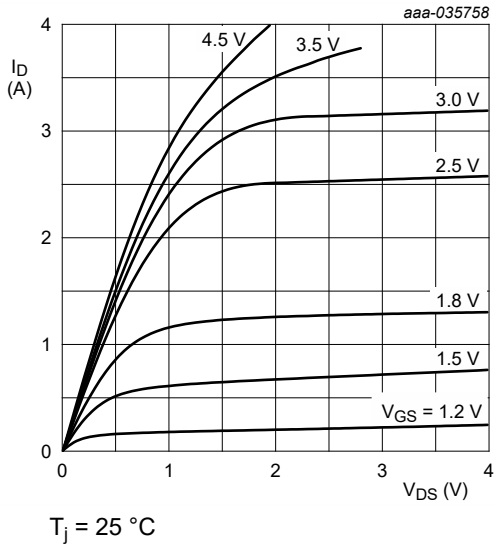


Fig. 7. Output characteristics: drain current as a function of drain-source voltage; typical values

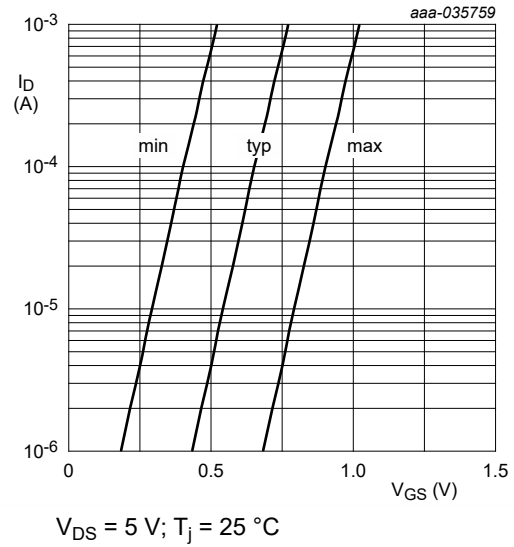


Fig. 8. Sub-threshold drain current as a function of gate-source voltage

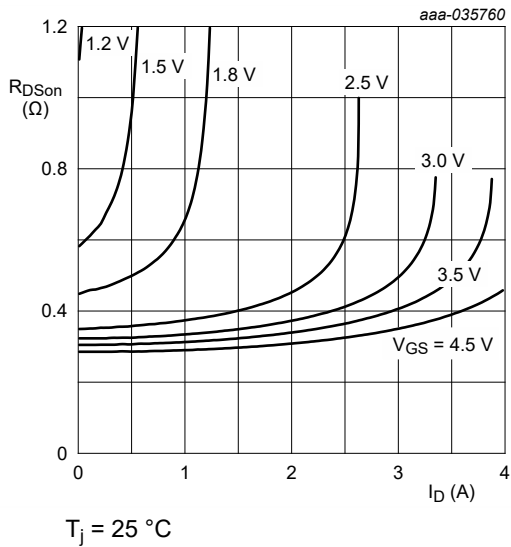


Fig. 9. Drain-source on-state resistance as a function of drain current; typical values

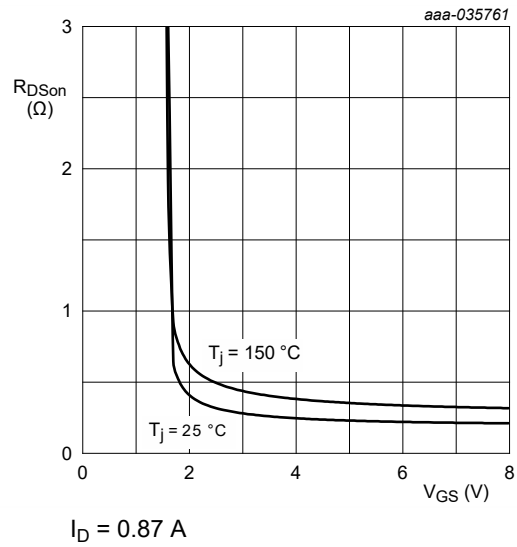


Fig. 10. Drain-source on-state resistance as a function of gate-source voltage; typical values

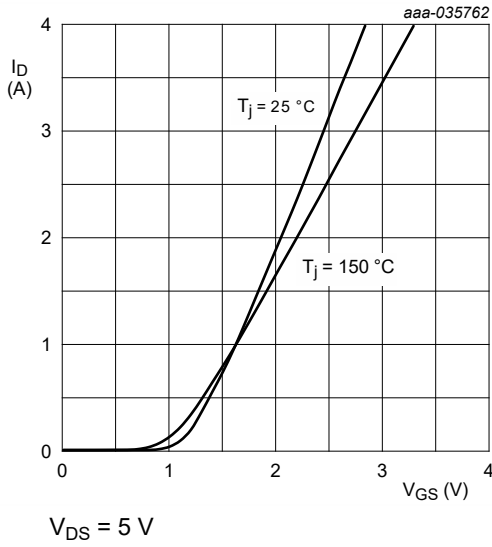
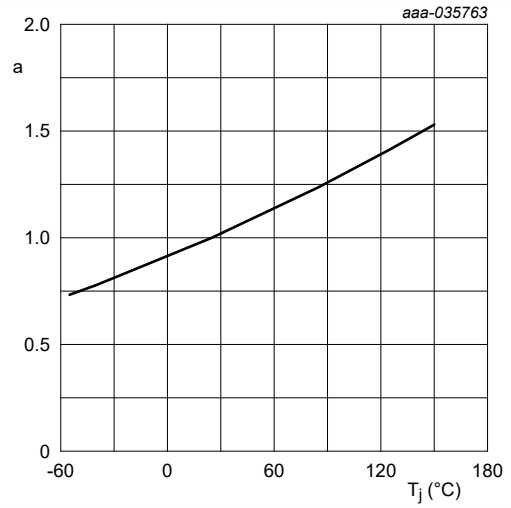
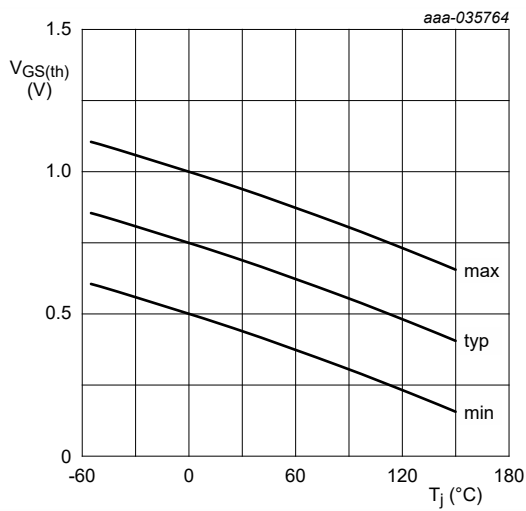


Fig. 11. Transfer characteristics: drain current as a function of gate-source voltage; typical values



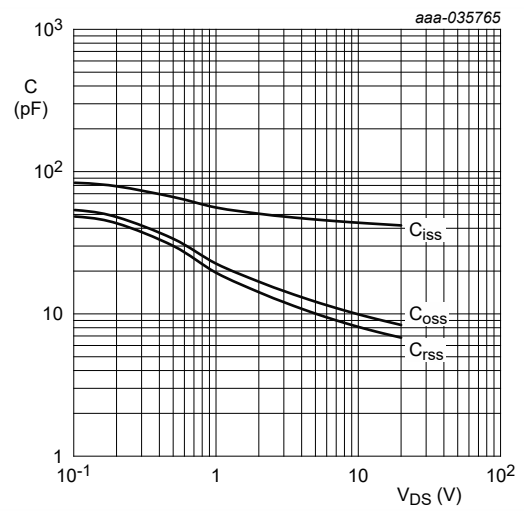
$$a = \frac{R_{DSon}}{R_{DSon}(25\text{ °C})}$$

Fig. 12. Normalized drain-source on-state resistance as a function of junction temperature; typical values



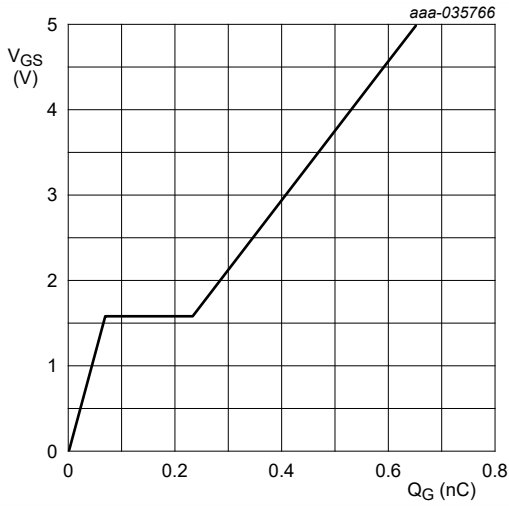
$I_D = 250\text{ }\mu\text{A}$; $V_{DS} = V_{GS}$

Fig. 13. Gate-source threshold voltage as a function of junction temperature



$f = 1\text{ MHz}$; $V_{GS} = 0\text{ V}$

Fig. 14. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values



$I_D = 1.2 \text{ A}; V_{DS} = 10 \text{ V}; T_{amb} = 25 \text{ }^\circ\text{C}$

Fig. 15. Gate-source voltage as a function of gate charge; typical values

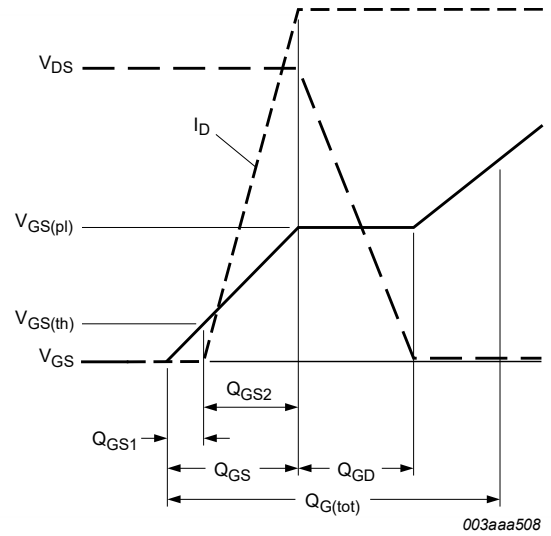
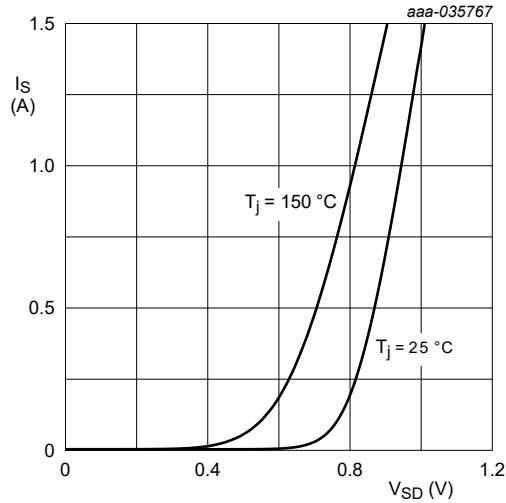


Fig. 16. Gate charge waveform definitions



$V_{GS} = 0 \text{ V}$

Fig. 17. Source current as a function of source-drain voltage; typical values

11. Test information

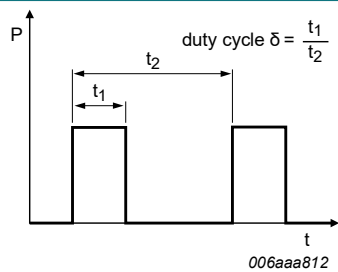


Fig. 18. Duty cycle definition

12. Package outline

DFN1010B-6: plastic thermal enhanced ultra thin small outline package; no leads;
6 terminals; body: 1.1 x 1.0 x 0.37 mm

SOT1216

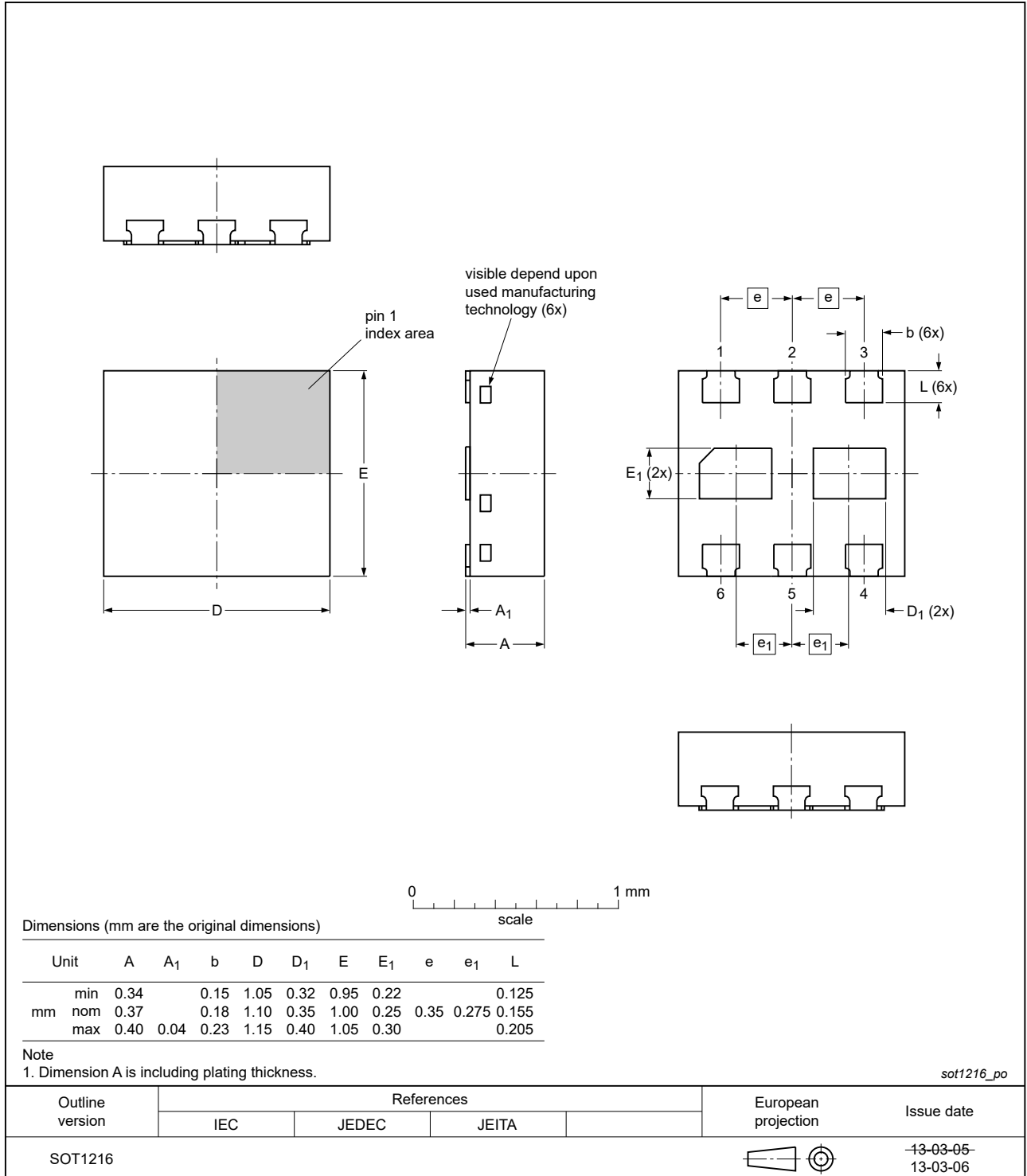


Fig. 19. Package outline DFN1010B-6 (SOT1216)

13. Soldering

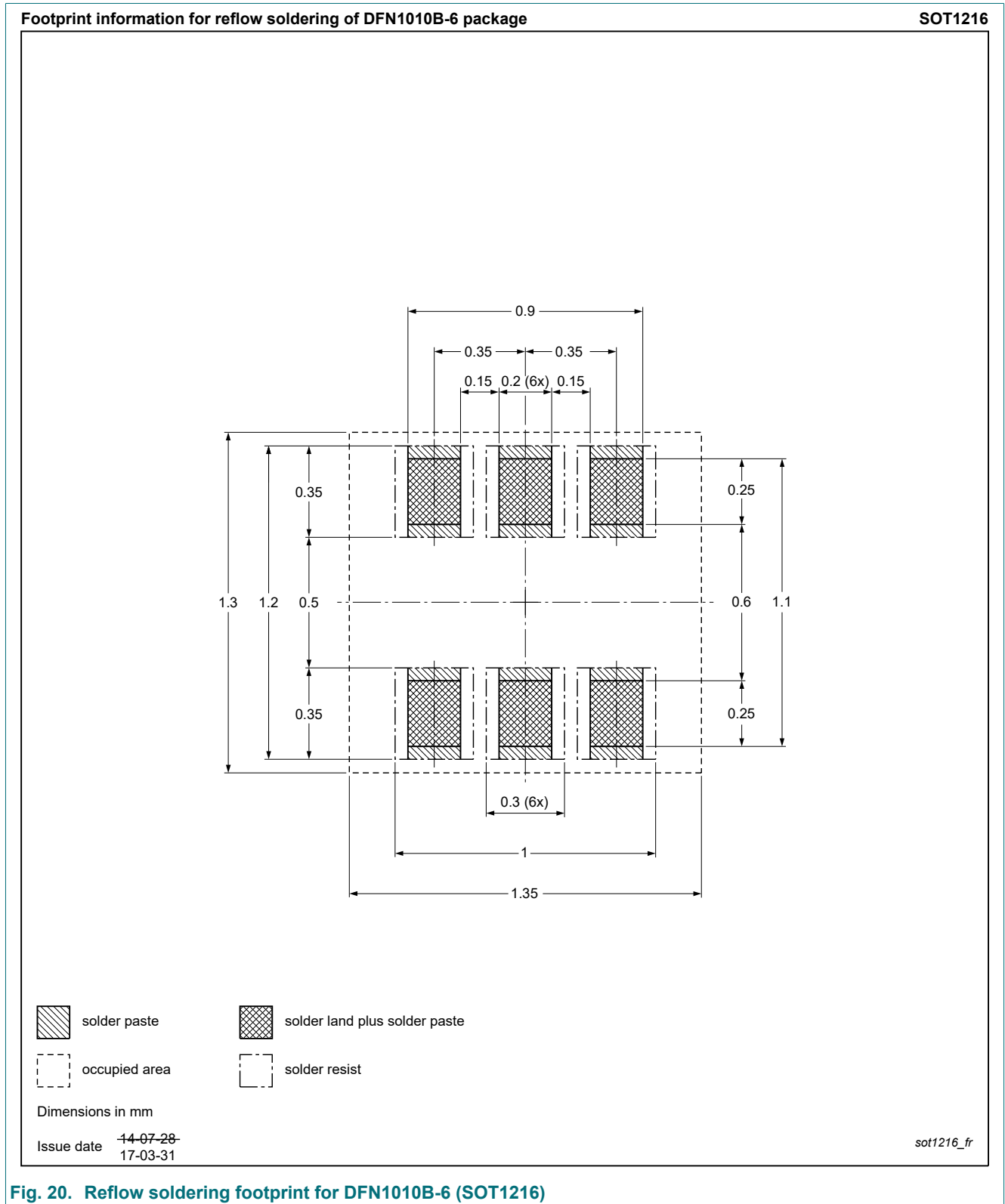


Fig. 20. Reflow soldering footprint for DFN1010B-6 (SOT1216)

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMDXB290UNE v.1	20230119	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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