



Radiation Hardened P-Channel MOSFET *Qualified per MIL-PRF-19500/630*

DESCRIPTION

Microsemi's first generation Rad- Hard MOSFET's are designed for Space and Military applications. The devices have been characterized for Total Dose (TID) and Single Event environments (SEE). These products may be used for satellite Power Supplies, Motor Controls and any miscellaneous power applications needed for Space. Microsemi's Rad- hard MOSFET's are qualified to MIL-PRF- 19500 slash sheet specifications. The 2N7389 is qualified to meet Slash Sheet /630 of MIL-PRF-19500.

Important: For the latest information, visit our website http://www.microsemi.com.

FEATURES

- JEDEC registered 2N7389 number
- Hermetically sealed package
- Internal metallurgical bonds
- RHA level JANS qualifications available per MIL-PRF-19500/630. (See <u>part nomenclature</u> for all available options.)
- RoHS compliant

APPLICATIONS / BENEFITS

- Low profile surface mount for crowded areas
- Lightweight package
- Military and other high-reliability rad-hard applications

MAXIMUM RATINGS @ $T_c = +25 \ ^{\circ}C$ unless otherwise stated

Parameters / Test Conditions	Symbol	Value	Unit
Operating & Storage Junction Temperature Range	T _J & T _{stg}	-55 to +150	°C
Thermal Resistance Junction-to-Case (see Figure 4)	R _{ejc}	5	°C/W
Total Power Dissipation@ $T_A = +25 \ ^{\circ}C$ @ $T_C = +25 \ ^{\circ}C \ ^{(1)}$	Ρτ	0.8 25	W
Gate-Source Voltage, dc	V _{GS}	± 20	V
Drain Current, dc @ T_c = +25 °C ^{(2) (3)}	I _{D1}	-6.5	Α
Drain Current, dc @ T_c = +100 ${}^{\circ}C$ (2) (3)	I _{D2}	-4.1	Α
Off-State Current (Peak Total Value) ⁽⁴⁾	I _{DM}	-26	Α
Source Current	Is	-6.5	Α

NOTES: 1. Derated linearly 0.2 W/ $^{\circ}$ C for T_C > +25 $^{\circ}$ C

2. The following formula derives the maximum theoretical I_D limit. I_D is limited by package and internal wires and may also be limited by pin diameter



- 3. See Figure 3 for maximum drain current graphs
- 4. $I_{DM} = 4 X I_{D1}$ as calculated in note (2)

<u>Qualified Levels</u>: JANSR and JANSF





U-18 LCC Package

Also available in:

TO-205AF (TO-39) Package (leaded top-hat) JANS 2N7389

MSC – Lawrence

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MSC – Ireland

Gort Road Business Park, Ennis, Co. Clare, Ireland Tel: +353 (0) 65 6840044 Fax: +353 (0) 65 6822298

Website:

www.microsemi.com



MECHANICAL and PACKAGING

- CASE: Ceramic LCC-18 with kovar gold plated lid
- TERMINALS: Gold plating over nickel
- MARKING: Manufacturer's ID, part number, date code, ESD symbol at pin 1 location
- TAPE & REEL option: Standard per EIA-481-D. Consult factory for quantities
- See <u>Package Dimensions</u> on last page.

PART NOMENCLATURE



SYMBOLS & DEFINITIONS				
Symbol	Definition			
di/dt	Rate of change of diode current while in reverse-recovery mode, recorded as maximum value.			
I _D	Drain Current, dc: The direct current into the drain terminal.			
I _{DSS}	Zero-Gate-Voltage Drain Current: The direct current into the gate terminal when the gate-source voltage is zero.			
I _F	Forward Current: The current flowing from the p-type region to the n-type region.			
I _{GSS}	Reverse-Gate Current, Drain Short-Circuited to Source: The direct current into the gate terminal with a forward gate source voltage applied (I _{GSSF}) or reverse gate source voltage applied (I _{GSSF}) and the drain terminal short-circuited to the source terminal.			
ls	Source Current, dc: The direct current into the source terminal.			
r _{DS(on)}	Static Drain-Source On-State Resistance: The dc resistance between the drain and source terminals with a specified gate-source voltage applied to bias the device to the on state.			
R _G	Gate Drive Impedance or Gate Resistance.			
V _{(BR)DSS}	Drain-Source Breakdown Voltage: Gate short-circuited to the source terminal.			
V _{DD}	Drain-Supply Voltage, dc: The dc supply voltage applied to a circuit connected to the drain terminal.			
V _{DG}	Drain-Gate Voltage, dc: The dc voltage between the drain and gate terminals.			
V _{DS}	Drain-Source Voltage, dc: The dc voltage between the drain terminal and the source terminal.			
$V_{\text{DS(on)}}$	Drain-Source On-State Voltage: The voltage between the drain and source terminals with a specified forward gate- source voltage supplied to bias the device to the on-state.			
V _{GS}	Gate-Source Voltage, dc: The dc voltage between the gate terminal and the source terminal.			



Parameters / Test Conditions	Symbol	Min.	Max.	Unit
PRE-IRRADIATION CHARACTERISTICS	I	1		
Drain-Source Breakdown Voltage				
$V_{GS} = 0 V, I_{D} = -1.0 mA$	$V_{(BR)DSS}$	-100		V
Gate-Source Voltage (Threshold) $V_{DS} \ge V_{GS}, I_D = -1 \text{ mA}$ $V_{DS} \ge V_{GS}, I_D = -1 \text{ mA}, T_J = +125^{\circ}\text{C}$ $V_{DS} \ge V_{GS}, I_D = -1 \text{ mA}, T_J = -55^{\circ}\text{C}$	V _{GS} (th)1 V _{GS} (th)2 V _{GS} (th)3	-2.0 -1.0	-4.0 -5.0	V
Gate Current $V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$ $V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}, T_{J} = +125^{\circ}\text{C}$	I _{GSS1} I _{GSS2}		±100 ±200	nA
Drain Current $V_{GS} = 0 V, V_{DS} = -80 V$	I _{DSS1}		-25	μA
Drain Current $V_{GS} = 0 V, V_{DS} = -80 V, T_{J} = +125 $ °C	I _{DSS2}		-0.25	mA
Static Drain-Source On-State Resistance V_{GS} = -12 V, I_D = -4.1 A pulsed	r _{DS(on)1}		0.30	Ω
Static Drain-Source On-State Resistance V_{GS} = -12 V, I_D = -6.5 A pulsed	r _{DS(on)2}		0.35	Ω
Static Drain-Source On-State Resistance $T_J = +125^{\circ}C$ $V_{GS} = -12 V$, $I_D = -4.1 A$ pulsed	r _{DS(on)3}		0.54	Ω
Diode Forward Voltage $V_{GS} = 0 V, I_D = -6.5 A pulsed$	V _{SD}		-3.0	V

ELECTRICAL CHARACTERISTICS @ $T_A = +25$ °C, unless otherwise noted

DYNAMIC CHARACTERISTICS

Parameters / Test Conditions	Symbol	Min.	Max.	Unit
Gate Charge:				
On-State Gate Charge				
V_{GS} = -12 V, I_{D} = -6.5 A, V_{DS} = -50 V	$Q_{g(on)}$		45	nC
Gate to Source Charge				
$V_{GS} = -12 \text{ V}, \text{ I}_{D} = -6.5 \text{ A}, \text{ V}_{DS} = -50 \text{ V}$	Q_gs		10	nC
Gate to Drain Charge				
$V_{GS} = -12 \text{ V}, \text{ I}_{D} = -6.5 \text{ A}, \text{ V}_{DS} = -50 \text{ V}$	Q_{gd}		25	nC

SWITCHING CHARACTERISTICS

Parameters / Test Conditions	Symbol	Min.	Max.	Unit
Turn-on delay time				
I_{D} = -6.5 A, V_{GS} = -12 V, R_{G} = 7.5 Ω , V_{DD} = -50 V	t _{d(on)}		30	ns
Rise time				
I_{D} = -6.5 A, V_{GS} = -12 V, R_{G} = 7.5 Ω , V_{DD} = -50 V	tr		50	ns
Turn-off delay time				
I_{D} = -6.5 A, V_{GS} = -12 V, R_{G} = 7.5 Ω , V_{DD} = -50 V	t _{d(off)}		70	ns
Fall time				
I_{D} = -6.5 A, V_{GS} = -12 V, R_{G} = 7.5 Ω , V_{DD} = -50 V	t _f		70	ns
Diode Reverse Recovery Time				
di/dt ≤ -100 A/µs, V _{DD} ≤ -50 V, I _F = -6.5 A	t _{rr}		250	ns



ELECTRICAL CHARACTERISTICS @ $T_A = +25 \text{ °C}$, unless otherwise noted (continued)

POST-IRRADIATION⁽¹⁾

Parameters / Test Conditions	Symbol	Min.	Max.	Unit
Drain-Source Breakdown Voltage				
$V_{GS} = 0 V, I_{D} = -1 mA$	V _{(BR)DSS}	-100		V
Gate-Source Voltage (Threshold)				
$V_{DS} \ge V_{GS}, I_D = -1.0 \text{ mA}$ JANSR	V _{GS(th)1}	-2.0	-4.0	V
$V_{DS} \ge V_{GS}, I_D = -1.0 \text{ mA}$ JANSF	V _{GS(th)1}	-2.0	-5.0	
Gate Current				
$V_{GS} = \pm 20 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$	I _{GSS1}		±100	nA
Drain Current				
$V_{GS} = 0 V$, $V_{DS} = -80 V$ of V_{DS} (pre-irradiated)	I _{DSS1}		-25	μA
Static Drain-Source On-State Voltage	r		1 00	V
$V_{GS} = -12 \text{ V}, \text{ I}_{\text{D}} = -4.1 \text{ pulsed}$	DS(on)		1.20	v
Diode Forward Voltage	V		-3.0	V
$V_{GS} = 0 \text{ V}, I_D = -6.5 \text{ pulsed}$	▼ SD		-0.0	v

NOTE: 1. Post-irradiation electrical characteristics apply to devices subjected to steady state total dose irradiation testing in accordance with MIL-STD-750, method 1019. Separate samples are tested for V_{GS} bias (12V), and V_{DS} bias (80V) conditions.

SAFE OPERATING AREA





GRAPHS

SEE (Single Event Effect) Typical Response:

Heavy lon testing of the 2N7389U device has been characterized at the Texas A&M cyclotron. The following SEE curve has been established using the elements, LET, range, and Total Energy conditions as shown:



It should be noted that total energy levels are considered to be a factor in SEE characterization. Comparisons to other datasets should not be based on LET alone. Please consult factory for more information.







GRAPHS (continued)



Thermal Impedance Curves



Millimeters

1.27 BSC

0.635 BSC

0.203 BSC

2.67 REF

3.05 REF

Max

9.14

7.49

2.92

1.39

1.65

0.76

1.40

2.03

3.30

PACKAGE DIMENSIONS



- 2. Millimeters are given for information only.
- In accordance with ASME Y14.5M, diameters are equivalent to Φx symbology.



PAD LAYOUT



SCHEMATIC

