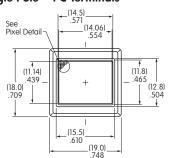
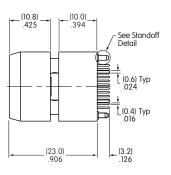


IS15BSBFP4RGB-BLK

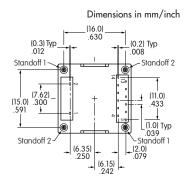
Tactile Actuation • Single Pole • PC Terminals

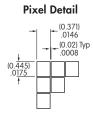


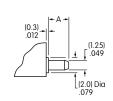




Footprint

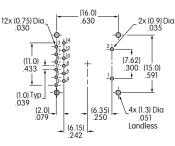






Standoff Detail





DISTINCTIVE CHARACTERISTICS

Compact Size

- Perfect for rack mount router and other applications with space limitations. Compact body size: 19.0mm (.748") x 18.0mm (.709")
- Vibrant Illumination
- Low Energy Consumption

Programmable LCD

Red/Green/Blue Backlighting Colors

Rubber Dome

Epoxy Sealed Straight PC Terminals

Snap-in standoff for easy, secure mounting and alignment

Programmable to display graphics, alphanumeric characters and animated sequences.

Integrated liquid crystal display provides wide viewing angle with high contrast and clarity.

Viewing area 14.5mm x 11.8mm (horizontal x vertical) at 36 x 24 pixels.

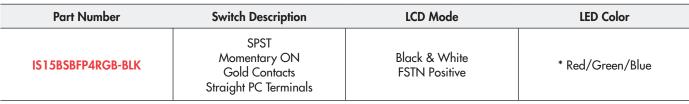
Dome gives crisp tactile feedback to positively indicate circuit transfer.

High reliability and long life of one million actuations minimum.

Epoxy sealed terminals prevent entry of solder flux and other contaminants.

Optional accessories available to simplify production process.

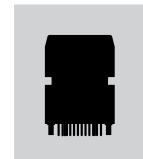




^{*} Simultaneous illumination of LEDs achieves infinite colors.









Compact LCD 36 x 24 with Black Cap

SWITCH SPECIFICATIONS

Circuit	SPST normally open
Electrical Capacity (Resistive Load)	100mA @ 12V DC
Contact Resistance	200 milliohms max @ 20mV 10mA
Insulation Resistance	100 megohms min @ 100V DC
Dielectric Strength	125V AC for 1 minute minimum
Mechanical Endurance	1,000,000 operations minimum
Electrical Endurance	1,000,000 operations minimum
Operating Force	2.2 ± 0.5 Newtons
Total Travel	1.8mm (.071")
Operating Temp. Range	-20°C ~ +60°C (-4°F ~ +140°F)
Storage Temp. Range	-30°C ~ +70°C (-22 °F ~ +158°F)

Optical Characteristics (Temperature at 25°C)

Items		Symbols	Minimum	Typical	Maximum
Contrast Ratio		Cr	_	3.0	_
Viewing Angle	Up & Down	θ	_	90°	_
(Cr ≥ 1.1)	Right & Left	ф		90°	

Absolute Maximum Ratings (Temperature at 25°C)

Items	Symbols	Ratings
Supply Voltage for Logics	V_{DD}	-0.3V to +7.0V
Supply Voltage for LCD	V_{LC}	-0.3V to +12.0V
Input Voltage	Vı	-0.3V to V _{DD} +0.3V
Output Voltage	Vo	-0.3V to V _{DD} +0.3V

LCD SPECIFICATIONS

Characteristics of Display			
Display Operation Mode	FSTN positive		
Display Condition	Transflective with built-in LED backlight		
Viewing Angle	6 o'clock		
Viewing Area	14.5mm x 11.8mm (horizontal x vertical)		
Driving Method	1/24 duty. 1/5 bias (built-in driving circuit)		
Pixel Format	36 x 24 pixels (horizontal x vertical)		
Pixel Size	0.371 mm x 0.445 mm (horizontal x vertical)		
Backlight LED	RGB: red/green/blue		

Recommended Operating Conditions (Temperature at 25°C)

Items	Symbols	Minimum	Typical	Maximum
Supply Voltage for Logics	V_{DD}	3.0V		5.5V
Supply Voltage	V_{LC}		* 7.3V	
Input Voltage	VI	0V		V _{DD}
Driving Frequency	f _{FLM}		150Hz	
Clock Operation Frequency	f _{SCP}		_	8.0MHz

^{*} LCD voltage (V_{LC}) level depends on the refreshing frequency and the temperature. The optimal V_{LC} can differ slightly from the stated typical value.

DC Characteristics of LCD Drive (Temperature at -20° C to $+60^{\circ}$ C and $V_{DD} = 5.0V \pm 10\%$)

ltems	Symbols	Test Conditions	Minimum	Typical	Maximum	Unit
High Level Input Voltage	V _{IH}		0.7V _{DD}		V _{DD}	٧
Low Level Input Voltage	V _{IL}		0		0.3V _{DD}	٧
High Level Input Leakage Current	I _{LIH}	$V_I = V_{DD}$	-		10	μA
Low Level Input Leakage Current	I _{LIL}	$V_i = 0V$			10	μA
High Level Output Voltage	V _{OH}	I _{OH} = -500μA	V _{DD} -0.5			٧
Low Level Output Voltage	V _{OL}	I _{OL} = 500µA			0.5	٧
High Level Output Leakage Current	I _{LOH}	$V_O = V_{DD}$			10	μA
Low Level Output Leakage Current	I _{LOL}	V _○ = 0V			10	μA
Supply Current	I _{DD}	$f_{SCP} = 1.0MHz$			500	μA
LCD Drive Current	I _{LC}	$f_{LP} = 2.4 \text{kHz V}_{LC} = 7.3 \text{V}$		500	2,000	μA



Compact LCD 36 x 24 with Black Cap

SMARTDISPLAY

Timing Characteristics of LCD Drive IC

(Temperature at -20° C to $+60^{\circ}$ C and $V_{DD} = 5.0V \pm 10\%$)

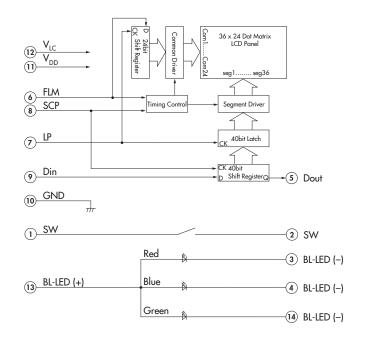
Symbols	Minimum	Maximum
f_{SCP}		8.0MHz
f_{LP}		50kHz
t _{CWH}	50ns	
t _{CWL}	50ns	
t _{DSD}	45ns	
t _{DHD}	50ns	
t _{PDO}		25ns
t _{DSL}	50ns	
t _{DHL}	50ns	
t _{LWH}	50ns	
t _{DSF}	50ns	
† _{DHF}	50ns	
t _r /t _f		1 <i>5</i> ns
	f _{SCP} f _{LP} t _{CWH} t _{CWL} t _{DSD} t _{DHD} t _{PDO} t _{DSL} t _{DHL} t _{LWH} t _{DSF}	f _{SCP} f _{LP} t _{CWH} 50ns t _{CWL} 50ns t _{DSD} 45ns t _{DHD} 50ns t _{DDD} 50ns t _{DHL} 50ns t _{LWH} 50ns t _{DSF} 50ns t _{DHF} 50ns

LED CHARACTERISTICS

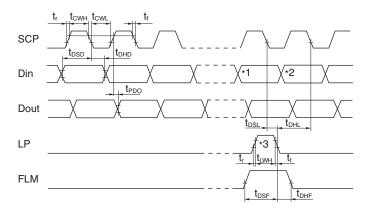
Typical Electrical Characteristics (Temperature at 25°C)

Backlight Color	Symbols	Red	Green	Blue
Forward Current	I _F	10mA	8.5mA	8.0mA

BLOCK DIAGRAM & PIN CONFIGURATIONS



Timing Diagram



- *1 Last data on first line
- *2 Beginning data on second line
- *3 Location of LP signal on first line

ABSOLUTE MAXIMUM FOR LEDS

Electrical Characteristics (Temperature at 25°C)

Backlight Color	Symbols	Red Green		Blue
Forward Current	I _F	20mA	20mA	20mA
Forward Voltage	V _F	2.0V (I _F = 10mA)	2.8V (I _F = 8.5 mA)	2.8V (I _F = 8.0mA)
Reverse Voltage	V_R	4.0V	4.0V	4.0V
Current Reduction Rate Above 25°C	$\Delta I_{F}(DC)$		-0.33mA/°C	
*Power Dissipation (LED Overall 115mW)	P_{D}	40mW	60mW	60mW

^{*}For uniform light emission, Power Dissipation should not exceed the Absolute Maximum Rating, and the Forward Current should not exceed the derated Absolute Forward Current.



IS15BSBFP4RGB-BLK RGB LED Backlight Black and White LCD





BLOCK DIAGRAM & PIN CONFIGURATIONS

<u>Pin No.</u>	<u>Symbol</u>	<u>Name</u>	<u>Function</u>
1	SW	Terminal of Switch	Normally open
(2) (3) (4)	SW	Terminal of Switch	Normally open
<u>(3)</u>	BL-LED (-)	Terminal of Backlight LED	Cathode for red
	BL-LED (-)	Terminal of Backlight LED	Cathode for blue
(5)	Dout	Data Output	Display serial output. Can be used to connect to Din of the next SWITCH. As a result, many SWITCHES can be controlled with one clock and data signal.
6	FLM	First Line Marker	The marking signal for the first line data of LCD display. The first line of LCD will be selected by the falling edge of LP signal during the high level (FLM).
7	LP	Latch Pulse	Line data latch pulse will latch content of internal 40-bit shift register at falling edge for one line of display. LP will also increment the display line by one.
8	SCP	Serial Clock Pulse	Clock used by 40-bit internal shift register of the switch, shifting the display data bit presented at Din at falling edge.
9	Din	Data Input	Display serial data bit. Note: to map the display data, because of the difference between the number of internal shift register data (40) and the single line of LCD pixels (36), the first four bits of data shifted will be dummy bits.
10	GND	Ground	
11)	$V_{\scriptscriptstyle DD}$	Power	Power source for logic circuit
12	V_{LC}	Power	Power source for LCD drive
13	BL-LED (+)	Terminal of Backlight LED	Anode for common
14	BL-LED (-)	Terminal of Backlight LED	Cathode for green

PRECAUTIONS FOR HANDLING & STORAGE

Handling

- 1. The IS Series devices are electrostatic sensitive.
- 2. Limit operating force to keytop to 100.0N maximum, as excessive pressure may damage the LCD device.
- 3. The IS series devices are not process sealed.
- 4. If the LCD is accidentally broken, avoid contact with the liquid and wash off any liquid spills to the skin or clothing.
- 5. Clean cap surface with dry cloth. If further cleaning is needed, wipe with dampened cloth using neutral cleanser and dry with clean cloth. Do not use organic solvent.
- 6. Recommended soldering time and temperature limits:
 - Do not exceed 70°C at the LCD level.
 - Wave soldering recommended. Preheat temperature 110°C, preheat time 30 seconds, peak temperature 270°C, peak time 5 seconds,1 cycle. Manual soldering recommended. Temperature 390°C for 4 seconds, 2 cycles.
- 7. Recommendation for backlight color uniformity: use constant current driver. For current limiting resistor method, the power source should be at least twice the backlight LED forward voltage.
- 8. The VLC voltage should not be applied before logic voltage. If VLC voltage is present before logic voltage, it may cause the driver logic to freeze and damage the LCD, and the driver logic may become damaged.
- 9. Backlight Forward Current should not exceed the derated Absolute Maximum Forward Current based on the temperature.
- 10. Excessive images may result after the same image is emitted continuously for an extended period of time.

Storage

- 1. Store in original container and away from direct sunlight.
- 2. Keep away from static electricity.
- 3. Avoid extreme temperatures, high humidity, gaseous substances, and all forms of chemical contamination.

OPTIONAL ACCESSORIES

AT9704-065F Socket for Compact Pushbutton

Materials:

Base - Glass Fiber Reinforced PBT Terminals - Brass/Beryllium Copper

- The socket permits the Compact SmartDisplay to be plugged in after automated processing.
- Use of the socket enables easy field replacement of the device.

Also available: LCD 36 x 24 Logic Boards





ATTENTION