

# MC9S08SH8/4

## Fact sheet

### Target Applications

- Personal care devices
- AC-powered consumer goods, including power tools, small appliances and hand-held devices
- Fire alarms
- Wireless sensor applications, including those enabled by a simple media access controller (SMAC)
- Watchdog coprocessors
- Secure boot coprocessors
- Security systems

### Overview

The MC9S08SH8/4 strengthens Freescale's entry-level 8-bit microcontroller portfolio by extending the advantages of the HCS08 core and peripherals to 5V. The highly integrated SH controller family is Freescale's first low-pin-count S08 with 40 MHz internal clock source (ICS). It includes an additional ganged output that can use one bit to toggle multiple pins, which allows for higher current drive. The family also features strong analog capabilities, a complete set of serial modules, a temperature sensor and robust memory options ideal for general-purpose consumer and industrial applications in the 2.7V to 5.5V range.

MC9S08SH8/4 Block Diagram

HCS08 CPU	
4/8 KB Flash	On-chip ICE (DBG)
256/512B RAM	BDC
LVI	12-ch., 10-bit ADC
COP	SCI
I <sup>2</sup> C	SPI
Int/Ext Osc.	Two 2-ch., 16-bit Timers
Internal Clock Source w/FLL	8-bit Modulo Timer w/Prescaler
Temperature Sensor	Up to 17 GPIO
	Analog Comparator
Up to 5V	

Features	Benefits
<b>8-bit HCS08 Central Processor Unit (CPU)</b>	
<ul style="list-style-type: none"> <li>• Up to 40 MHz HCS08 CPU (20 MHz bus frequency) for 50 ns minimum instruction time</li> <li>• HC08 instruction set with added BGND instruction</li> </ul>	<ul style="list-style-type: none"> <li>• Offering high performance up to 5V, ideal for industrial applications</li> <li>• Backward object-code compatibility with 68HC08 and 68HC05 so existing code libraries can still be used</li> <li>• Allows for efficient, compact module coding in assembly or C compiler</li> </ul>
<ul style="list-style-type: none"> <li>• Support for up to 32 interrupt/reset sources</li> </ul>	<ul style="list-style-type: none"> <li>• Allows for software flexibility and optimization for real-time applications</li> </ul>
<b>Integrated Third-Generation Flash Memory and RAM</b>	
<ul style="list-style-type: none"> <li>• Embedded flash that is in-application reprogrammable over the full operating voltage and temperature range with a single power supply</li> </ul>	<ul style="list-style-type: none"> <li>• Provides users a single solution for multiple platforms or a single platform that is field reprogrammable in virtually any environment</li> <li>• Does not require an additional pin or power supply for flash programming, simplifying the interface for in-line programming and allowing for more GPIO pins</li> </ul>
<ul style="list-style-type: none"> <li>• Extremely fast, byte-writable programming; as fast as 20 us/byte</li> </ul>	<ul style="list-style-type: none"> <li>• Helps reduce production programming costs through ultra-fast programming as well as lower system power consumption due to shorter writes</li> </ul>
<ul style="list-style-type: none"> <li>• Up to 100,000 write/erase cycles at typical voltage and temperature (10k minimum write/erase); 100 years typical data retention (15 years minimum)</li> </ul>	<ul style="list-style-type: none"> <li>• Allows electrically erasable programmable read-only memory (EEPROM) emulation, reducing system costs and board real estate</li> </ul>
<b>Flexible Clock Options</b>	
<ul style="list-style-type: none"> <li>• Internal clock source (ICS) module containing a frequency-locked loop (FLL) controlled by internal or external reference</li> </ul>	<ul style="list-style-type: none"> <li>• Can eliminate the cost of all external clock components, reduce board space and increase system reliability</li> </ul>
<ul style="list-style-type: none"> <li>• Precision trimming of internal reference allows typical 0.1 percent resolution and +0.5 percent to -1 percent deviation over operating temperature and voltage</li> </ul>	<ul style="list-style-type: none"> <li>• Provides one of the most accurate internal clock sources on the market for the money</li> </ul>
<ul style="list-style-type: none"> <li>• Internal reference can be trimmed from 31.25 kHz to 39.065 kHz, allowing for 16 MHz to 20 MHz FLL output</li> </ul>	<ul style="list-style-type: none"> <li>• Can use trimming to adjust bus clocks for optimal serial communication baud rates and/or timer intervals</li> </ul>
<ul style="list-style-type: none"> <li>• Low-power oscillator module (XOSC) with software selectable crystal or ceramic resonator range, 31.25 kHz to 38.4 kHz or 1 MHz to 16 MHz, and supports external clock source input up to 40 MHz</li> </ul>	<ul style="list-style-type: none"> <li>• 32 kHz oscillator provides low-power option for systems requiring time-keeping functionality (i.e., time and date) while in low-power modes</li> </ul>
<b>17 Bidirectional Input/Output (I/O) Lines; One Output Only Line</b>	
<ul style="list-style-type: none"> <li>• Outputs 10 mA each; 60 mA max for package</li> </ul>	<ul style="list-style-type: none"> <li>• High-current I/O allows direct drive of LED and other circuits to virtually eliminate external drivers and reduce system costs</li> </ul>
<ul style="list-style-type: none"> <li>• Software selectable pull-ups on ports when used as input; internal pull-up on reset and interrupt request (IRQ) pin</li> </ul>	<ul style="list-style-type: none"> <li>• Reduces customer system cost by eliminating need for external resistors</li> </ul>
<ul style="list-style-type: none"> <li>• Software selectable slew rate control and drive strength on ports when used as output</li> </ul>	<ul style="list-style-type: none"> <li>• Can configure ports for slower slew rate and weaker drive to minimize noise emissions from the MCU</li> </ul>
<ul style="list-style-type: none"> <li>• 8-pin keyboard interrupt module with software selectable polarity on edge or edge/level modes</li> </ul>	<ul style="list-style-type: none"> <li>• Keyboard scan with programmable pull-ups/pull-downs virtually eliminates external glue logic when interfacing to simple keypads</li> </ul>
<b>Ganged Output Option for PTB (5:2) and PTC (3:0)</b>	
<ul style="list-style-type: none"> <li>• Allows single write to change state of multiple pins</li> </ul>	
<ul style="list-style-type: none"> <li>• Drives up to 80 mA without risk of runaway code shorting</li> </ul>	
<ul style="list-style-type: none"> <li>• Provides option to tie multiple pins from different ports to same control registers</li> </ul>	
<ul style="list-style-type: none"> <li>• Safely drives multiple outputs</li> </ul>	
<b>Multiple Serial Communication Options</b>	
<ul style="list-style-type: none"> <li>• Serial communications interface module with option for 13-bit break capabilities and double-buffered transmit and receive</li> </ul>	<ul style="list-style-type: none"> <li>• All serial peripherals available for use in parallel on 16-pin devices</li> </ul>
<ul style="list-style-type: none"> <li>• Serial peripheral interface module and I<sup>2</sup>C bus module</li> </ul>	

## Features

## Benefits

### Integrated Analog Peripherals

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| <ul style="list-style-type: none"> <li>• 12-ch., 10-bit analog-to-digital converter (ADC)           <ul style="list-style-type: none"> <li>◦ Automatic compare function, software programmable for greater than/equal to or less than conditions</li> <li>◦ Asynchronous clock source</li> <li>◦ Temperature sensor</li> <li>◦ Internal bandgap reference channel</li> <li>◦ Hardware triggerable using the RTI counter</li> <li>◦ Low-power and high-speed options</li> </ul> </li> </ul> | <ul style="list-style-type: none"> <li>• Easy interface to analog inputs, such as sensors</li> <li>• Used to set conversion complete and generate interrupt only when result matches condition</li> <li>• Can be used to run ADC when MCU clocks are off, such as in STOP3 low-power mode</li> <li>• Calculates temperature without any external components and saves an ADC input channel for other use</li> <li>• Constant voltage source for calibrating ADC results requires no external components</li> <li>• Takes periodic measurements without CPU involvement; can be used in STOP3 with compare function to take measurement and wake MCU from STOP3 only when compare level is reached</li> <li>• Flexible configuration to meet high-performance and low-power requirements</li> </ul> |
| <ul style="list-style-type: none"> <li>• Analog comparator module (ACMP)           <ul style="list-style-type: none"> <li>◦ Option to compare to internal reference</li> <li>◦ Option to route comparator output directly to pin</li> <li>◦ Output can be optionally routed to timer/pulse width modulation (TPM) module as input capture trigger</li> </ul> </li> </ul>   | <ul style="list-style-type: none"> <li>• Requires only single pin for input signal</li> <li>• Allows other components in system to see results of comparator with minimal delay</li> <li>• Can be used for single slope ADC and resistance-capacitance (RC) time constant measurements</li> </ul>  |
| <ul style="list-style-type: none"> <li>• Real-time counter (RTC)           <ul style="list-style-type: none"> <li>◦ 8-bit modulus counter with binary- or decimal-based prescaler</li> <li>◦ External clock source for precise time base, time-of-day, calendar or task scheduling functions</li> </ul> </li> </ul>  | <ul style="list-style-type: none"> <li>• Free running on-chip low-power oscillator (1 kHz) for cyclic wake-up without external components; runs in all MCU modes</li> </ul>  |

### Three Timer Modules

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| <ul style="list-style-type: none"> <li>• Programmable 16-bit timer/PWM modules (TPM1 and TPM2)</li> </ul> | <ul style="list-style-type: none"> <li>• Each channel can be independently programmable for input capture, output compare, buffered edge-aligned pulse width modulation (PWM) or buffered center-aligned PWM</li> </ul> |
| <ul style="list-style-type: none"> <li>• 8-bit modulo timer module (MTIM) with 8-bit prescaler</li> </ul> | <ul style="list-style-type: none"> <li>• Timer overflow interrupt can be enabled to generate periodic interrupts for time-based software loops</li> </ul>   |

### System Protection

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| <ul style="list-style-type: none"> <li>• Watchdog computer operating properly (COP) reset with option to run from dedicated 1 kHz internal clock source or bus clock</li> <li>• Low-voltage detection with reset or interrupt</li> </ul> | <ul style="list-style-type: none"> <li>• Resets device in instance of runaway or corrupted code, and independent clock source provides additional protection in case of loss of clock</li> <li>• Allows system to write/save important variables before voltage drops too low</li> <li>• Can hold device in reset until reliable voltage levels are reapplied to the part</li> </ul> |
| <ul style="list-style-type: none"> <li>• Illegal opcode detection with reset</li> </ul>  | <ul style="list-style-type: none"> <li>• Resets device in instance of runaway or corrupted code</li> </ul>   |
| <ul style="list-style-type: none"> <li>• Flexible block protection</li> </ul>  | <ul style="list-style-type: none"> <li>• Secures code sections to prevent accidental corruption by runaway code</li> <li>• Option to protect various block sizes</li> <li>• Option to put bootloader code in protected space and clear flash for reprogramming</li> </ul>  |
| <ul style="list-style-type: none"> <li>• Security feature for flash and RAM</li> </ul>   | <ul style="list-style-type: none"> <li>• Prevents unauthorized access to memory to protect a customer's valuable software IP</li> </ul>  |
| <ul style="list-style-type: none"> <li>• Always-on power-on reset (POR) circuitry</li> </ul>   | <ul style="list-style-type: none"> <li>• Significantly reduces risk of code runaway due to brownout situations</li> </ul>  |

### Background Debugging System and On-chip In-circuit Emulation (ICE) with Real-time Bus Capture

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|---|---|
| <ul style="list-style-type: none"> <li>• On-chip ICE</li> </ul> | <ul style="list-style-type: none"> <li>• Provides single-wire debugging and emulation interface; eliminates need for expensive emulation tools</li> <li>• Provides circuit emulation without the need for additional, expensive development hardware</li> </ul> |
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## Cost-effective Development Tools

### DEMO9S08SH8

**\$59\***

Cost-effective demonstration board with potentiometer, LEDs, serial port and built-in USB-BDM cable for debugging and programming

### M68CYCLONEPRO

**\$499\***

HC08/HCS08/HC12/HCS12 stand-alone flash programmer or in-circuit emulator, debugger and flash programmer with USB, serial or Ethernet interface options

### USBMULTILINKBDM

**\$99\***

Universal HC08 in-circuit debugger and flash programmer with USB PC interface

### CWS-H08-STDED-CX

**Free\*\***

CodeWarrior™ Special Edition for Microcontrollers includes integrated development environment (IDE), linker, debugger, unlimited assembler, Processor Expert™ auto-code generator, full-chip simulation and 16 KB C compiler

\*Prices indicated are MSRP.

\*\*Subject to license agreement and registration.

### Package Options

Part Number	Package	Temp. Range
MC9S08SH8CSC	8-pin SOIC	-40°C to +85°C
MC9S08SH4CSC	8-pin SOIC	-40°C to +85°C
MC9S08SH8CTG	16-pin TSSOP	-40°C to +85°C
MC9S08SH4CTG	16-pin TSSOP	-40°C to +85°C
MC9S08SH8CPJ	20-pin PDIP	-40°C to +85°C
MC9S08SH4CPJ	20-pin PDIP	-40°C to +85°C
MC9S08SH8CTJ	20-pin TSSOP	-40°C to +85°C
MC9S08SH4CTJ	20-pin TSSOP	-40°C to +85°C
MC9S08SH8CWJ	20-pin SOIC	-40°C to +85°C
MC9S08SH4CWJ	20-pin SOIC	-40°C to +85°C
MC9S08SH8CFK	24-pin QFN	-40°C to +85°C
MC9S08SH4CFK	24-pin QFN	-40°C to +85°C
MC9S08SH8MSC	8-pin SOIC	-40°C to +125°C
MC9S08SH4MSC	8-pin SOIC	-40°C to +125°C
MC9S08SH8MTG	16-pin TSSOP	-40°C to +125°C
MC9S08SH4MTG	16-pin TSSOP	-40°C to +125°C
MC9S08SH8MPJ	20-pin PDIP	-40°C to +125°C
MC9S08SH4MPJ	20-pin PDIP	-40°C to +125°C
MC9S08SH8MTJ	20-pin TSSOP	-40°C to +125°C
MC9S08SH4MTJ	20-pin TSSOP	-40°C to +125°C
MC9S08SH8MWJ	20-pin SOIC	-40°C to +125°C
MC9S08SH4MWJ	20-pin SOIC	-40°C to +125°C
MC9S08SH8MFK	24-pin QFN	-40°C to +125°C
MC9S08SH4MFK	24-pin QFN	-40°C to +125°C

### Learn More:

For current information about Freescale products and documentation, please visit [www.freescale.com/8bit](http://www.freescale.com/8bit).