

## N-Channel Enhancement Mode Power MOSFET

<p><b>Description</b></p> <p>The G20N03K uses advanced trench technology to provide excellent <math>R_{DS(ON)}</math>, low gate charge. It can be used in a wide variety of applications.</p> <p><b>General Features</b></p> <ul style="list-style-type: none"> <li>● <math>V_{DS}</math> 30V</li> <li>● <math>I_D</math> (at <math>V_{GS} = 10V</math>) 30A</li> <li>● <math>R_{DS(ON)}</math> (at <math>V_{GS} = 10V</math>) &lt; 20mΩ</li> <li>● <math>R_{DS(ON)}</math> (at <math>V_{GS} = 4.5V</math>) &lt; 24mΩ</li> <li>● 100% Avalanche Tested</li> <li>● RoHS Compliant</li> </ul> <p><b>Application</b></p> <ul style="list-style-type: none"> <li>● Power switch</li> <li>● DC/DC converters</li> </ul>	<p>Schematic diagram</p> <p>TO-252</p>		
<b>Device</b>	<b>Package</b>	<b>Marking</b>	<b>Packaging</b>
G20N03K	TO-252	G20N03	2500pcs/Reel

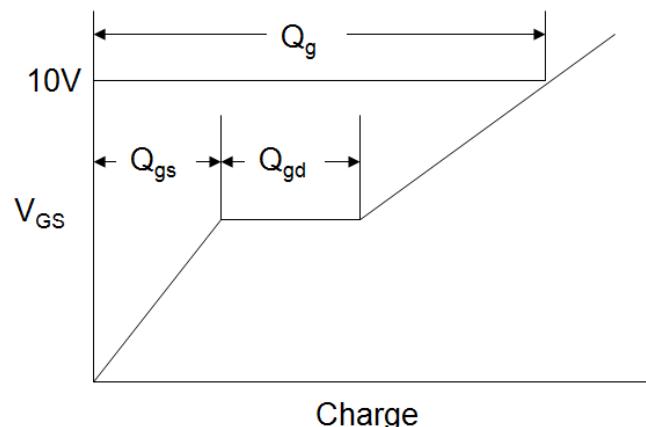
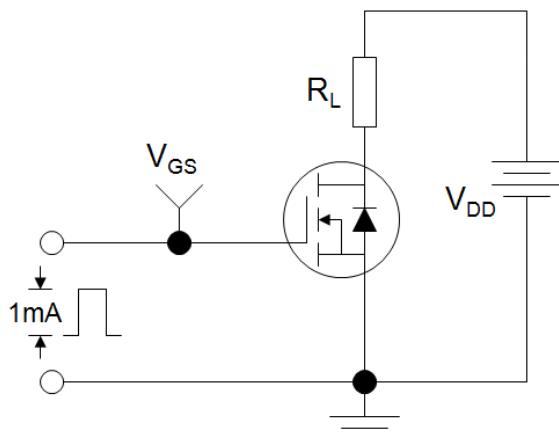
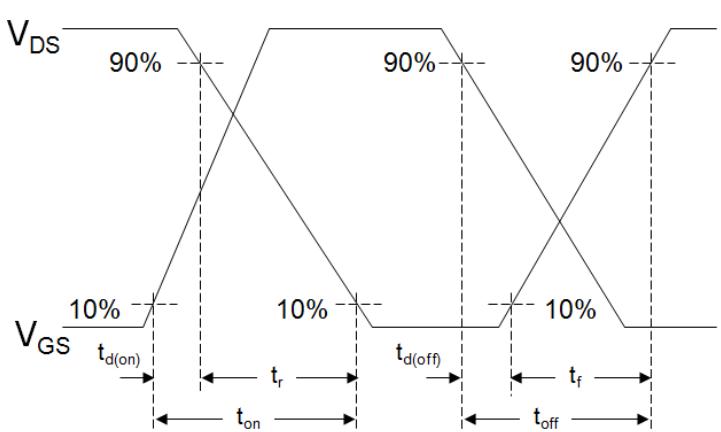
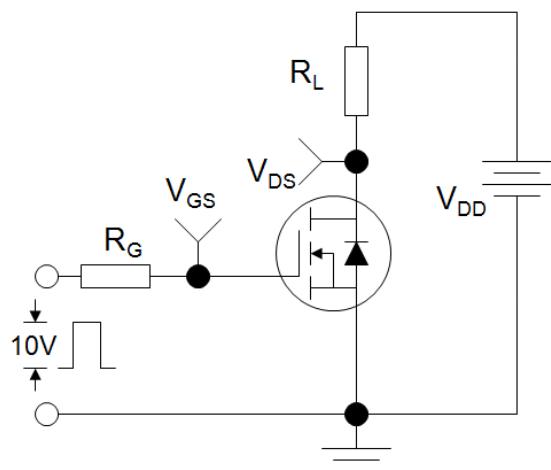
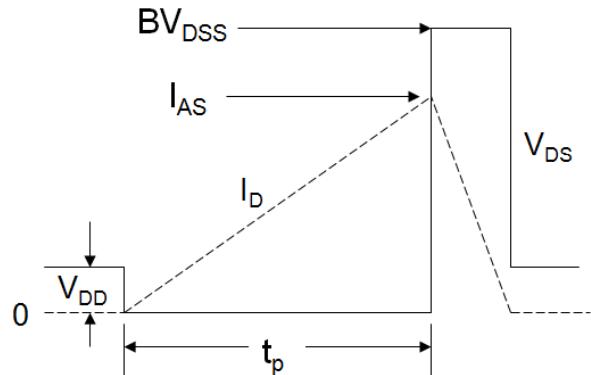
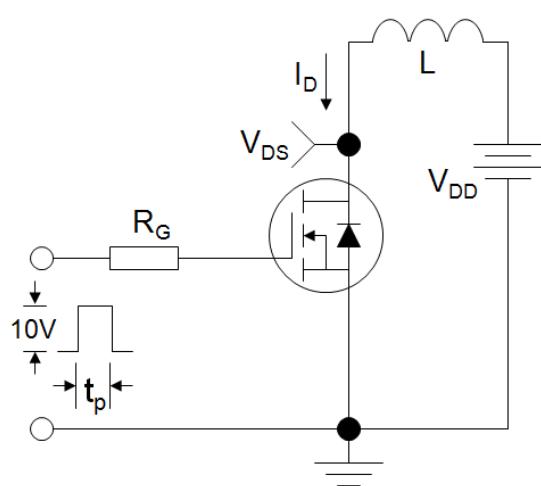
<b>Absolute Maximum Ratings</b> $T_C = 25^\circ\text{C}$ , unless otherwise noted			
<b>Parameter</b>	<b>Symbol</b>	<b>Value</b>	<b>Unit</b>
Drain-Source Voltage	$V_{DS}$	30	V
Continuous Drain Current	$I_D$	30	A
Pulsed Drain Current (note1)	$I_{DM}$	80	A
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Power Dissipation	$P_D$	33	W
Single pulse avalanche energy (note3)	$E_{AS}$	31	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 To 150	$^\circ\text{C}$
<b>Thermal Resistance</b>			
<b>Parameter</b>	<b>Symbol</b>	<b>Value</b>	<b>Unit</b>
Thermal Resistance, Junction-to-Case	$R_{thJC}$	3.8	$^\circ\text{C}/\text{W}$

**Specifications**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
<b>Static Parameters</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{GS} = 0V, I_D = 250\mu\text{A}$	30	--	--	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{DS} = 30V, V_{GS} = 0V$	--	--	1	$\mu\text{A}$
Gate-Source Leakage	$I_{\text{GSS}}$	$V_{GS} = \pm 20V$	--	--	$\pm 100$	nA
Gate-Source Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1	1.5	2.5	V
Drain-Source On-Resistance	$R_{DS(\text{on})}$	$V_{GS} = 10V, I_D = 10\text{A}$	--	12	20	$\text{m}\Omega$
		$V_{GS} = 4.5V, I_D = 8\text{A}$	--	17	24	
Forward Transconductance	$g_{\text{FS}}$	$V_{DS}=5V, I_D=10\text{A}$	26	--	--	S
<b>Dynamic Parameters</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{GS} = 0V,$ $V_{DS} = 15V,$ $f = 1.0\text{MHz}$	--	923	--	pF
Output Capacitance	$C_{\text{oss}}$		--	100	--	
Reverse Transfer Capacitance	$C_{\text{rss}}$		--	94	--	
Total Gate Charge	$Q_g$	$V_{DD} = 15V,$ $I_D = 10\text{A},$ $V_{GS} = 10V$	--	18	--	nC
Gate-Source Charge	$Q_{gs}$		--	3	--	
Gate-Drain Charge	$Q_{gd}$		--	4	--	
Turn-on Delay Time	$t_{d(\text{on})}$	$V_{DD} = 15V,$ $I_D = 10\text{A},$ $R_G = 3\Omega$	--	5	--	ns
Turn-on Rise Time	$t_r$		--	12	--	
Turn-off Delay Time	$t_{d(\text{off})}$		--	19	--	
Turn-off Fall Time	$t_f$		--	6	--	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Body Diode Current	$I_S$	$T_C = 25^\circ\text{C}$	--	--	30	A
Body Diode Voltage	$V_{SD}$	$T_J = 25^\circ\text{C}, I_{SD} = 10\text{A}, V_{GS} = 0V$	--	--	1.2	V
Reverse Recovery Time	$\text{Tr}_{\text{rr}}$	$I_S = 10\text{A}, V_{GS} = 0V$ $di/dt = 100\text{A}/\mu\text{s}$	--	19	--	ns
Reverse Recovery Charge	$Q_{\text{rr}}$		--	10	--	nC

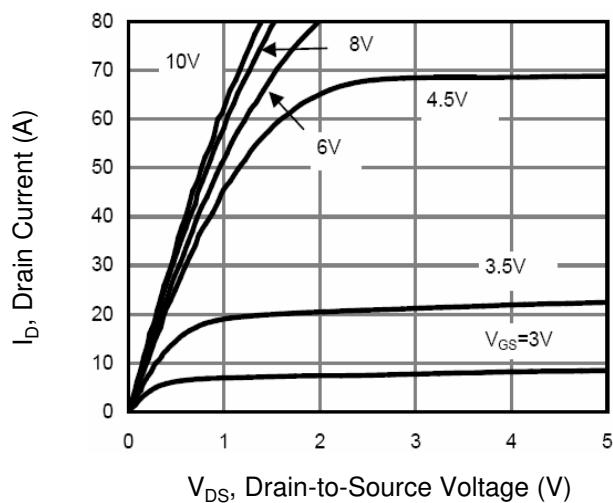
**Notes**

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. Identical low side and high side switch with identical  $R_G$
3. EAS condition :  $T_J=25^\circ\text{C}$ ,  $V_{DD}=30V$ ,  $V_{GS}=10V$ ,  $L=0.5\text{mH}$ ,  $R_g=25\Omega$

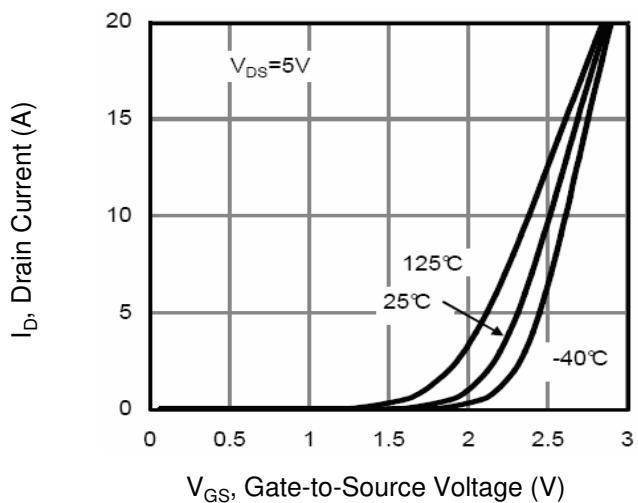
**Gate Charge Test Circuit****Switch Time Test Circuit****EAS Test Circuit**

**Typical Characteristics**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

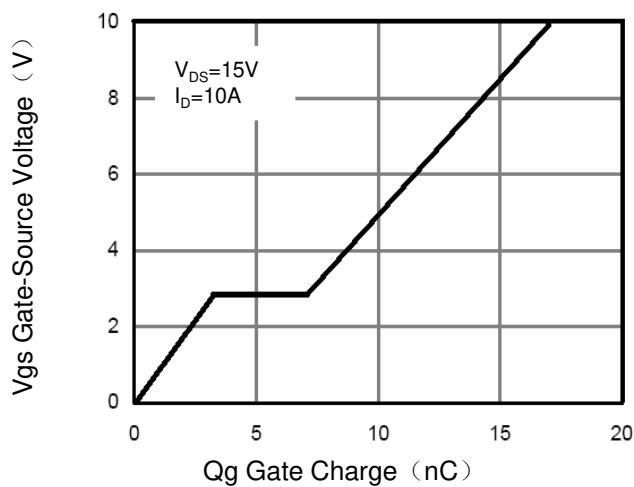
**Figure 1. Output Characteristics**



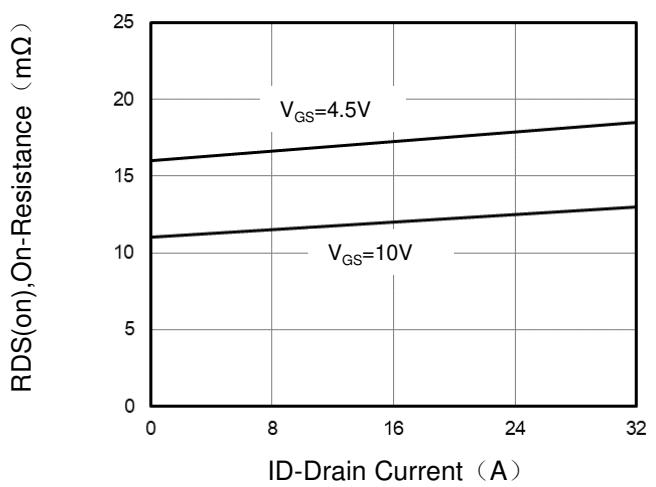
**Figure 2. Transfer Characteristics**



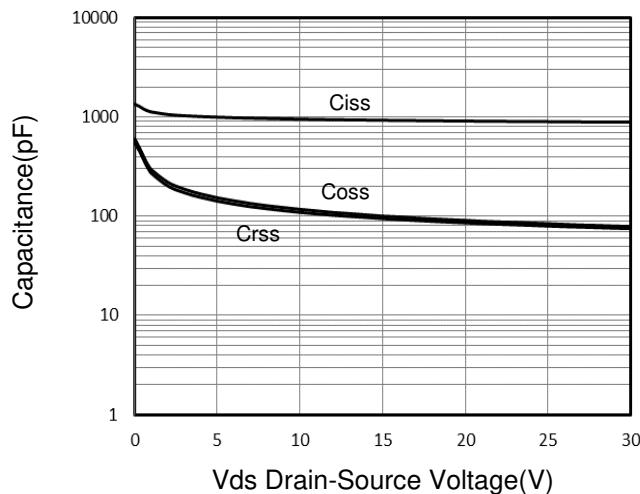
**Figure 3. Gate Charge**



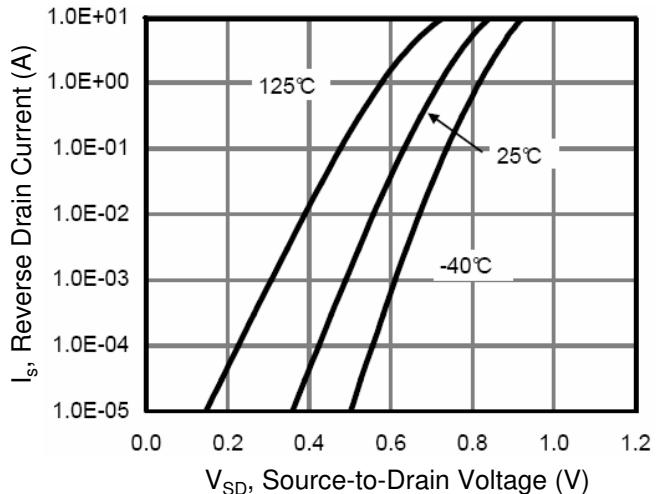
**Figure 4. Drain Source On Resistance**



**Figure 5. Capacitance**

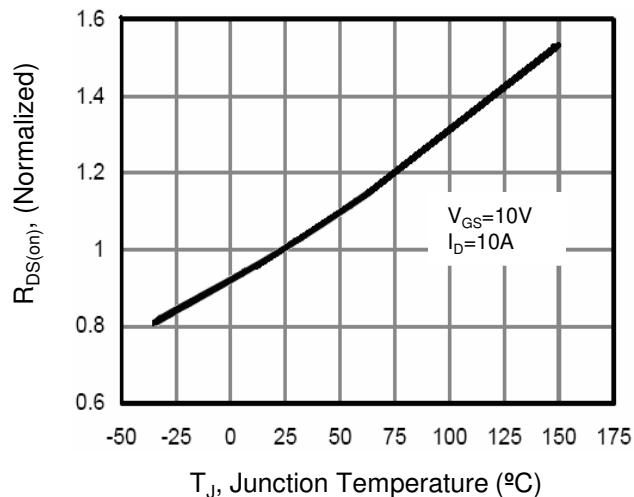


**Figure 6. Source-Drain Diode Forward**

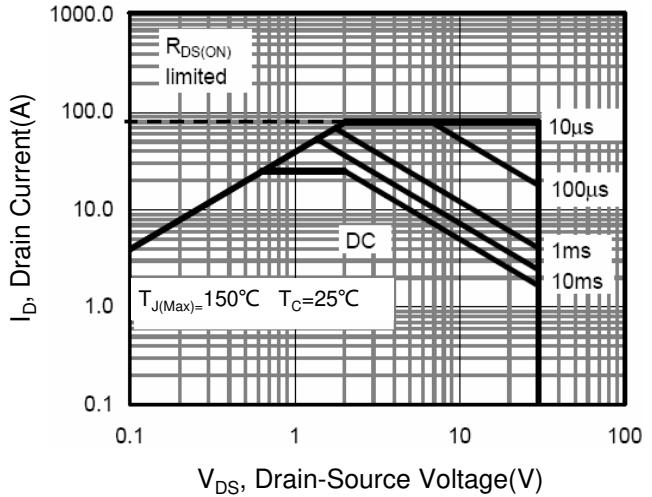


**Typical Characteristics**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

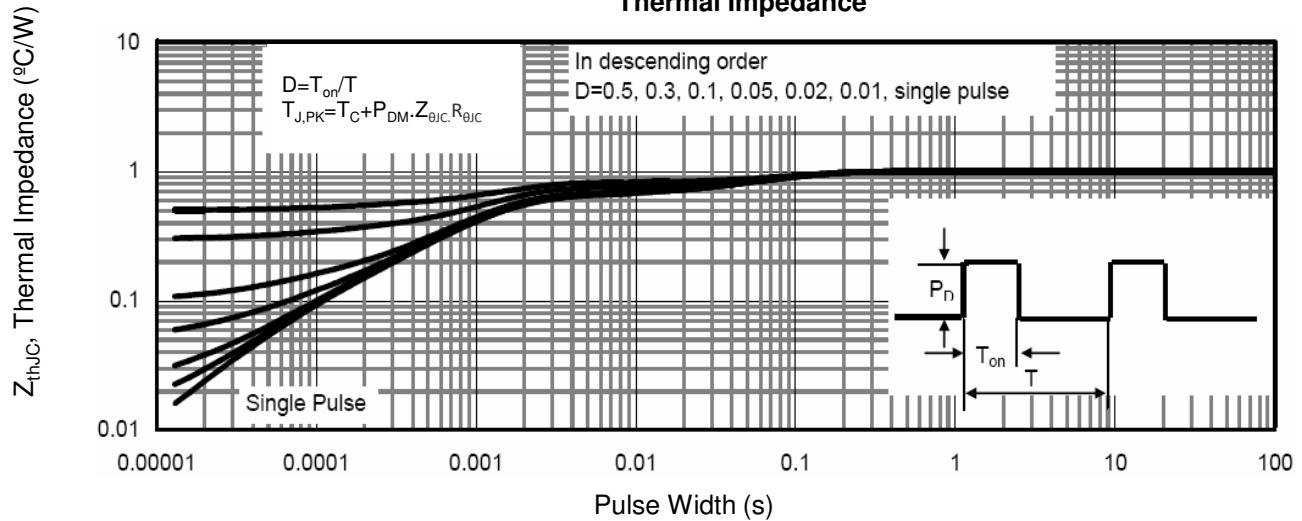
**Figure 7. Drain-Source On-Resistance**



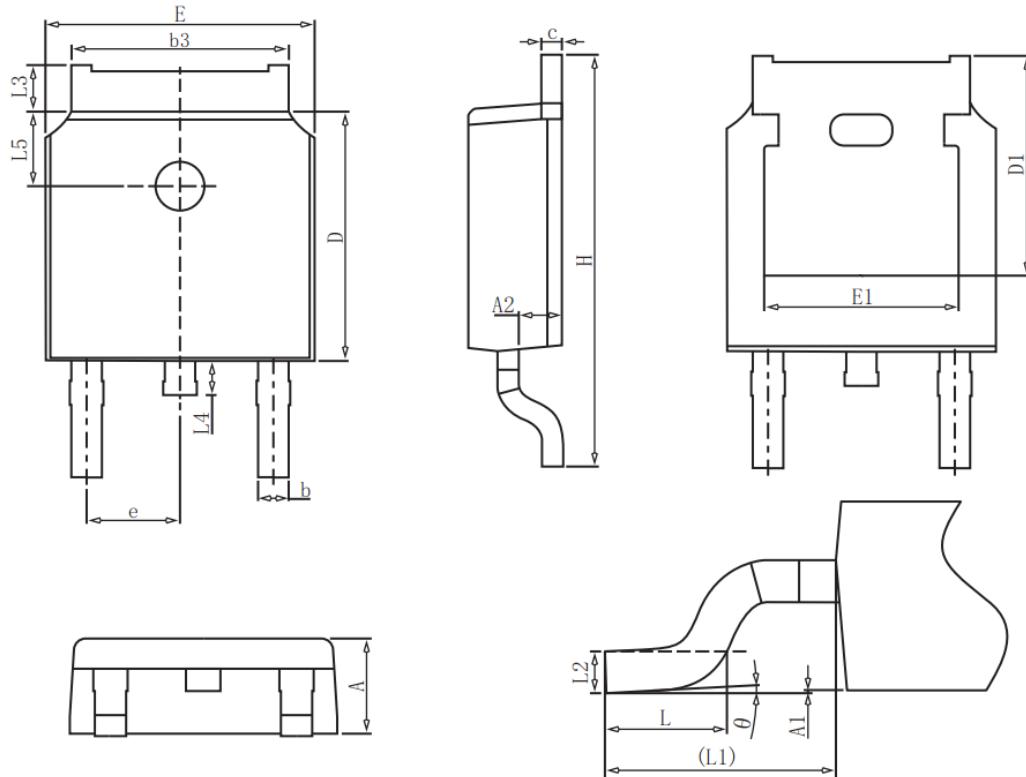
**Figure 8. Safe Operation Area**



**Figure 9. Normalized Maximum Transient Thermal Impedance**



## TO-252 Package Information



Symbol	Dimensions in Millimeters		
	MIN.	NOM.	MAX.
A	2.2	2.3	2.4
A1	0		0.2
A2	0.97	1.07	1.17
b	0.68	0.78	0.9
b3	5.2	5.33	5.5
c	0.43	0.53	0.63
D	5.98	6.1	6.22
D1	5.30REF		
E	6.4	6.6	6.8
E1	4.63		
e	2.286BSC		
H	9.4	10.1	10.5
L	1.38	1.5	1.75
L1	2.90REF		
L2	0.51BSC		
L3	0.88		1.28
L4	0.5		1
L5	1.65	1.8	1.95
θ	0°		8°