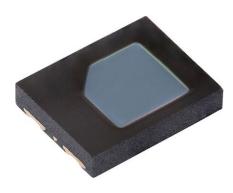


## **Ambient Light Sensor**



#### **DESCRIPTION**

VEMD5510C is a high speed and high sensitive PIN photodiode. It is a low profile surface-mount device (SMD) including the chip with a 7.5 mm<sup>2</sup> sensitive area detecting visible light much like the human eye. The diode has its peak sensitivity at 550 nm and a low capacitance.

#### **FEATURES**

- Package type: surface-mount
- Package form: top view
- Dimensions (L x W x H in mm): 5 x 4 x 0.9
- Radiant sensitive area (in mm<sup>2</sup>): 7.5
- · High photo sensitivity
- Suppression filter for near infrared radiation
- Fast response times
- Angle of half sensitivity:  $\varphi = \pm 65^{\circ}$
- Floor life: 72 h, MSL 4, according to J-STD-020
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



GREEN (5-2008)

### **APPLICATIONS**

- Wearables
- · Optical heart rate monitoring
- · Ambient light sensors

PRODUCT SUMMARY				
COMPONENT	I <sub>ra</sub> (μΑ)	φ <b>(°)</b>	λ <sub>0.5</sub> (nm)	
VEMD5510C	0.6	± 65	440 to 700	

#### Note

• Test conditions see table "Basic Characteristics"

ORDERING INFORMATION				
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM	
VEMD5510C	Tape and reel	MOQ: 1000 pcs, 1000 pcs/reel	Top view	
VEMD5510C-GS15	Tape and reel	MOQ: 5000 pcs, 5000 pcs/reel	Top view	

### Note

• MOQ: minimum order quantity

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		$V_{R}$	20	V	
Power dissipation	T <sub>amb</sub> ≤ 25 °C	P <sub>V</sub>	215	mW	
Junction temperature		T <sub>j</sub>	110	°C	
Operating temperature range		T <sub>amb</sub>	-40 to +100	°C	
Storage temperature range		T <sub>stg</sub>	-40 to +100	°C	
Soldering temperature	According to reflow solder profile Fig. 8	T <sub>sd</sub>	260	°C	
Thermal resistance junction-to-ambient		R <sub>thJA</sub>	350	K/W	
ESD safety HBM	± 2000 V, 1.5 kΩ, 100 pF, 3 pulses	ESD <sub>HBM</sub>	≥ 2	kV	



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<b>BASIC CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I <sub>F</sub> = 50 mA	V <sub>F</sub>	-	0.9	1.3	V
Breakdown voltage	I <sub>R</sub> = 100 μA, E = 0	V <sub>(BR)</sub>	20	-	=	V
Reverse dark current	V <sub>R</sub> = 10 V, E = 0	I <sub>ro</sub>	-	0.2	10	nA
Diode capacitance	$V_R = 0 V, f = 1 MHz, E = 0$	C <sub>D</sub>	-	80	=	pF
	$V_R = 3 V, f = 1 MHz, E = 0$	C <sub>D</sub>	-	30	40	pF
Open circuit voltage	E <sub>V</sub> = 100 lx, CIE illuminant A	Vo	-	240	=	mV
Temperature coefficient of Vo	E <sub>V</sub> = 100 lx, CIE illuminant A	TK <sub>Vo</sub>	-	-2.5	=	mV/K
Short circuit current	E <sub>V</sub> = 100 lx, CIE illuminant A	l <sub>k</sub>	-	0.6	=	μΑ
Davoras light augrent	$E_e = 0.2 \text{ mW/cm}^2$ , $\lambda = 525 \text{ nm}$ , $V_R = 5 \text{ V}$	I <sub>ra</sub>	2.0	2.6	=	μΑ
Reverse light current	$E_V = 100 \text{ lx, CIE illuminant A, } V_R = 5 \text{ V}$	I <sub>ra</sub>	0.46	0.55	=	μΑ
Angle of half sensitivity		φ	-	± 65	=	0
Wavelength of peak sensitivity		$\lambda_{p}$	-	550	ı	nm
Range of spectral bandwidth		λ <sub>0.5</sub>	-	440 to 700	ı	nm
Rise time	$V_R = 5 \text{ V}, \ R_L = 50 \ \Omega, \ \lambda = 525 \ \text{nm}$	t <sub>r</sub>	-	40		ns
Fall time	$V_R = 5 \text{ V}, \ R_L = 50 \ \Omega, \ \lambda = 525 \ \text{nm}$	t <sub>f</sub>	-	30	-	ns

### **BASIC CHARACTERISTICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)

Basic characteristics graphs to be extended to 110 °C ambient temperatures where applicable.

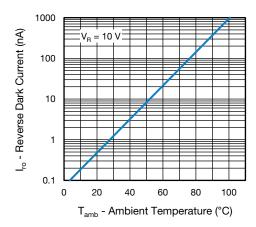


Fig. 1 - Reverse Dark Current vs. Ambient Temperature

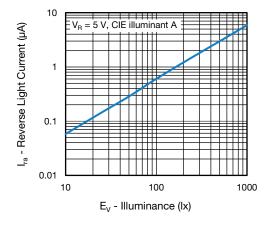


Fig. 2 - Reverse Light Current vs. Irradiance



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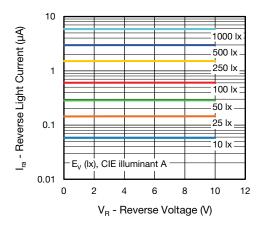


Fig. 3 - Reverse Light Current vs. Reverse Voltage

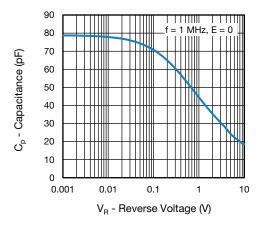


Fig. 4 - Diode Capacitance vs. Reverse Voltage

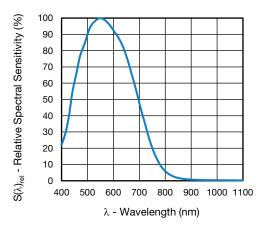


Fig. 5 - Relative Spectral Sensitivity vs. Wavelength

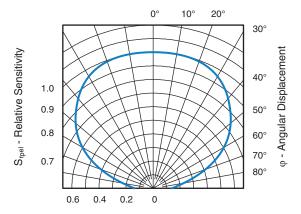
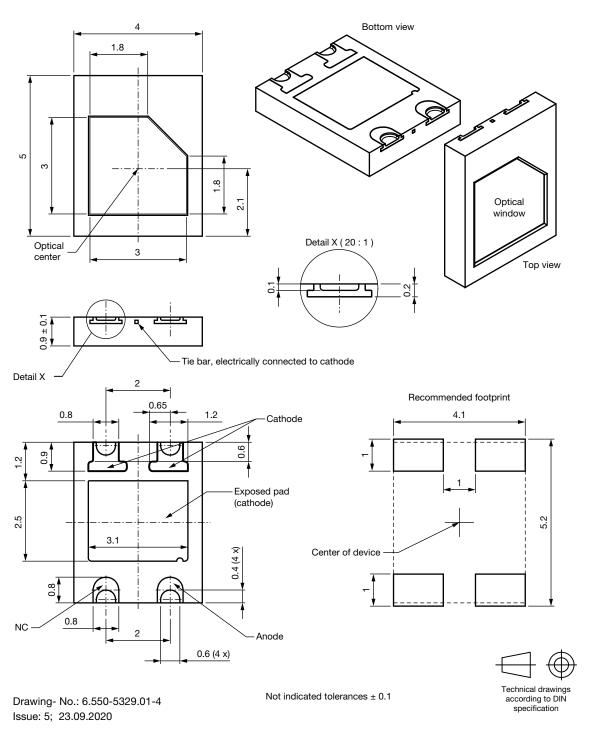


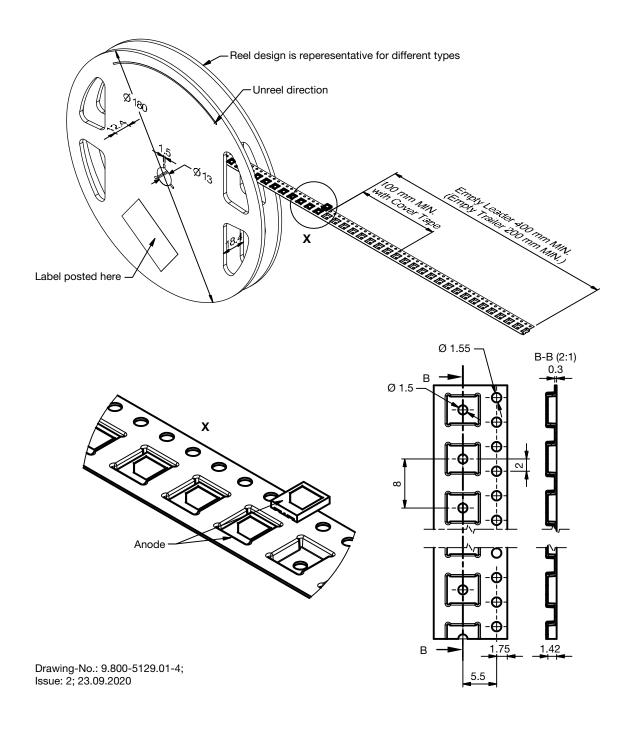
Fig. 6 - Relative Sensitivity vs. Angular Displacement



### **PACKAGE DIMENSIONS** in millimeters



### TAPE AND REEL DIMENSIONS in millimeters





#### **SOLDER PROFILE**

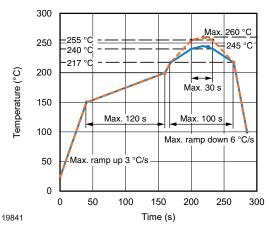


Fig. 7 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020D

#### **DRYPACK**

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

#### **FLOOR LIFE**

Time between soldering and removing from MBB must not exceed the time indicated in J-STD-020:

Moisture sensitivity: level 4

Floor life: 72 h

Conditions:  $T_{amb}$  < 30 °C, RH < 60 %

#### **DRYING**

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or recommended conditions:

192 h at 40 °C (+ 5 °C), RH < 5 %

or

96 h at 60 °C (+ 5 °C), RH < 5 %

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