TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSIV)

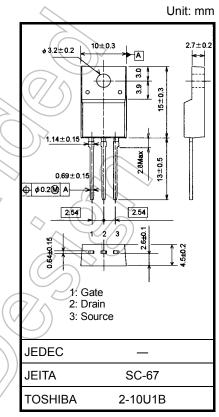
2SK3565

Switching Regulator Applications

- Low drain-source ON resistance: R_{DS} (ON) = 2.0 Ω (typ.)
- High forward transfer admittance: |Y_{fs}| = 4.5 S (typ.)
- Low leakage current: I_{DSS} = 100 μA (V_{DS} = 720 V)
- Enhancement mode: V_{th} = 2.0 to 4.0 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V _{DSS}	900	V
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V _{DGR}	900	V
Gate-source voltage		V _{GSS}	±30	> v
Drain current	DC (Note 1)	Ι _D	5	
	Pulse (t = 1 ms) (Note 1)	I _{DP}	15	A
Drain power dissipation (Tc = 25° C)		PD	45	$\langle w \rangle$
Single pulse avalanche energy (Note 2)		EAS	595	mJ
Avalanche current		TAR	5	A
Repetitive avalanche energy (Note 3)		EAR	4.5	ĻmJ
Channel temperature		7 Ich	150	°C
Storage temperature range		T _{stg}	-55~150	∽℃



Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

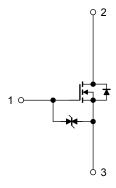
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	2.78	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W



Note 2: $V_{DD} = 90 \text{ V}, \text{ T}_{ch} = 25^{\circ}\text{C}(\text{Initial}), \text{ L} = 43.6 \text{ mH}, \text{ I}_{AR} = 5.0 \text{ A}, \text{ R}_{G} = 25 \Omega$

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.



Start of commercial production 2002-06

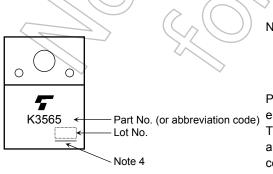
Electrical Characteristics (Ta = 25°C)

Char	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rent	I _{GSS}	$V_{GS}=\pm 25~V,~V_{DS}=0~V$			±10	μA
Gate-source brea	akdown voltage	V (BR) GSS	$I_G = \pm 10 \ \mu A, \ V_{DS} = 0 \ V$	±30			V
Drain cut-off curr	ent	I _{DSS}	$V_{DS} = 720 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$		—	100	μA
Drain-source bre	akdown voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	900		—	V
Gate threshold v	oltage	V _{th}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	2.0)}_	4.0	V
Drain-source ON	resistance	R _{DS (ON)}	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 3 \text{ A}$		2.0	2.5	Ω
Forward transfer	admittance	Y _{fs}	$V_{DS} = 20 V, I_D = 3 A$	2.0	4.5	—	S
Input capacitance	e	C _{iss}			1150	_	
Reverse transfer capacitance		C _{rss}	$V_{DS} = 25 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$	7 —	20	_	pF
Output capacitance		C _{oss}			100	_	
Switching time	Rise time	tr	V_{GS} $U_D = 3 $ V_{OUT}	- (30	\geq	
	Turn-on time	t _{on}	$\begin{array}{c} 0 \\ 0 \\ 0 \\ 50 \\ \Omega \\ \end{array} \begin{array}{c} 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 $		70) —	20
	Fall time	t _f	$\begin{array}{c} 00.1 12 \\ 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 77 $		60	_	ns
	Turn-off time	t _{off}	Duty \leq 1%, t _w = 10 μ s		170	_	
Total gate charge Q _g		Qg) —	28		
Gate-source charge Q _{gs}		Qgs	$V_{DD} \simeq 400 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 5 \text{ A}$		17		nC
Gate-drain charge Qgd		Qgd			11		

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)			_	_	5	А
Pulse drain reverse current (Note 1)	IDRP	$(\sqrt{2})$ –	_	_	15	А
Forward voltage (diode)	VDSF	I _{DR} = 5 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	tr	I _{DR} = 5 A, V _{GS} = 0 V,	_	900	_	ns
Reverse recovery charge	Q _{rr}	dI _{DR} /dt = 100 A/µs		5.4	_	μC

Marking

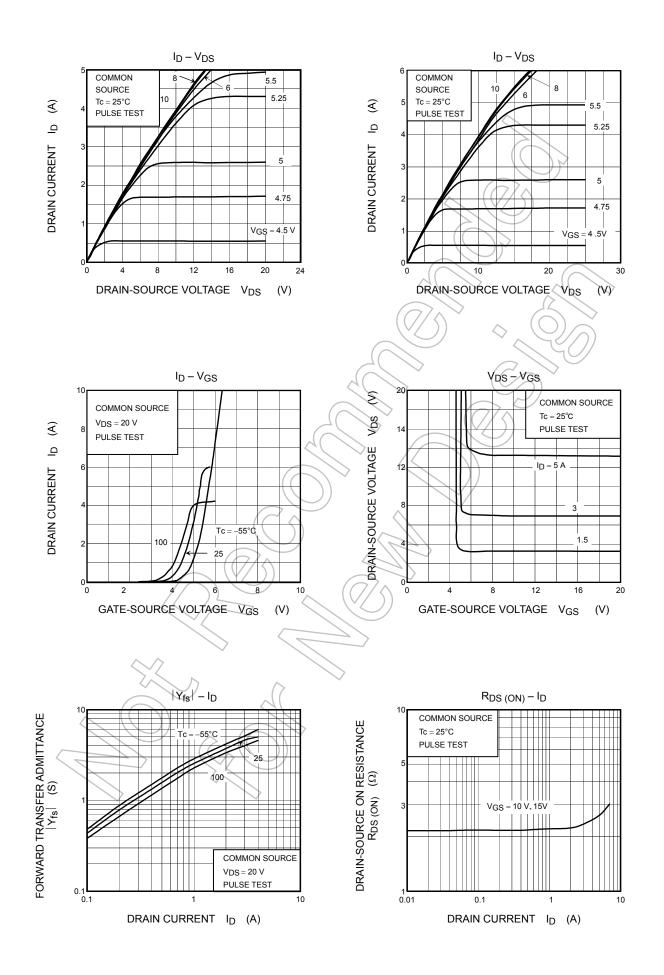


Note 4: A line under a Lot No. identifies the indication of product Labels.

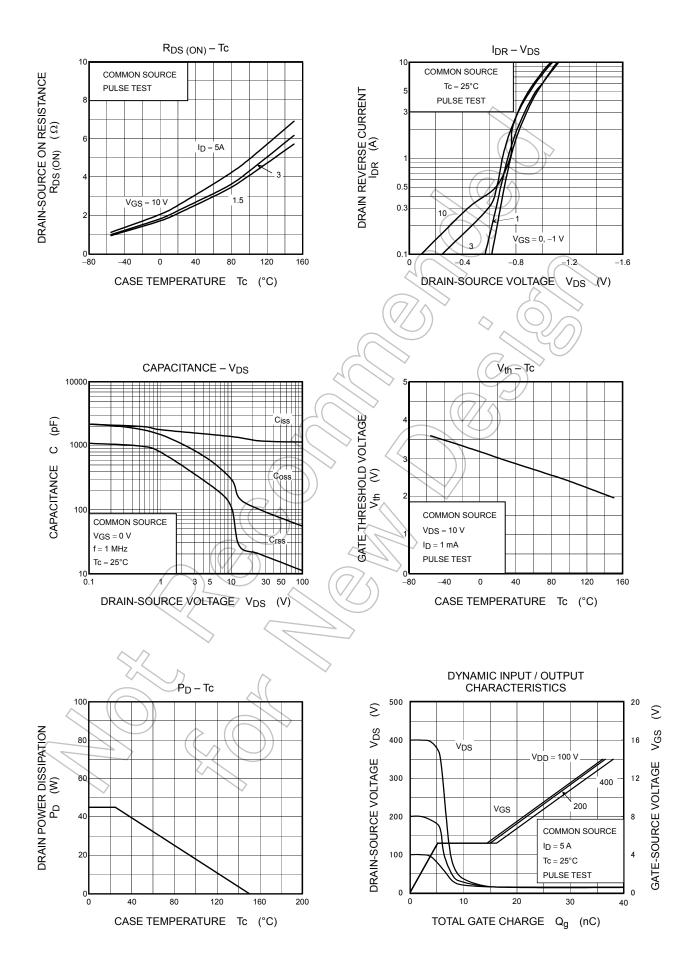
Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

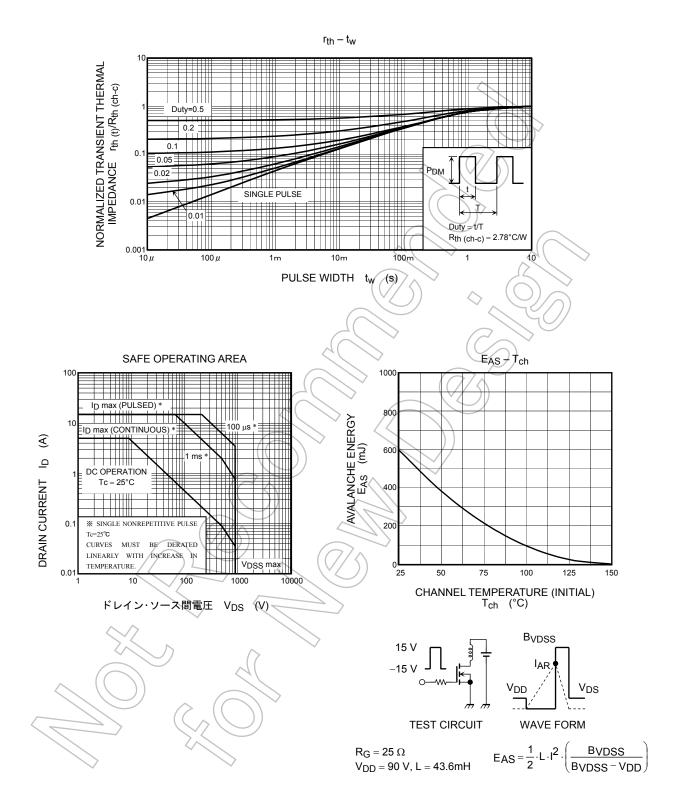
Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

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