Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π -MOSVII)

TK2P60D

Switching Regulator Applications

• Low drain-source ON-resistance: RDS (ON) = 3.3 Ω (typ.)

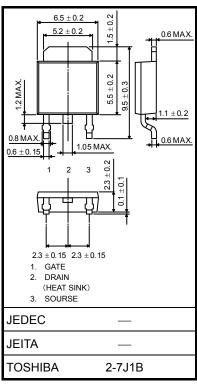
• High forward transfer admittance: $|Y_{fs}| = 1.0 \text{ S (typ.)}$

• Low leakage current: $I_{DSS} = 10 \mu A (V_{DS} = 600 \text{ V})$

• Enhancement-mode: $V_{th} = 2.4 \text{ to } 4.4 \text{ V (VDS} = 10 \text{ V, ID} = 1 \text{ mA)}$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	600	V	
Gate-source voltage		V _{GSS}	±30	V	
Drain current	DC (Note 1)	I _D	2		
	Pulse (t = 1 ms) (Note 1)	I _{DP}	8	Α	
Drain power dissipati	on (Tc = 25°C)	PD	60	W	
Single pulse avalanche energy (Note 2)		E _{AS}	101	mJ	
Avalanche current		I _{AR}	2	Α	
Repetitive avalanche energy (Note 3)		E _{AR}	6	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	



Weight: 0.36 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

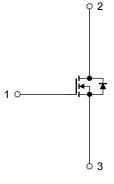
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	2.08	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	125	°C/W

Note 1: Please use devices on conditions that the channel temperature is below 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 44.1 mH, R_G = 25 Ω , I_{AR} = 2 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic sensitive device. Please handle with caution.



Start of commercial production 2009-09

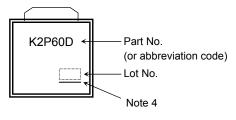
Electrical Characteristics (Ta = 25°C)

Charac	eteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μΑ
Drain cut-off currer	nt	I _{DSS}	V _{DS} = 600 V, V _{GS} = 0 V	_	_	10	μА
Drain-source break	kdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	600	_		٧
Gate threshold voltage		V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.4	_	4.4	٧
Drain-source ON-r	esistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 1 A		3.3	4.3	Ω
Forward transfer a	dmittance	Y _{fs}	V _{DS} = 10 V, I _D = 1 A	0.3	1.0	_	S
Input capacitance		C _{iss}		_	280	_	pF
Reverse transfer capacitance		C _{rss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	_	1.5	_	
Output capacitance		Coss		_	30	-	
Switching time	Rise time	t _r	$V_{GS} = 1 \text{ A Vout}$ $V_{GS} = 1 \text{ A Vout}$ $V_{GS} = 1 \text{ A Vout}$ $V_{DO} = 200 \Omega$ $V_{DD} \approx 200 \text{ V}$	_	15	_	
	Turn-on time	t _{on}			35		ns
	Fall time	t _f			7		115
	Turn-off time	t _{off}			55		
Total gate charge		Qg		_	7	_	nC
Gate-source charge		Q _{gs}	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 2 \text{ A}$	_	4	_	
Gate-drain charge		Q _{gd}		_	3	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

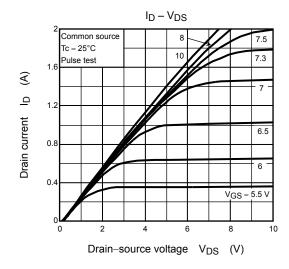
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	2	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	8	Α
Forward voltage (diode)	V _{DSF}	$I_{DR} = 2 A$, $V_{GS} = 0 V$	_	_	-1.7	٧
Reverse recovery time	t _{rr}	$I_{DR} = 2 \text{ A}, V_{GS} = 0 \text{ V},$	_	550	_	ns
Reverse recovery charge	Q _{rr}	dI _{DR} /dt = 100 A/μs	_	2.2	_	μС

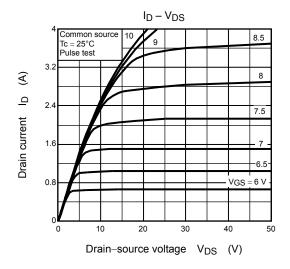
Marking

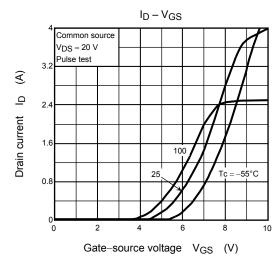


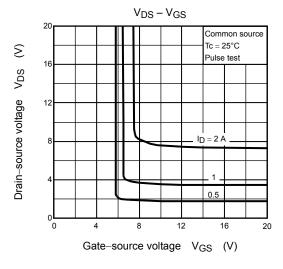
Note 4 : A line under a Lot No. identifies the indication of product Labels $\hbox{[[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]}$

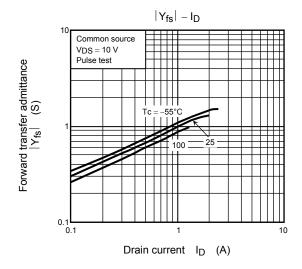
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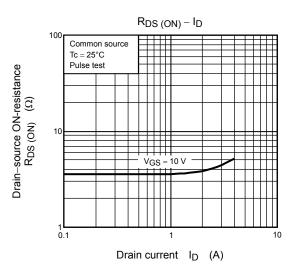




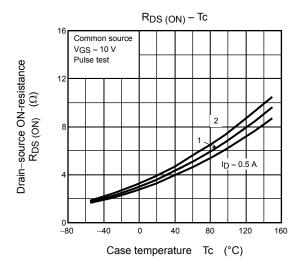


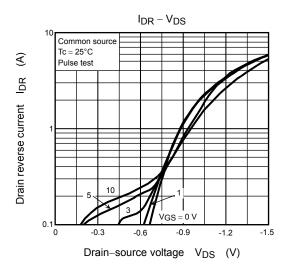


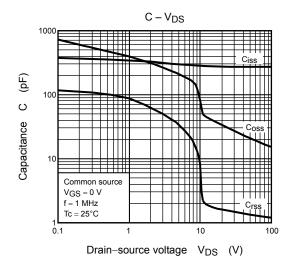


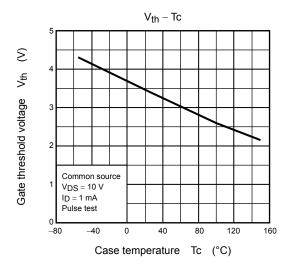


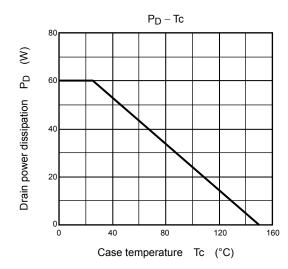
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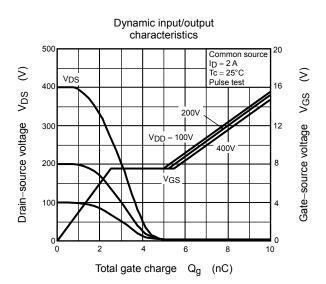


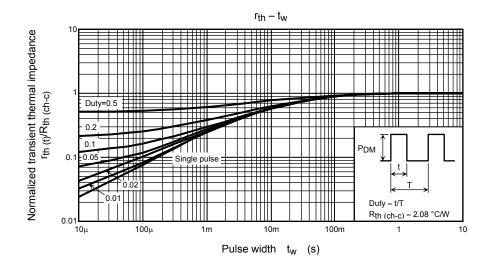


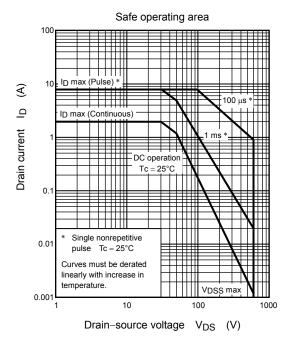


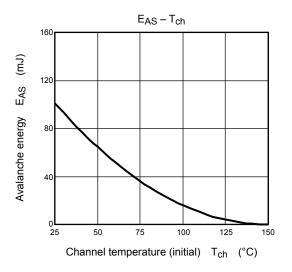


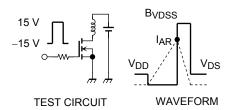












$$R_G = 25 \Omega$$

$$V_{DD} = 90 \text{ V, L} = 44.1 \text{ mH}$$

$$E_{AS} = \frac{1}{2} \cdot \text{L} \cdot \text{I}^2 \cdot \left(\frac{\text{BVDSS}}{\text{BVDSS} - \text{VDD}}\right)$$

5 2013-11-01

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