

# MMBT5401LT1

Preferred Device

## High Voltage Transistor

### PNP Silicon

#### Features

- Pb-Free Packages are Available

#### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	$V_{CEO}$	–150	Vdc
Collector–Base Voltage	$V_{CBO}$	–160	Vdc
Emitter–Base Voltage	$V_{EBO}$	–5.0	Vdc
Collector Current – Continuous	$I_C$	–500	mAdc

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

#### THERMAL CHARACTERISTICS

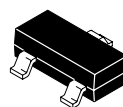
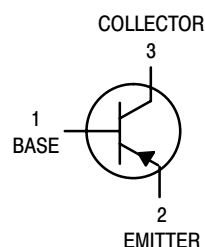
Characteristic	Symbol	Max	Unit
Total Device Dissipation FR–5 Board (Note 1) $T_A = 25^\circ\text{C}$ Derate Above $25^\circ\text{C}$	$P_D$	225	mW
Thermal Resistance, Junction–to–Ambient	$R_{\theta JA}$	556	$^\circ\text{C}/\text{W}$
Total Device Dissipation Alumina Substrate (Note 2) $T_A = 25^\circ\text{C}$ Derate Above $25^\circ\text{C}$	$P_D$	300	mW
Thermal Resistance, Junction–to–Ambient	$R_{\theta JA}$	417	$^\circ\text{C}/\text{W}$
Junction and Storage Temperature	$T_J, T_{stg}$	–55 to +150	$^\circ\text{C}$

- FR–5 =  $1.0 \times 0.75 \times 0.062$  in.
- Alumina =  $0.4 \times 0.3 \times 0.024$  in 99.5% alumina.



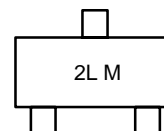
ON Semiconductor®

<http://onsemi.com>



SOT–23 (TO–236)  
CASE 318  
STYLE 6

#### MARKING DIAGRAM



2L = Device Code  
M = Month Code

#### ORDERING INFORMATION

Device	Package	Shipping†
MMBT5401LT1	SOT–23	3000 Tape & Reel
MMBT5401LT1G	SOT–23 (Pb–Free)	3000 Tape & Reel
MMBT5401LT3	SOT–23	10,000 Tape & Reel
MMBT5401LT3G	SOT–23 (Pb–Free)	10,000 Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Preferred devices are recommended choices for future use and best overall value.

# MMBT5401LT1

## ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
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### OFF CHARACTERISTICS

Collector–Emitter Breakdown Voltage (I <sub>C</sub> = –1.0 mA <sub>dc</sub> , I <sub>B</sub> = 0)	V <sub>(BR)CEO</sub>	–150	–	Vdc
Collector–Base Breakdown Voltage (I <sub>C</sub> = –100 μA <sub>dc</sub> , I <sub>E</sub> = 0)	V <sub>(BR)CBO</sub>	–160	–	Vdc
Emitter–Base Breakdown Voltage (I <sub>E</sub> = –10 μA <sub>dc</sub> , I <sub>C</sub> = 0)	V <sub>(BR)EBO</sub>	–5.0	–	Vdc
Collector Cutoff Current (V <sub>CB</sub> = –120 Vdc, I <sub>E</sub> = 0) (V <sub>CB</sub> = –120 Vdc, I <sub>E</sub> = 0, T <sub>A</sub> = 100°C)	I <sub>CES</sub>	–	–50	nA <sub>dc</sub> μA <sub>dc</sub>

### ON CHARACTERISTICS

DC Current Gain (I <sub>C</sub> = –1.0 mA <sub>dc</sub> , V <sub>CE</sub> = –5.0 Vdc) (I <sub>C</sub> = –10 mA <sub>dc</sub> , V <sub>CE</sub> = –5.0 Vdc) (I <sub>C</sub> = –50 mA <sub>dc</sub> , V <sub>CE</sub> = –5.0 Vdc)	h <sub>FE</sub>	50 60 50	– 240 –	–
Collector–Emitter Saturation Voltage (I <sub>C</sub> = –10 mA <sub>dc</sub> , I <sub>B</sub> = –1.0 mA <sub>dc</sub> ) (I <sub>C</sub> = –50 mA <sub>dc</sub> , I <sub>B</sub> = –5.0 mA <sub>dc</sub> )	V <sub>CE(sat)</sub>	– –	–0.2 –0.5	Vdc
Base–Emitter Saturation Voltage (I <sub>C</sub> = –10 mA <sub>dc</sub> , I <sub>B</sub> = –1.0 mA <sub>dc</sub> ) (I <sub>C</sub> = –50 mA <sub>dc</sub> , I <sub>B</sub> = –5.0 mA <sub>dc</sub> )	V <sub>BE(sat)</sub>	– –	–1.0 –1.0	Vdc

### SMALL–SIGNAL CHARACTERISTICS

Current–Gain — Bandwidth Product (I <sub>C</sub> = –10 mA <sub>dc</sub> , V <sub>CE</sub> = –10 Vdc, f = 100 MHz)	f <sub>T</sub>	100	300	MHz
Output Capacitance (V <sub>CB</sub> = –10 Vdc, I <sub>E</sub> = 0, f = 1.0 MHz)	C <sub>obo</sub>	–	6.0	pF
Small Signal Current Gain (I <sub>C</sub> = –1.0 mA <sub>dc</sub> , V <sub>CE</sub> = –10 Vdc, f = 1.0 kHz)	h <sub>fe</sub>	40	200	–
Noise Figure (I <sub>C</sub> = –200 μA <sub>dc</sub> , V <sub>CE</sub> = –5.0 Vdc, R <sub>S</sub> = 10 Ω, f = 1.0 kHz)	NF	–	8.0	dB

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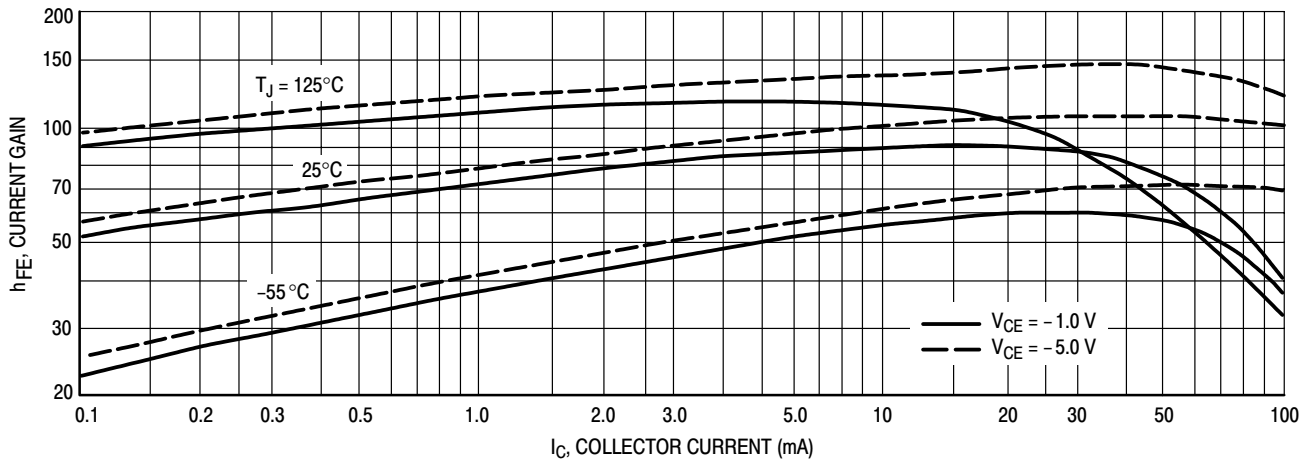


Figure 1. DC Current Gain

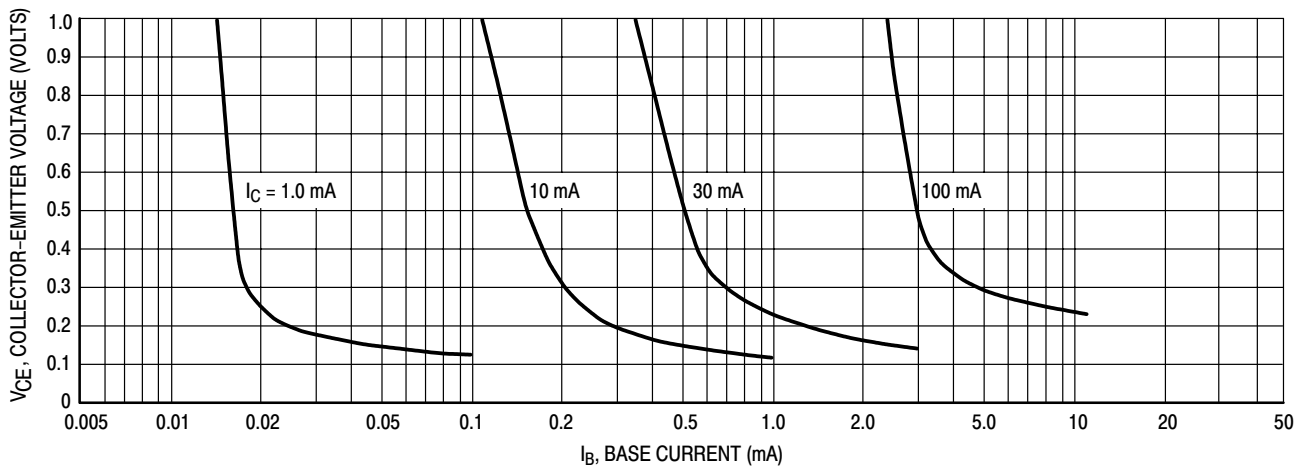


Figure 2. Collector Saturation Region

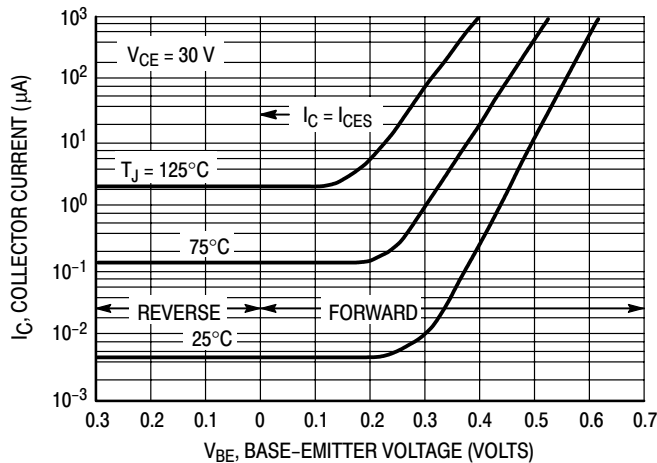


Figure 3. Collector Cut-Off Region

# MMBT5401LT1

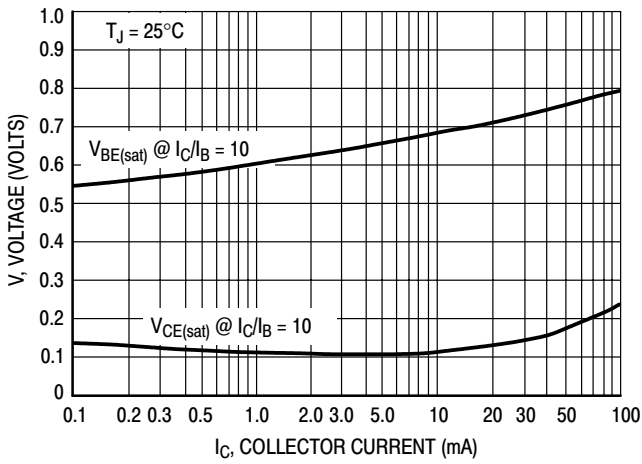


Figure 4. "On" Voltages

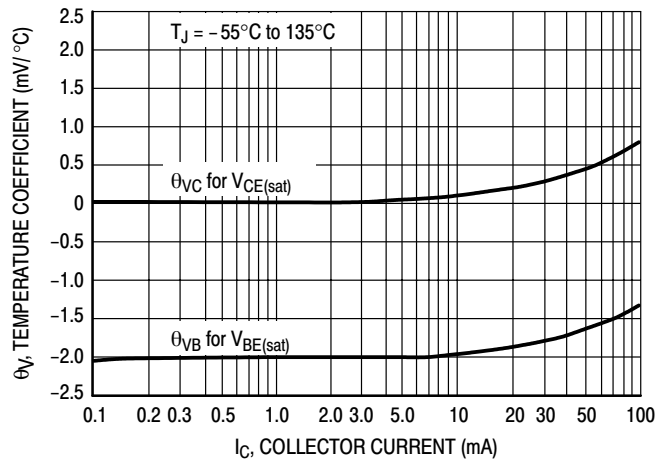


Figure 5. Temperature Coefficients

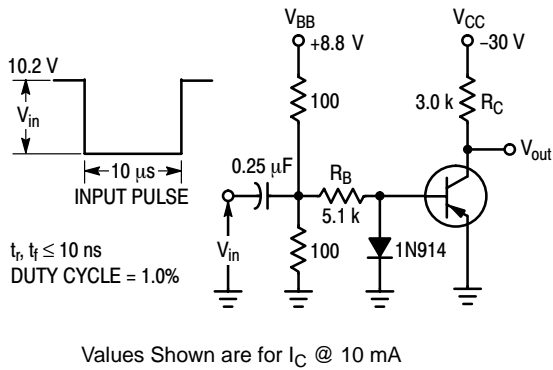


Figure 6. Switching Time Test Circuit

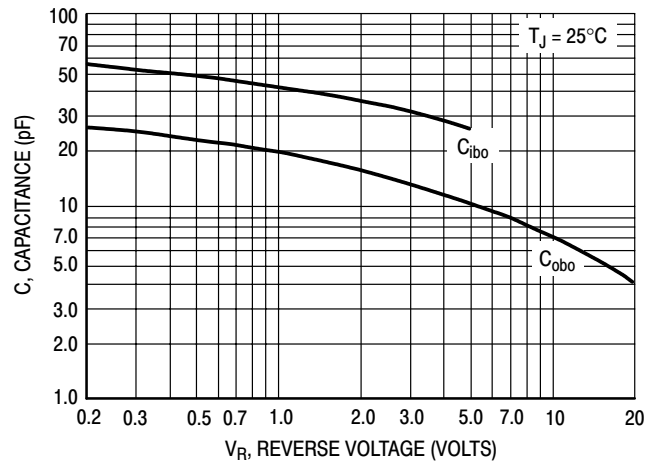


Figure 7. Capacitances

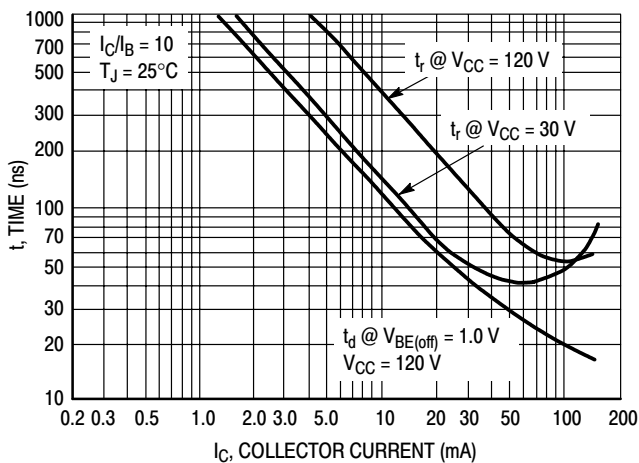


Figure 8. Turn-On Time

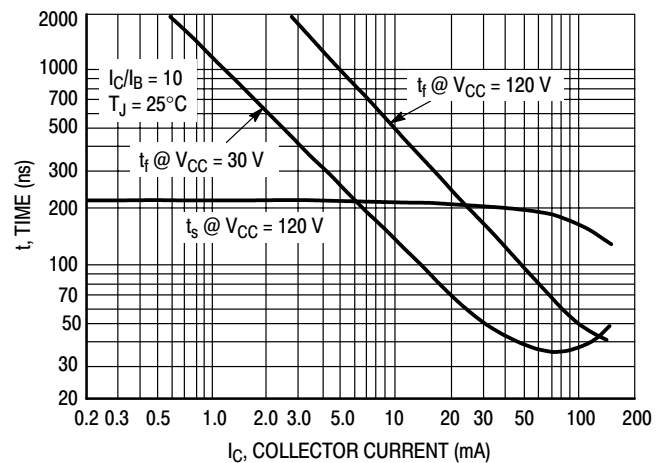
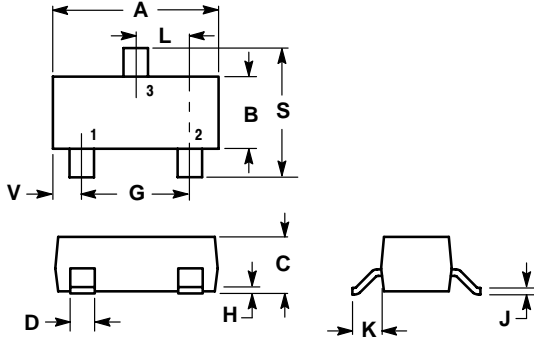


Figure 9. Turn-Off Time

# MMBT5401LT1

## PACKAGE DIMENSIONS

SOT-23-3 (TO-236)  
CASE 318-08  
ISSUE AK

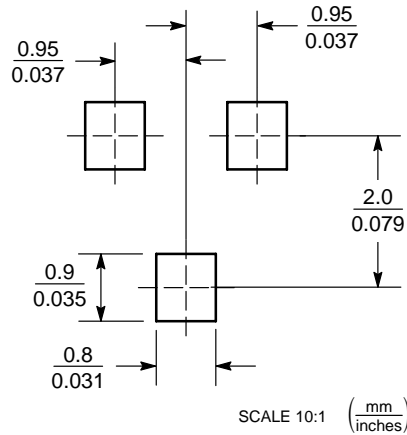


- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION: INCH.
  3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
  4. 318-01 THRU -07 AND -09 OBSOLETE, NEW STANDARD 318-08.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.1102	0.1197	2.80	3.04
B	0.0472	0.0551	1.20	1.40
C	0.0350	0.0440	0.89	1.11
D	0.0150	0.0200	0.37	0.50
G	0.0701	0.0807	1.78	2.04
H	0.0005	0.0040	0.013	0.100
J	0.0034	0.0070	0.085	0.177
K	0.0140	0.0285	0.35	0.69
L	0.0350	0.0401	0.89	1.02
S	0.0830	0.1039	2.10	2.64
V	0.0177	0.0236	0.45	0.60


- STYLE 6:  
PIN 1. BASE  
2. EMITTER  
3. COLLECTOR

## SOLDERING FOOTPRINT\*



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

# MMBT5401LT1

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