

To our customers,

Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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Phase-out/Discontinued

PNP SILICON EPITAXIAL TRANSISTOR (DARLINGTON CONNECTION)
FOR LOW-FREQUENCY POWER AMPLIFIERS AND LOW-SPEED SWITCHING

The 2SB1430 is a Darlington power transistor that can directly drive from the IC output. This transistor is ideal for motor drivers and solenoid drivers in such as OA and FA equipment.

In addition, this transistor features a small resin-molded insulation type package, thus contributing to high-density mounting and mounting cost reduction.

FEATURES

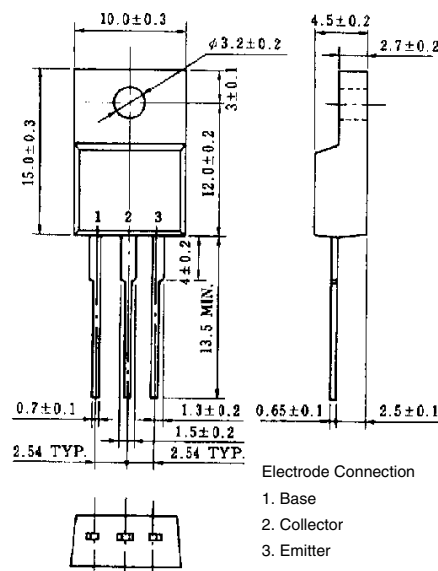
- High h_{FE} due to Darlington connection:
 $h_{FE} \geq 2,000$ ($V_{CE} = 2\text{ V}$, $I_C = 2\text{ A}$)
- Mold package that does not require an insulating board or insulation bushing

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$)

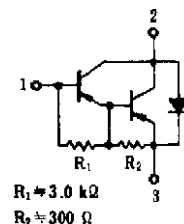
Parameter	Symbol	Ratings	Unit
Collector to base voltage	V_{CBO}	-100	V
Collector to emitter voltage	V_{CEO}	-100	V
Emitter to base voltage	V_{EBO}	-7.0	V
Collector current (DC)	$I_{C(DC)}$	-5.0	A
Collector current (pulse)	$I_{C(pulse)^*}$	-10	A
Base current (DC)	$I_{B(DC)}$	-0.5	A
Total power dissipation	P_T ($T_C = 25^\circ\text{C}$)	20	W
Total power dissipation	P_T ($T_A = 25^\circ\text{C}$)	2.0	W
Junction temperature	T_j	150	$^\circ\text{C}$
Storage temperature	T_{stg}	-55 to +150	$^\circ\text{C}$

* $PW \leq 10\text{ ms}$, duty cycle $\leq 50\%$

PACKAGE DRAWING (UNIT: mm)



EQUIVALENT CIRCUIT



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ELECTRICAL CHARACTERISTICS (T_A = 25°C)

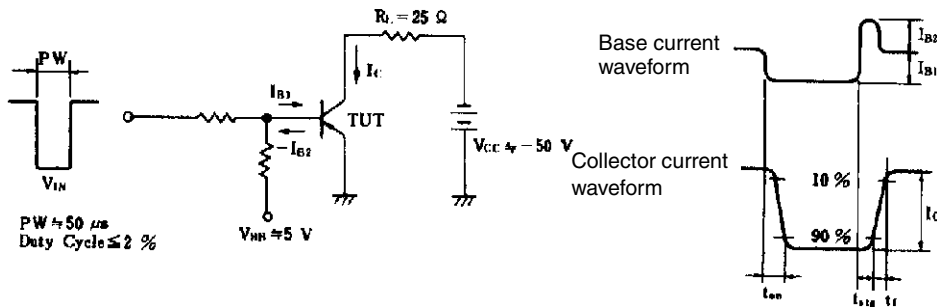
Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Collector cutoff current	I _{CBO}	V _{CB} = -100 V, I _E = 0			-1.0	μA
DC current gain	h _{FE1} *	V _{CE} = -2.0 V, I _C = -2.0 A	2,000		20,000	
DC current gain	h _{FE2} *	V _{CE} = -2.0 V, I _C = -4.0 A	500			
Collector saturation voltage	V _{CE(sat)} *	I _C = -2.0 A, I _B = -2.0 mA			-1.5	V
Base saturation voltage	V _{BE(sat)} *	I _C = -2.0 A, I _B = -2.0 mA			-2.0	V
Gain bandwidth product	f _T	V _{CE} = -5.0 V, I _C = -0.5 A		80		MHz
Collector capacitance	C _{ob}	V _{CB} = -10 V, I _E = 0, f = 1.0 MHz		60		pF
Turn-on time	t _{on}	I _C = -2.0 A, I _{B1} = -I _{B2} = -2.0 mA, R _L = 25 Ω, V _{CC} ≅ 50 V Refer to the test circuit.		0.5		μs
Storage time	t _{stg}			1.0		μs
Fall time	t _f			1.0		μs

* Pulse test PW ≤ 350 μs, duty cycle ≤ 2%

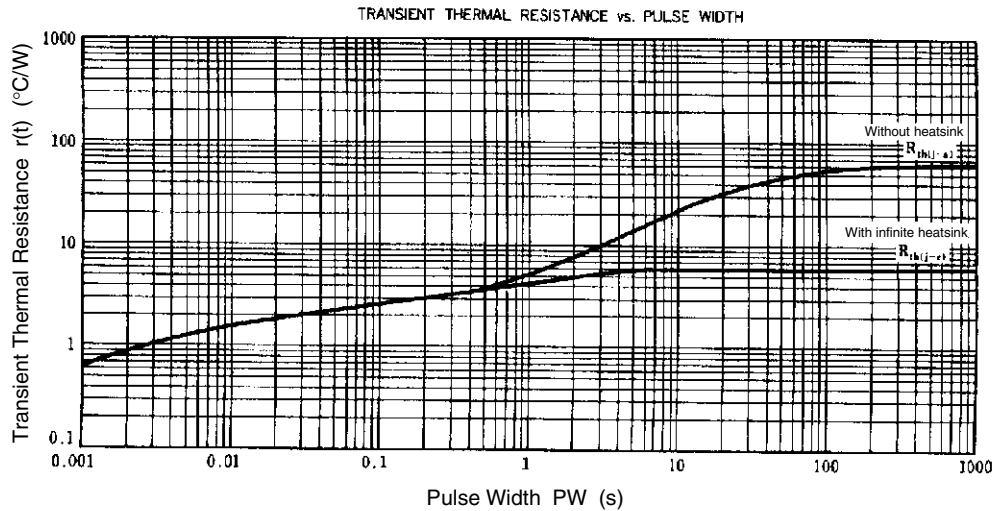
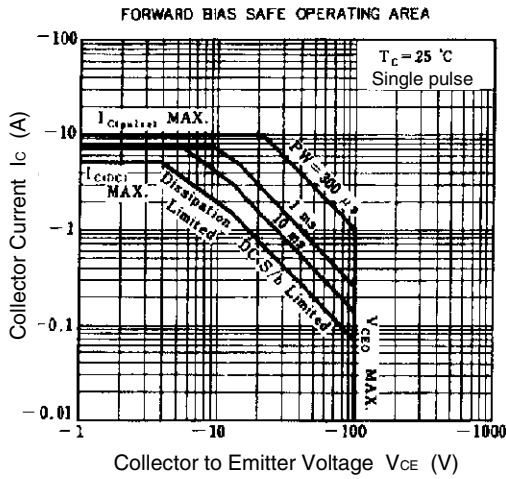
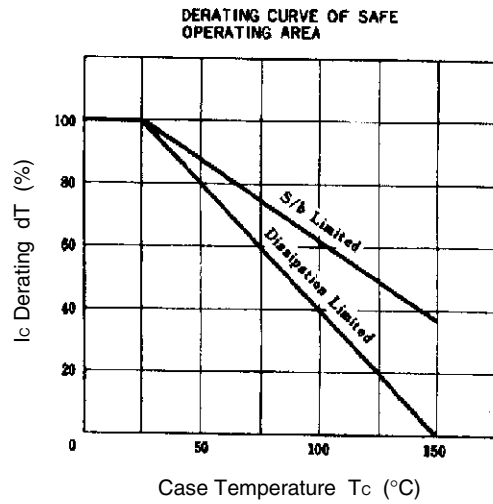
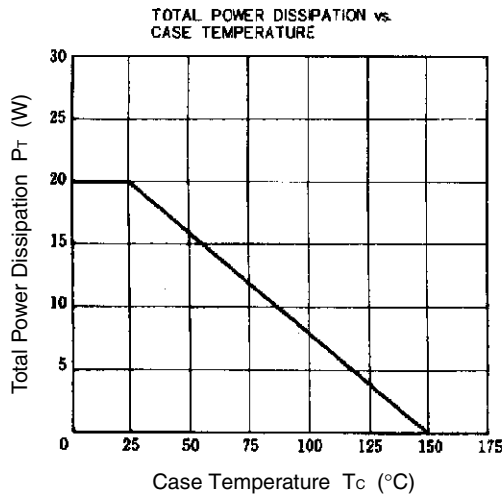
h_{FE} CLASSIFICATION

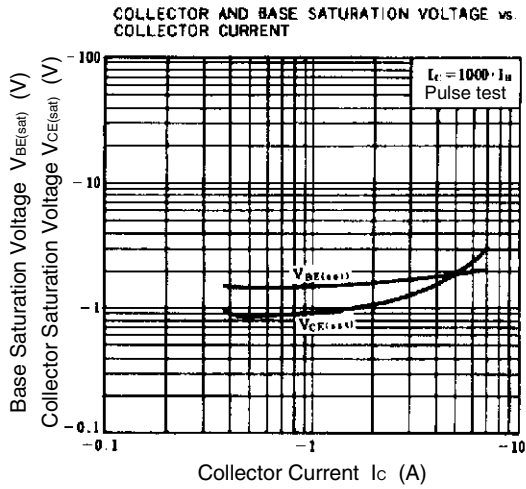
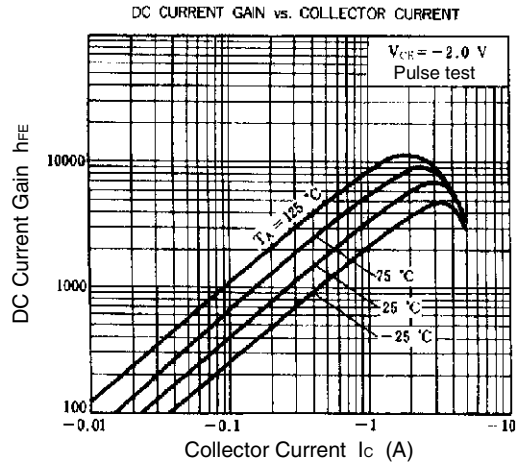
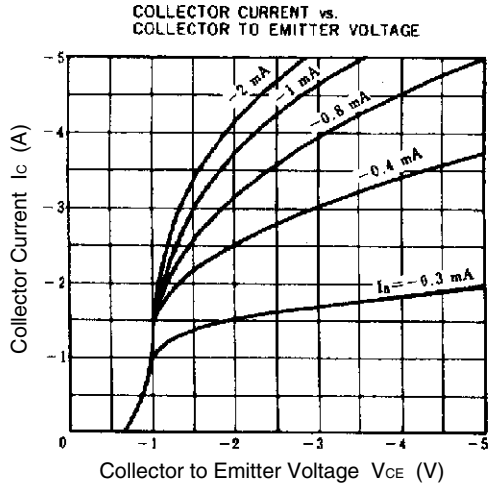
Marking	M	L	K
h _{FE1}	2,000 to 5,000	4,000 to 10,000	8,000 to 20,000

SWITCHING TIME (t_{on}, t_{stg}, t_f) TEST CIRCUIT



TYPICAL CHARACTERISTICS (T_A = 25°C)





[MEMO]

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