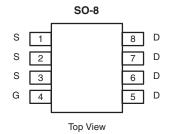




N-Channel 30-V (D-S) MOSFET with Schottky Diode

PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A) ^a	Q _g (Typ.)		
30	0.0085 at V _{GS} = 10 V	18	11.7 nC		
	0.0125 at V _{GS} = 4.5 V	15	11.7110		

SCHOTTKY AND BODY DIODE PRODUCT SUMMARY				
V _{DS} (V)	V _{SD} (V) Diode Forward Voltage	I _S (A)		
30	0.4 at 2 A	5 ^a		



Ordering Information: Si4322DY-T1-E3 (Lead (Pb)-free)

Si4322DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

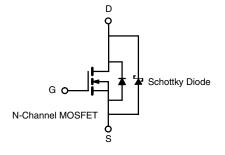
FEATURES

- Halogen-free According to IEC 61249-2-21 Available
- TrenchFET[®] Power MOSFET
- 100 % R_g Tested

ROHS COMPLIANT HALOGEN FREE Available

APPLICATIONS

- · Synchronous Buck-Low Side
 - Notebook
 - Server
 - Workstation
- Synchronous Rectifier-POL



ABSOLUTE MAXIMUM RATINGS T_A	= 25 °C, unless other	wise noted		
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	30	v	
Gate-Source Voltage	V_{GS}	± 20		
Continuous Drain Current (T _J = 150 °C)	T _C = 25 °C		18	
	T _C = 70 °C	l _D	15	
	T _A = 25 °C		14 ^{b, c}	
	T _A = 70 °C		11 ^{b, c}	Α
Pulsed Drain Current		I _{DM} 50		
Continuous Source-Drain Diode Current	T _C = 25 °C	I _S	5	
Continuous Source-Drain Diode Current	T _A = 25 °C	'S	2.8 ^{b, c}	
	T _C = 25 °C		5.4	
Maximum Power Dissipation	T _C = 70 °C	P_D	3.4	W
	T _A = 25 °C	' D	3.1 ^{b, c}	VV
	T _A = 70 °C		2.0 ^{b, c}	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Тур.	Max.	Unit	
Maximum Junction-to-Ambient ^{b, d}	t ≤ 10 s	R_{thJA}	34	40	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	17	17 23		

Notes:

- a. Based on $T_C = 25$ °C.
- b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 10 s
- d. Maximum under Steady State conditions is 85 °C/W.

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SPECIFICATIONS $T_J = 25$ °C, unless otherwise noted								
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit		
Static								
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			٧		
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.5		3.0	V		
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA		
Zana Oata Vallana Busin Oamani		V _{DS} = 30 V, V _{GS} = 0 V		0.18	1			
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V, T _J = 100 °C	22		100	mA		
On -State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	30			Α		
	D	V _{GS} = 10 V, I _D = 15 A		0.007	0.0085	Ω		
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 12 \text{ A}$		0.0095	0.012			
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 15 A		56		S		
Dynamic ^b								
Input Capacitance	C _{iss}			1640		pF		
Output Capacitance	C _{oss}	V _{DS} = 15 V, V _{GS} = 0 V, f = 1 MHz		380				
Reverse Transfer Capacitance	C _{rss}	1		118				
T. 10 . 0	Q _g	V _{DS} = 15 V, V _{GS} = 10 V, I _D = 15 A		25.5	38	nC		
Total Gate Charge		20 00 0		11.7	17.5			
Gate-Source Charge	Q_{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 15 \text{ A}$		5.1				
Gate-Drain Charge	Q _{gd}] [3.6				
Gate Resistance	R_{g}	f = 1 MHz		2.3	3.5	Ω		
Turn-On Delay Time	t _{d(on)}			24	36	ns		
Rise Time	t _r	V_{DD} = 15 V, R_L = 3 Ω		84	126			
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 5 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_G = 1 \Omega$		36	54			
Fall Time	t _f	1		17	26			
Turn-On Delay Time	t _{d(on)}			12	18			
Rise Time	t _r	$V_{DD} = 15 \text{ V}, R_L = 3 \Omega$		36	54			
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 5 \text{ A}, V_{GEN} = 10 \text{ V}, R_G = 1 \Omega$		36	54			
Fall Time	t _f	1		7	11			
Drain-Source Body Diode and Schottky	Characterist	tics		<u> </u>	<u>'</u>			
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			5	Α.		
Pulse Diode Forward Current ^a	I _{SM}				50	Α		
Body Diode Voltage	V _{SD}	I _S = 2 A		0.35	0.4	V		
Body Diode Reverse Recovery Time	t _{rr}			26	40	ns		
Body Diode Reverse Recovery Charge	Q _{rr}	I _F = 4 A, dl/dt = 100 A/μs, T _J = 25 °C		16	25	nC		
Reverse Recovery Fall Time	t _a]		12.5		ns		
Reverse Recovery Rise Time	t _b	1		13.5				

Notes:

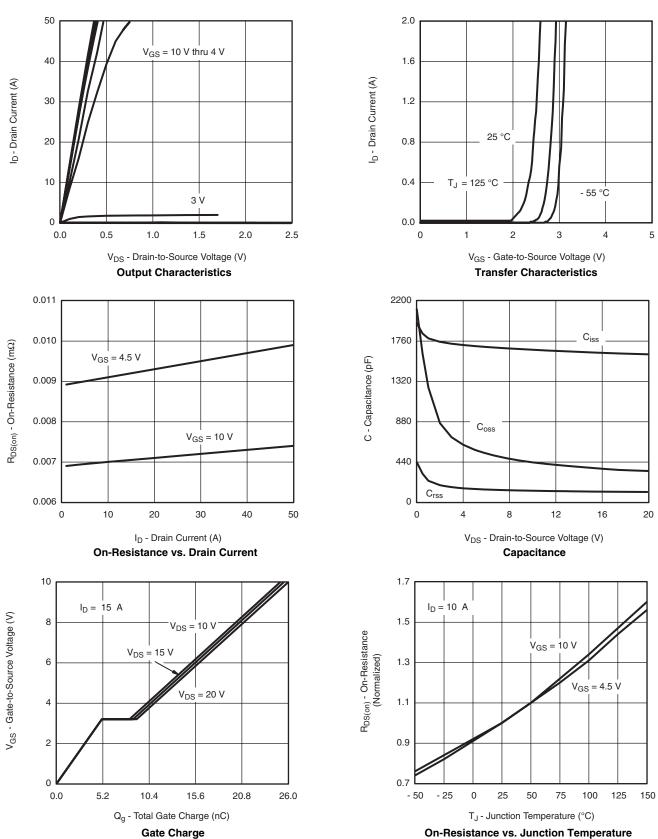
- a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



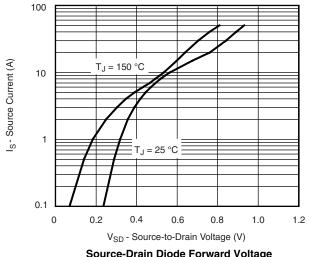


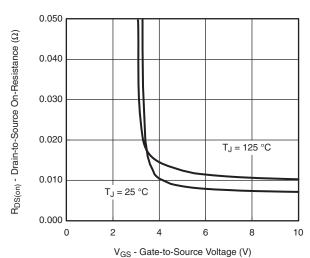
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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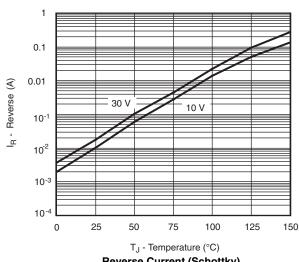
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

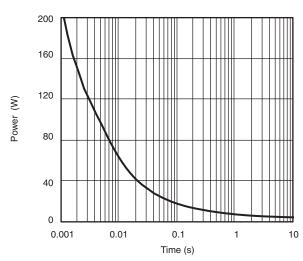




Source-Drain Diode Forward Voltage

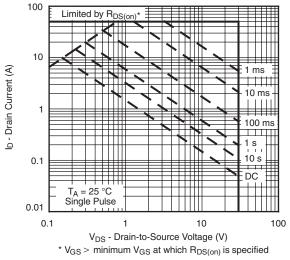






Reverse Current (Schottky)

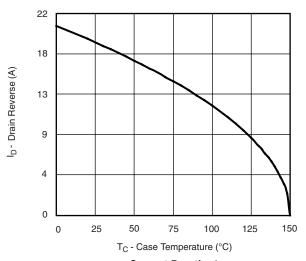
Junction-to-Ambient



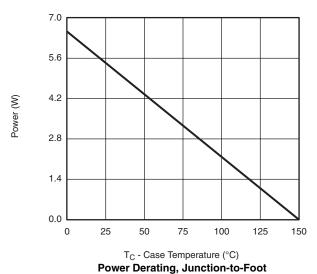


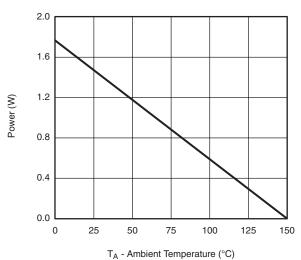


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Current Derating*





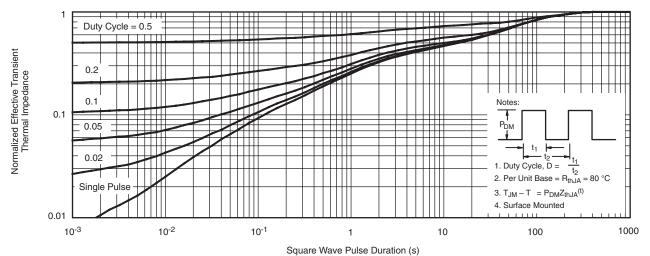
Power Derating, Junction-to-Ambient

^{*} The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

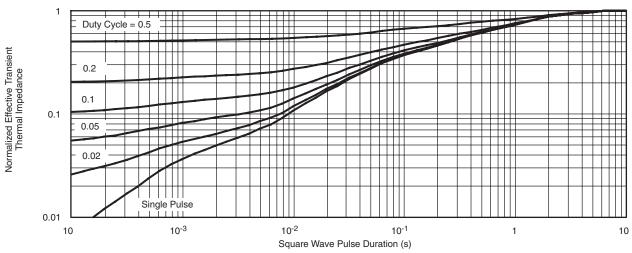
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

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