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April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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MOS FIELD EFFECT TRANSISTOR 2SJ559

P-CHANNEL MOS FIELD EFFECT TRANSISTOR FOR HIGH SPEED SWITCHING

DESCRIPTION

The 2SJ559 is a switching device which can be driven directly by a 2.5 V power source.

The 2SJ559 has excellent switching characteristics, and is suitable for use as a high-speed switching device in digital circuits.

FEATURES

- Can be driven by a 2.5 V power source.
- · Low gate cut-off voltage.

★ ORDERING INFORMATION

PART NUMBER	PACKAGE
2SJ559	SC-75 (USM)

Marking: C1

Remark

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

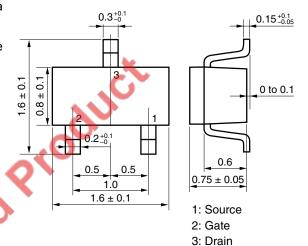
Drain to Source Voltage	Voss	-30	V
Gate to Source Voltage	Vgss	∓20	V
Drain Current (DC)	ID(DC)	∓0.1	Α
Drain Current (pulse) Note1	I _{D(pulse)}	∓0.4	Α
Total Power Dissipation Note2	Рт	200	mW
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	-55 to +150	°C

Notes 1. PW \leq 10 μ s, Duty Cycle \leq 1%

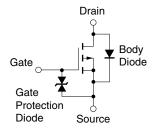
2. Mounted on ceramic substrate of 3.0 cm² x 0.64 mm

the rated voltage may be applied to this device.

★ PACKAGE DRAWING (Unit: mm)



EQUIVALENT CIRCUIT



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The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding

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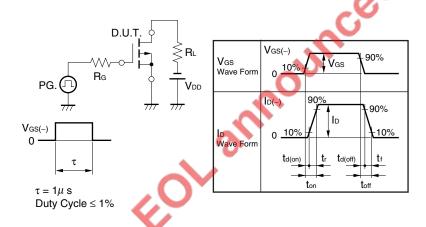


ELECTRICAL CHARACTERISTICS (TA = 25°C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V _{DS} = -30 V, V _{GS} = 0 V			-1.0	μА
Gate Leakage Current	Igss	V _{GS} = ∓20 V, V _{DS} = 0 V			∓10	μА
Gate Cut-off Voltage	V _{GS(off)}	$V_{DS} = -3.0 \text{ V}, I_{D} = -10 \mu\text{A}$	-1.0	-1.4	-1.7	V
Forward Transfer Admittance Note	y _{fs}	$V_{DS} = -3.0 \text{ V}, I_{D} = -10 \text{ mA}$	20			mS
Drain to Source On-state Resistance Note	RDS(on)1	V _{GS} = -2.5 V, I _D = -1.0 mA		23	60	Ω
	RDS(on)2	V _{GS} = -4.0 V, I _D = -10 mA		11	23	Ω
	RDS(on)3	V _{GS} = -10 V, I _D = -10 mA		6.0	13	Ω
Input Capacitance	Ciss	V _{DS} = -3.0 V		5.0		pF
Output Capacitance	Coss	V _{GS} = 0 V		15		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz	*	1.3		pF
Turn-on Delay Time	t d(on)	V _{DD} = -3.0 V	.C	140		ns
Rise Time	tr	I _D = −10 mA	2	330		ns
Turn-off Delay Time	td(off)	V _{GS} = -4.0 V		220		ns
Fall Time	tr	R _G = 10 Ω		320		ns

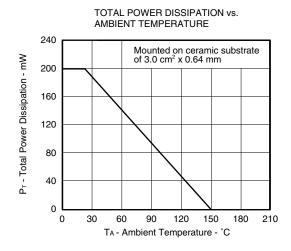
Note Plused

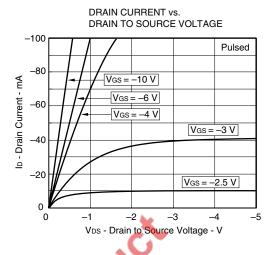
★ TEST CIRCUIT SWITCHING TIME

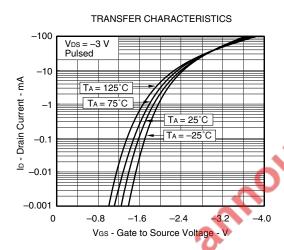


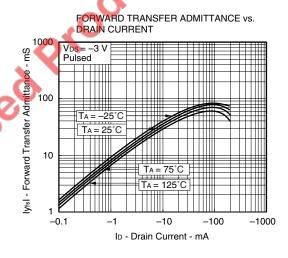


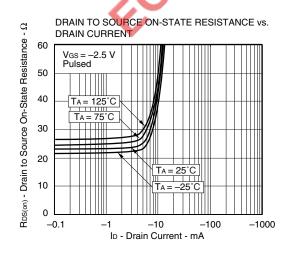
TYPICAL CHARACTERISTICS (TA = 25°C)

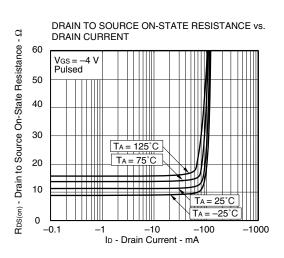




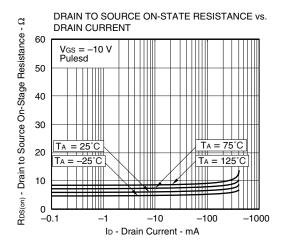


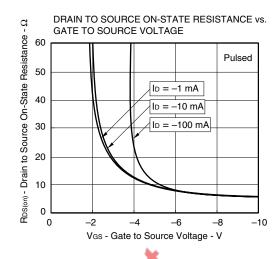


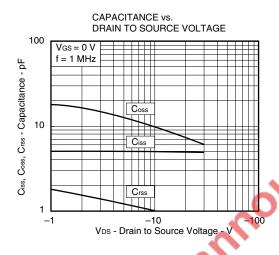


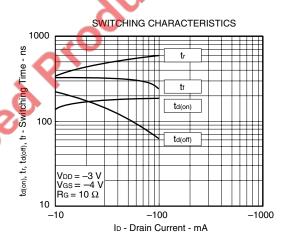


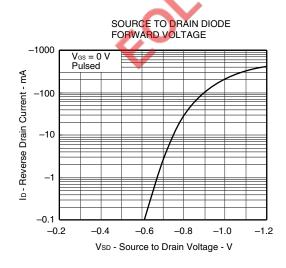
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