BC327, BC327-16, BC327-25, BC327-40

Amplifier Transistors

PNP Silicon

Features

• These are Pb-Free Devices*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector - Emitter Voltage	V _{CEO}	-45	Vdc
Collector - Emitter Voltage	V _{CES}	-50	Vdc
Emitter-Base Voltage	V _{EBO}	-5.0	Vdc
Collector Current - Continuous	Ic	-800	mAdc
Total Power Dissipation @ T _A = 25°C Derate above T _A = 25°C	P _D	625 5.0	mW mW/°C
Total Power Dissipation @ T _A = 25°C Derate above T _A = 25°C	P _D	1.5 12	W mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C

THERMAL CHARACTERISTICS

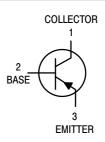
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	200	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	83.3	°C/W

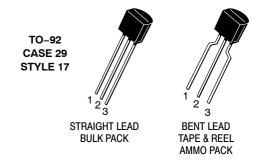
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



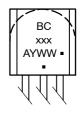
ON Semiconductor®

http://onsemi.com





MARKING DIAGRAM



BCxxx = Device Code

A = Assembly Location

Y = Year WW = Work Week

= Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

See detailed ordering, marking, and shipping information in the package dimensions section on page 4 of this data sheet.

1

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

BC327, BC327-16, BC327-25, BC327-40

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

TELESTRICAL STATIACTERISTICS (1A = 25 O UNICSS SUICIWISE HOLEU)	1			I	
Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Collector – Emitter Breakdown Voltage (I _C = -10 mA, I _B = 0)	V _{(BR)CEO}	-45	_	-	Vdc
Collector – Emitter Breakdown Voltage $(I_C = -100 \mu A, I_E = 0)$	V _{(BR)CES}	-50	_	-	Vdc
Emitter – Base Breakdown Voltage ($I_E = -10 \mu A$, $I_C = 0$)	V _{(BR)EBO}	-5.0	-	-	Vdc
Collector Cutoff Current $(V_{CB} = -30 \text{ V}, I_E = 0)$	I _{CBO}	_	-	-100	nAdc
Collector Cutoff Current (V _{CE} = -45 V, V _{BE} = 0)	I _{CES}	_	-	-100	nAdc
Emitter Cutoff Current $(V_{EB} = -4.0 \text{ V}, I_C = 0)$	I _{EBO}	_	-	-100	nAdc
ON CHARACTERISTICS					
DC Current Gain (I _C = -100 mA, V _{CE} = -1.0 V) BC327-16 BC327-26 BC327-40	5	100 100 160 250	- - -	630 250 400 630	-
$(I_C = -300 \text{ mA}, V_{CE} = -1.0 \text{ V})$		40	-	_	
Base–Emitter On Voltage ($I_C = -300 \text{ mA}, V_{CE} = -1.0 \text{ V}$)	V _{BE(on)}	_	-	-1.2	Vdc
Collector – Emitter Saturation Voltage (I _C = -500 mA, I _B = -50 mA)	V _{CE(sat)}	-	_	-0.7	Vdc
SMALL-SIGNAL CHARACTERISTICS		•	•		
Output Capacitance $(V_{CB} = -10 \text{ V}, I_E = 0, f = 1.0 \text{ MHz})$	C _{ob}	_	11	-	pF
Current – Gain – Bandwidth Product (I _C = –10 mA, V _{CE} = –5.0 V, f = 100 MHz)	f _T	_	260	-	MHz

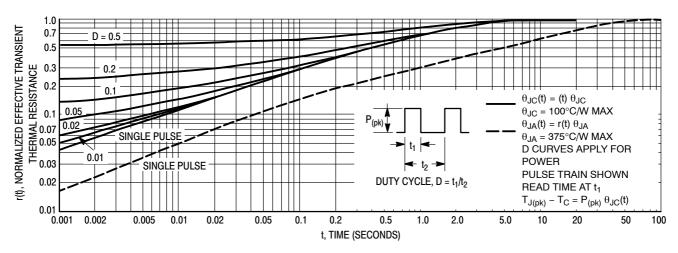


Figure 1. Thermal Response

BC327, BC327-16, BC327-25, BC327-40

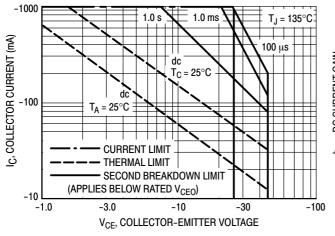


Figure 2. Active Region - Safe Operating Area

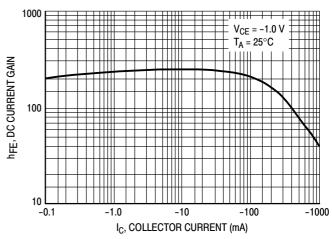


Figure 3. DC Current Gain

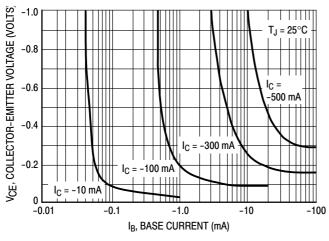


Figure 4. Saturation Region

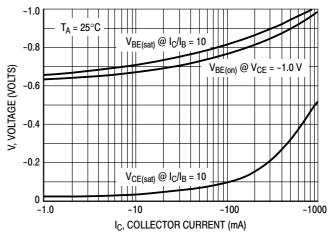


Figure 5. "On" Voltages

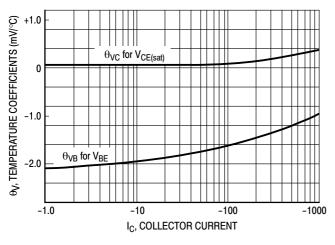


Figure 6. Temperature Coefficients

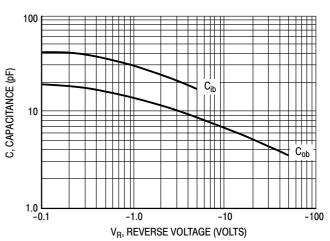


Figure 7. Capacitances

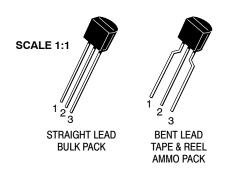
BC327, BC327-16, BC327-25, BC327-40

ORDERING INFORMATION

Device Order Number	Specific Device Marking	Package Type	Shipping [†]
BC327G	7	TO-92 Straight Lead (Pb-Free)	5000 Units / Bulk
BC327RL1G	327	TO-92 Bent Lead (Pb-Free)	2000 / Tape & Reel
BC327-025G	327	TO-92 Straight Lead (Pb-Free)	5000 Units / Bulk
BC327-25RL1G	7–25	TO-92 Bent Lead (Pb-Free)	2000 / Tape & Reel
BC327-25ZL1G	32725	TO-92 Bent Lead (Pb-Free)	2000 / Tape & Ammo Box
BC327-40ZL1G	7–40	TO-92 Bent Lead (Pb-Free)	2000 / Tape & Ammo Box

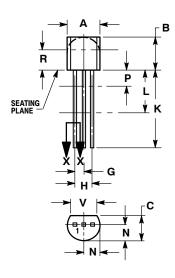
[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.





TO-92 (TO-226) CASE 29-11 **ISSUE AM**

DATE 09 MAR 2007

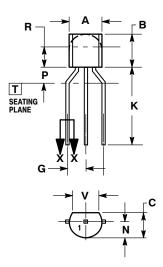


STRAIGHT LEAD **BULK PACK**



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
 4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIN	IETERS	
DIM	MIN	MAX	MIN	MAX	
Α	0.175	0.205	4.45	5.20	
В	0.170	0.210	4.32	5.33	
С	0.125	0.165	3.18	4.19	
D	0.016	0.021	0.407	0.533	
G	0.045	0.055	1.15	1.39	
Н	0.095	0.105	2.42	2.66	
J	0.015	0.020	0.39	0.50	
K	0.500		12.70		
L	0.250		6.35		
N	0.080	0.105	2.04	2.66	
Р		0.100		2.54	
R	0.115		2.93		
٧	0.135		3.43		



BENT LEAD TAPE & REEL AMMO PACK



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 2. CONTROLLING DIMENSION: MILLIMETERS.
 3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
 4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	MILLIMETERS				
DIM	MIN	MAX			
Α	4.45	5.20			
В	4.32	5.33			
С	3.18	4.19			
D	0.40	0.54			
G	2.40	2.80			
J	0.39	0.50			
K	12.70				
N	2.04	2.66			
P	1.50	4.00			
R	2.93				
V	3.43				

STYLES ON PAGE 2

DOCUMENT NUMBER:	98ASB42022B	Electronic versions are uncontrolle	•	
STATUS:	ON SEMICONDUCTOR STANDARD	accessed directly from the Document versions are uncontrolled except		
NEW STANDARD:		"CONTROLLED COPY" in red.		
DESCRIPTION:	TO-92 (TO-226)		PAGE 1 OF 3	

TO-92 (TO-226) CASE 29-11

ISSUE AM

DATE 09 MAR 2007

STYLE 1: PIN 1. 2. 3.	EMITTER BASE COLLECTOR	STYLE 2: PIN 1. 2. 3.	BASE EMITTER COLLECTOR	STYLE 3: PIN 1. 2. 3.	ANODE ANODE CATHODE	STYLE 4: PIN 1. 2. 3.	CATHODE CATHODE ANODE	STYLE 5: PIN 1. 2. 3.	DRAIN SOURCE GATE
2. 3.	SOURCE & SUBSTRATE DRAIN	2. 3.	DRAIN GATE	2. 3.	GATE SOURCE & SUBSTRATE	2. 3.	EMITTER BASE 2	2. 3.	ANODE
2.	ANODE CATHODE & ANODE CATHODE	STYLE 12: PIN 1. 2. 3.	MAIN TERMINAL 1 GATE MAIN TERMINAL 2	STYLE 13: PIN 1. 2. 3.	ANODE 1 GATE CATHODE 2	STYLE 14: PIN 1. 2. 3.	EMITTER COLLECTOR BASE	STYLE 15: PIN 1. 2. 3.	ANODE 1 CATHODE ANODE 2
STYLE 16: PIN 1. 2. 3.	ANODE GATE CATHODE	STYLE 17: PIN 1. 2. 3.	COLLECTOR BASE EMITTER	STYLE 18: PIN 1. 2. 3.	ANODE CATHODE NOT CONNECTED	STYLE 19: PIN 1. 2. 3.	GATE ANODE CATHODE	STYLE 20: PIN 1. 2. 3.	NOT CONNECTED CATHODE ANODE
2.	COLLECTOR EMITTER BASE	STYLE 22: PIN 1. 2. 3.	GATE	2	GATE SOURCE DRAIN	PIN 1. 2.	EMITTER COLLECTOR/ANODE CATHODE	PIN 1.	MT 1 GATE
	V _{CC} GROUND 2 OUTPUT	STYLE 27: PIN 1. 2. 3.	MT SUBSTRATE MT	STYLE 28: PIN 1. 2. 3.	CATHODE ANODE GATE	STYLE 29: PIN 1. 2. 3.	NOT CONNECTED ANODE CATHODE	STYLE 30: PIN 1. 2. 3.	DRAIN GATE SOURCE
	GATE	PIN 1. 2.	BASE COLLECTOR EMITTER	PIN 1.	RETURN INPUT	2.	INPUT GROUND LOGIC	2.	

DOCUMENT NUMBER:	98ASB42022B	Electronic versions are uncontrolle	'
STATUS:	ON SEMICONDUCTOR STANDARD	accessed directly from the Document versions are uncontrolled except	' '
NEW STANDARD:		"CONTROLLED COPY" in red.	
DESCRIPTION:	TO-92 (TO-226)		PAGE 2 OF 3



DOCUMENT	NUMBER:
98ASB42022	В

PAGE 3 OF 3

ISSUE	REVISION	DATE
AM	ADDED BENT-LEAD TAPE & REEL VERSION. REQ. BY J. SUPINA.	09 MAR 2007

ON Semiconductor and una are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. arising out of the application of use of any product or circuit, and specifications can and do vary in different applications and actual performance may vary over time. All operating parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death. associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

onsemi, ONSEMI., and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems. or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

ADDITIONAL INFORMATION

TECHNICAL PUBLICATIONS:

 $\textbf{Technical Library:} \ \underline{www.onsemi.com/design/resources/technical-documentation}$

onsemi Website: www.onsemi.com

ONLINE SUPPORT: www.onsemi.com/support

For additional information, please contact your local Sales Representative at

www.onsemi.com/support/sales