

RM45P20D3

P-Channel Enhancement Mode Power MOSFET

Description

The RM45P20D3 uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

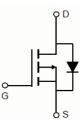
General Features

• $V_{DS} = -19V, I_D = -45A$ $R_{DS(ON)} < 7m\Omega @ V_{GS} = -4.5V$ $R_{DS(ON)} < 9m\Omega @ V_{GS} = -2.5V$ $R_{DS(ON)} < 12m\Omega @ V_{GS} = -1.8V$

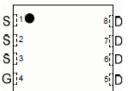
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation

Application

- Load switch
- Battery protection



Schematic diagram



Pin Assignment



DFN 3.3x3.3 EP top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
45P20	RM45P20D3	DFN 3.3x3.3 EP	-	-	-

Absolute Maximum Ratings (T_c=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	Vds	-19	V	
Gate-Source Voltage	Vgs	±12	V	
Drain Current-Continuous	Ι _D	-45	A	
Drain Current-Continuous(T _C =100℃)	I _D (100℃)	-35	A	
Pulsed Drain Current	I _{DM}	-200	A	
Maximum Power Dissipation	PD	80	W	
Derating factor		0.64	W/°C	
Operating Junction and Storage Temperature Range	T _J ,T _{STG}	-55 To 150	°C	

Thermal Characteristic

Thermal Resistance, Junction-to-Case ^(Note 2)	R _{θJC}	1.6	°C/W
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Electrical Characteristics (T_c=25 $^\circ\!\mathrm{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit	
Off Characteristics	·		•	•		•	
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =-250µA	-19	-	-	V	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-16V,V _{GS} =0V	-	-	1	μA	
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±12V,V _{DS} =0V	-	-	±100	nA	
On Characteristics (Note 3)			•	•		•	
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =-250µA	-0.4	-0.6	-1.0	V	
	R _{DS(ON)}	V _{GS} =-4.5V, I _D =-20A	-	5.8	7	1	
Drain-Source On-State Resistance		V _{GS} =-2.5V, I _D =-20A	-	7.2	9	mΩ	
		V _{GS} =-1.8V, I _D =-20A		9	12		
Forward Transconductance	g fs	V _{DS} =-5V,I _D =-20A	80	-	-	S	
Dynamic Characteristics (Note4)	I		•			•	
Input Capacitance	C _{lss}		-	3500	-	PF	
Output Capacitance	C _{oss}	V_{DS} =-10V, V_{GS} =0V,	-	577	-	PF	
Reverse Transfer Capacitance	C _{rss}	F=1.0MHz	-	445	-	PF	
Switching Characteristics (Note 4)			•	•		•	
Turn-on Delay Time	t _{d(on)}		-	18	-	nS	
Turn-on Rise Time	tr	V _{DD} =-10V, R _{GEN} =3Ω V _{GS} =-4.5V,R _L =0.5Ω	-	42	-	nS	
Turn-Off Delay Time	t _{d(off)}		-	85	-	nS	
Turn-Off Fall Time	t _f		-	23	-	nS	
Total Gate Charge	Qg	$y_{1} = 40y_{1} = 000$	-	55	-	nC	
Gate-Source Charge	Q _{gs}	V_{DS} =-10V,I _D =-20A,	-	10	-	nC	
Gate-Drain Charge	Q _{gd}	V _{GS} =-4.5V	-	15	-	nC	
Drain-Source Diode Characteristics			•	•		•	
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =-20A	-	-	-1.2	V	
Diode Forward Current (Note 2)	Is		-	-	-45	А	
Reverse Recovery Time	t _{rr}	TJ = 25°C, IF = -10A	-	47	-	nS	
Reverse Recovery Charge	Qrr	di/dt = 100A/µs ^(Note3)	-	53	-	nC	
Forward Turn-On Time	t _{on}	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)					

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

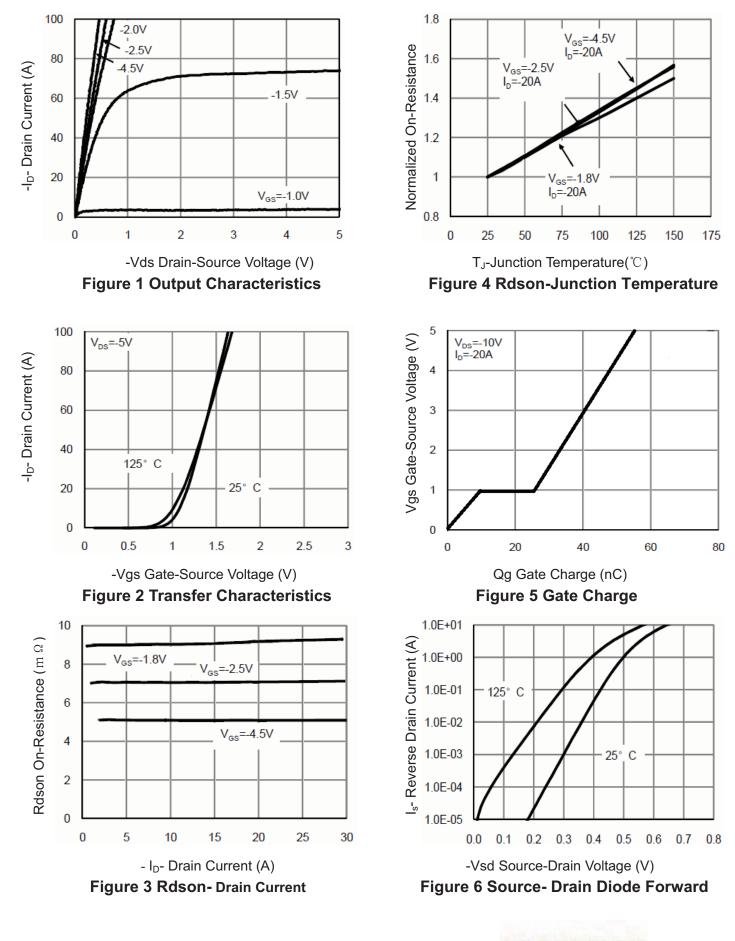
2. Surface Mounted on FR4 Board, $t \le 10$ sec.

3. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.

4. Guaranteed by design, not subject to production



RATING AND CHARACTERISTICS CURVES (RM45P20D3)



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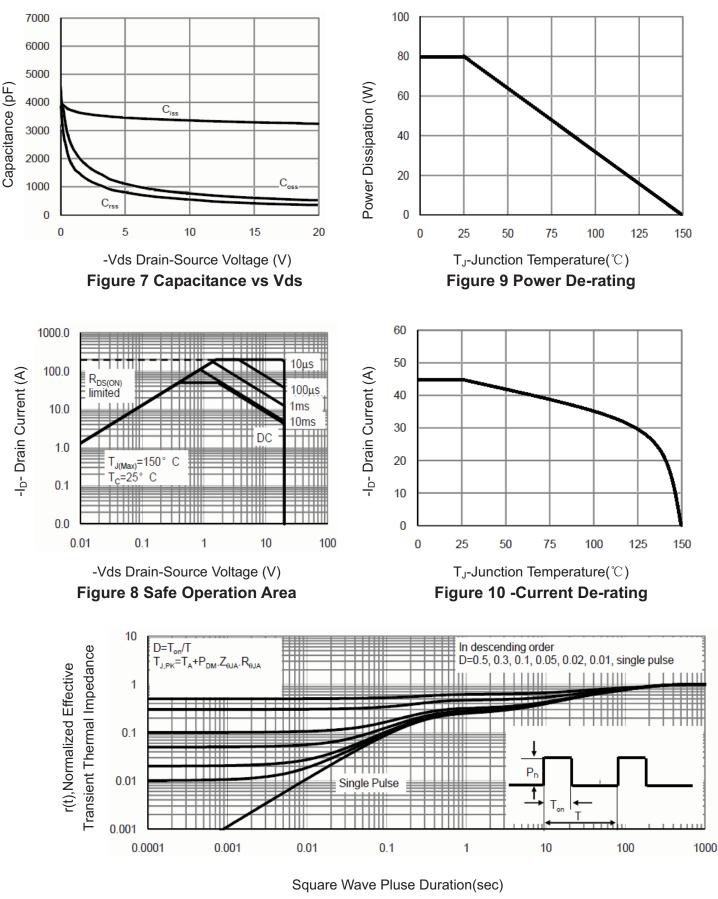
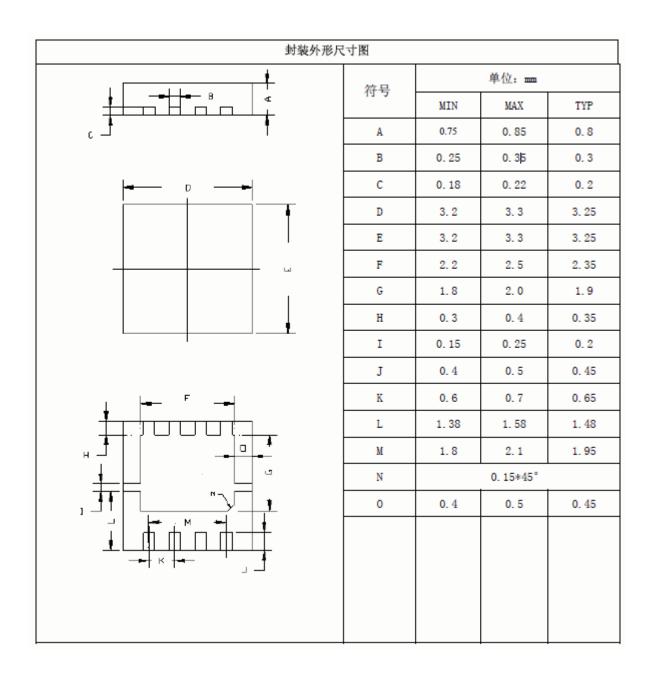


Figure 11 Normalized Maximum Transient Thermal Impedance

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