

$\mu HVIC^{TM}$

Features

- Up to 480V voltage capability
- Constant output current (4.5mA)
- Programmable upper threshold level
- Fixed lower threshold (4.2V)
- ENN input
- Over-temperature shutdown
- Ultra-low off current (2.5μA)
- Internal 20.8V clamp on VOUT pin
- Excellent latch immunity on all inputs & outputs
- Integrated ESD protection on all pins
- 5-pin SOT-23 package

Applications

- High-voltage start-up
- Low standby power circuits
- General purpose switched mode power electronics

High-Voltage Start-Up IC

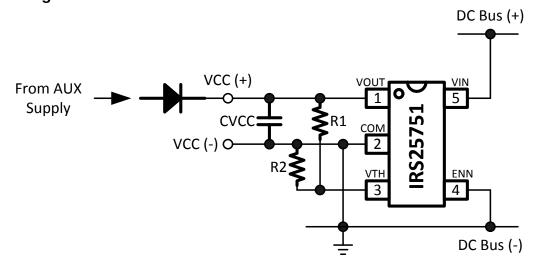
Description

The IRS25751 is a 480V high-voltage start-up IC ideal for supplying initial supply starting current from a high-voltage bus. The IRS25751 supplies a constant current during start-up and then consumes ultra-low standby (off) current. Additional features include programmability of the upper turn-off threshold, an ENN input, and overtemperature protection. IR's proprietary HVIC technology provides robust operation from high input voltage levels with simple yet flexible features.

Package Options



Application Diagram

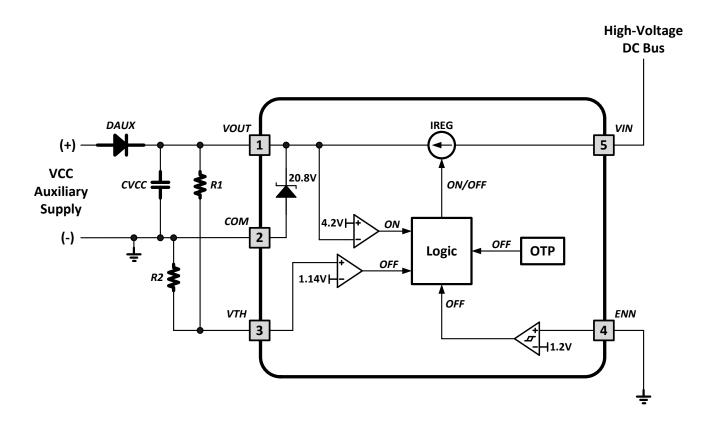


Ordering Information

Dana Bart Marchan		Standar	d Pack		
Base Part Number	Package Type	Form	Quantity	Orderable Part Number	
IRS25751LPBF	5L-SOT-23	Tape and Reel	3000	IRS25751LTRPBF	



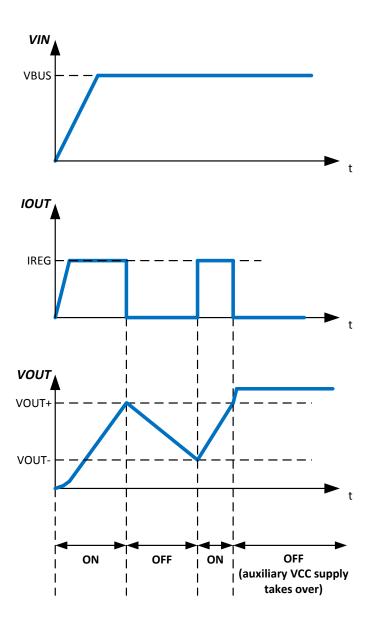
Functional Block Diagram



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Timing Diagram (VIN, IOUT, VOUT)





Qualification Information[†]

			Industrial ^{††}			
		,				
			(per JEDEC JESD 47E)			
Qualification Level		Comments: This fa	Comments: This family of ICs has passed JEDEC's			
		Industrial qualification	Industrial qualification. IR's Consumer qualification level is			
		granted by extension	granted by extension of the higher Industrial level.			
Majatura Canajtivitu	Laval	COT 22	MSL1 ^{†††}			
Moisture Sensitivity Level		SOT-23	(per IPC/JEDEC J-STD-020C)			
	Machine Model		Class B			
ESD	Macrime Model	(per JEDEC s	(per JEDEC standard EIA/JESD22-A115-A)			
E3D	Human Pady Madal		Class 2			
	Human Body Model	(per EIA/JED	er EIA/JEDEC standard JESD22-A114-B)			
IC Latch-Up Test			Class I, Level A			
			(per JESD78A)			
RoHS Compliant			Yes			

- † Qualification standards can be found at International Rectifier's web site http://www.irf.com/
- †† Higher qualification ratings may be available should the user have such requirements. Please contact your International Rectifier sales representative for further information.
- ††† Higher MSL ratings may be available for the specific package types listed here. Please contact your International Rectifier sales representative for further information.



Absolute Maximum Ratings

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. All voltage parameters are absolute voltages referenced to COM, all currents are defined positive into any pin. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions.

Symbol	Definition		Min.	Max.	Units
VIN	VIN pin voltage			625	
VOUT	VOUT pin voltage			VCLAMP [†]	V
VTH	VTH pin voltage		-0.3	VOUT + 0.3	
ENN	ENN pin voltage				
RΘja	Thermal resistance, junction to ambient 5L-SOT-23			191	ºC/W
T_J	Junction temperature		-55	150	
T _S	Storage temperature		-33	130	∘C
T _L	IC Pin temperature (soldering, 10 seconds)			300	

[†] This IC contains voltage clamp structures between the VOUT and COM pins that has a nominal breakdown voltage of 20.8V. Please note that this pin should not be driven by a DC, low impedance power source greater than the VCLAMP specified in the Electrical Characteristics section.

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Recommended Operating Conditions

For proper operation the device should be used within the recommended conditions.

Symbol	Definition	Min.	Max.	Units
VIN	VIN pin voltage	-0.3	480	
VOUT	VOUT pin voltage		VCLAMP	V
VTH	VTH pin voltage	COM		\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
VENN	ENN pin voltage			
TJ	Junction temperature	-40	125	ōС

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Recommended Component Values

Symbol	Component	Min.	Тур.	Max.	Units
R1	VTH pin programming resistor		1.0		Meg Ohm
R2	VTH pin programming resistor		90.9		K Ohm
CVCC	VOUT pin external capacitor		10.0		μF



Electrical Characteristics

R1 = 1 Meg Ohm, R2 = 90.9 K Ohm, $Ta = 25 \, ^{\circ}C$ unless otherwise specified. All parameters are referenced to COM pin.

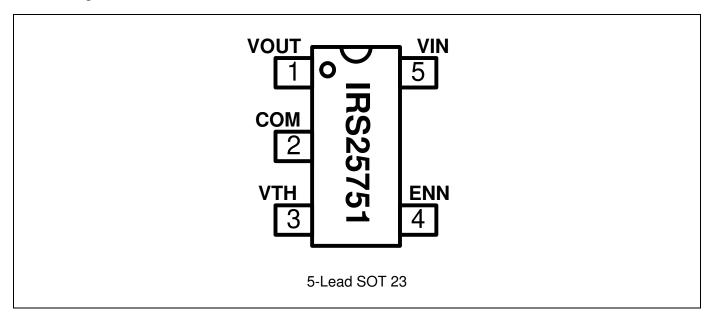
Symbol	Definition Min Typ		Тур	Max	Units	Test Conditions	
VOUT Pin Characteristics							
VOUT-	VOUT pin falling turn-on threshold	3.78	4.2	4.62			
VCLAMP	VOUT pin internal clamp voltage	19.8	20.8	21.8	V	VIN = COM, IVOUT = 5mA	
IREG	Regulated output current		4.6		mA	VIN = 400V, VOUT = COM	
VIN Pin Char	VIN Pin Characteristics						
I_VIN_OFF	VIN pin off-state leakage current		2.5		μΑ	VIN = 400V	
VTH Pin Cha	racteristics	•					
VTH+	VTH pin rising turn-off threshold	1.08	1.14	1.2	V		
ENN Pin Cha	racteristics						
VENN+	ENN pin rising disable threshold		1.2		V		
Over-Temperature Protection							
TjSD	Junction temperature thermal shutdown		155		. ºC		
TjSD_HYS	Junction temperature thermal shutdown hysteresis		50		J		



Pin Definitions

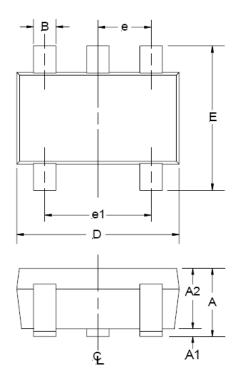
Pin	Name	Description
1	VOUT	Output voltage and current
2	СОМ	IC ground
3	VTH	Programmable upper VOUT turn-off threshold input
4	ENN	Enable pin (high level disables IC)
5	VIN	High-voltage input

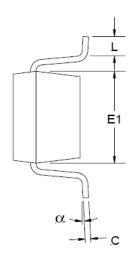
Pin Assignments





Package Details: 5-Pin SOT23



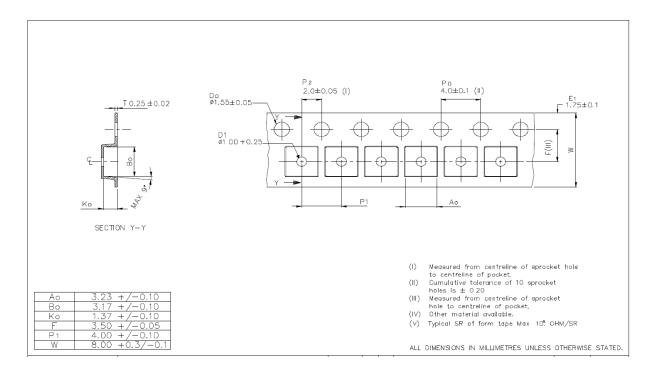


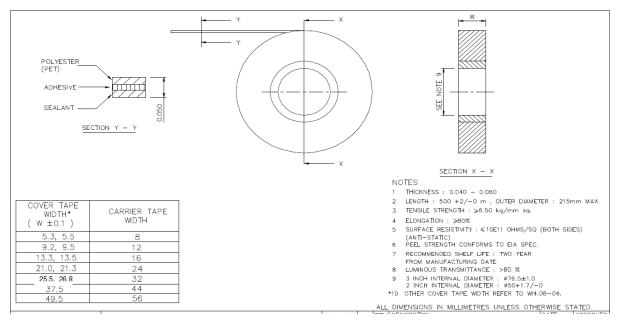
SYMBOL	MIN	MAX	
Α	0.90	1.45	
A1	0.00	0.15	
A2	0.90	1.30	
В	0.25	0.50	
С	0.09	0.20	
D	2.80	3.00	
E	2.60	3.00	
E1	1.50	1.75	
е	0.95 REF		
e1	1.90 REF		
L	0.35	0.55	
α	08	108	

NOTE: ALL MEASUREMENTS ARE IN MILLIMETERS.



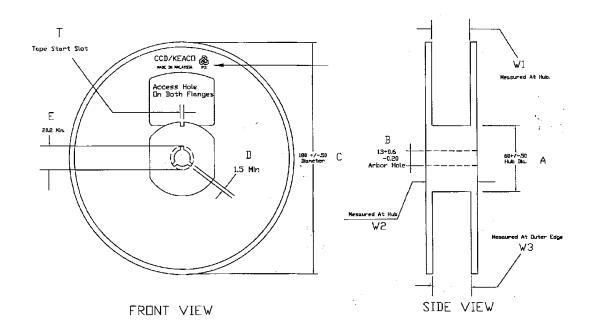
Tape and Reel Details: 5-Pin SOT23

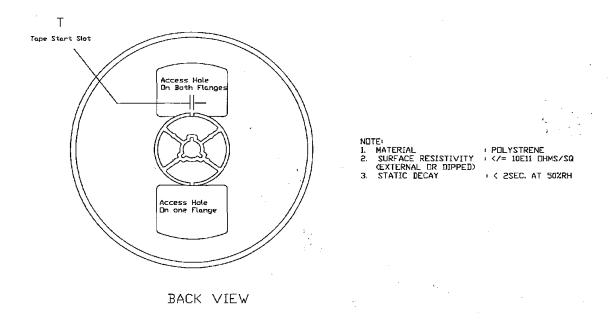






Tape and Reel Details: 5-Pin SOT23

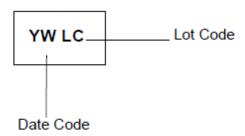




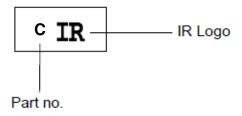


Part Marking Information: 5-Pin SOT23

Top Marking



Bottom Marking



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