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LM317 3-Terminal Positive Adjustable Regulator

FAIRCHILD

SEMICONDUCTOR

LM317 3-Terminal Positive Adjustable Regulator

General Description

This monolithic integrated circuit is an adjustable 3-terminal positive voltage regulator designed to supply more than 1.5A of load current with an output voltage adjustable over a 1.2 to 37V. It employs internal current limiting, thermal shut-down and safe area compensation.

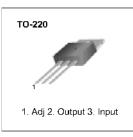
Features

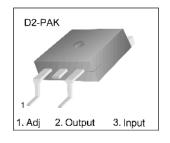
- Output Current In Excess of 1.5A
- Output Adjustable Between 1.2V and 37V
- Internal Thermal Overload Protection
- Internal Short Circuit Current Limiting
- Output Transistor Safe Operating Area Compensation
- TO-220 Package
- D2 PAK Package

Ordering Code:

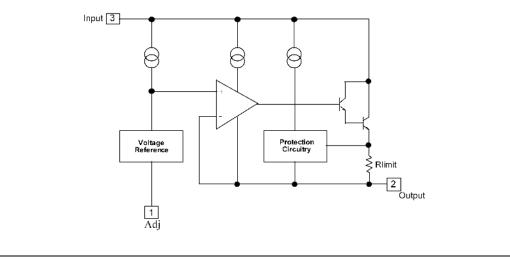
Product Number	Package	Operating Temperature
LM317T	TO-220	0°C to +125°C
LM317D2TXM	D2 PAK	0°C to +125°C

Connection Diagrams





Internal Block Diagram



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Absolute Maximum Ratings

-			
Parameter	Symbol	Value	Unit
Input-Output Voltage Differential	V _I - V _O	40	V
Lead Temperature	T _{LEAD}	230	°C
Power Dissipation	PD	Internally limited	W
Operating Junction Temperature Range	Tj	0 ~ +125	°C
Storage Temperature Range	T _{STG}	-65 ~ +125	°C
Temperature Coefficient of Output Voltage	ΔV _O /ΔT	±0.02	% / °C

Note 1: Absolute Maximum Ratings: are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

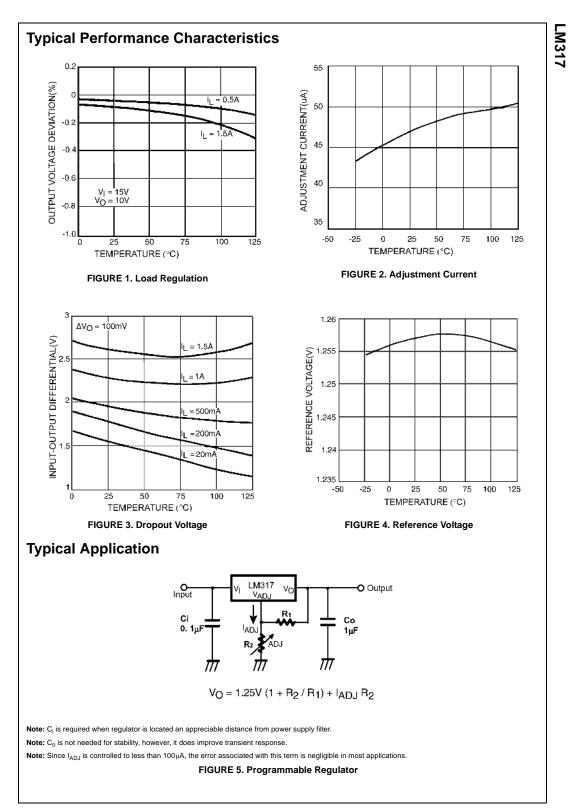
Electrical Characteristic

(V_I - V_O = 5V, I_O = 0.5A, 0°C \leq T_J \leq + 125°C, I_{MAX} = 1.5A, P_{DMAX} = 20W, unless otherwise specified)

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Line Regulation (Note 2)	Rline	$T_A = +25^{\circ}C$		0.01	0.04	%/V
		$3V \le V_I - V_O \le 40V$		0.01	0.04	% / V
		$3V \le V_I - V_O \le 40V$	-	0.02	0.07	% / V
Load Regulation (Note 2)	Rload	$T_A = +25^{\circ}c, \ 10mA \leq I_O \leq I_{MAX}$				
		V _O < 5V	-	18.0	25.0	mV% / V _O
		$V_O \ge 5V$		0.4	0.5	
		$10mA \le I_O \le I_{MAX}$				
		V _O < 5V	-	40.0	70.0	mV% / V _O
		$V_O \ge 5V$		0.8	1.5	
Adjustable Pin Current	I _{ADJ}	-	-	46.0	100	μA
Adjustable Pin Current Change	ΔI_{ADJ}	$3V \leq V_I - V_O \leq 40V$		2.0	5.0	
		$10mA \leq I_O \leq I_{MAX} \ P_D \leq P_{MAX}$	-	2.0	5.0	μA
Reference Voltage	V _{REF}	$3V \le V_{IN}$ - $V_O \le 40V$				
		$10mA \le I_O \le I_{MAX}$	1.20	1.25	1.30	V
		$P_D \le P_{MAX}$				
Temperature Stability	ST_T	-	-	0.7	-	% / V _O
Minimum Load Current to Maintain Regulation	I _{L(MIN)}	$V_{I} - V_{O} = 40V$	-	3.5	12.0	mA
Maximum Output Current	I _{O(MAX)}	$V_{I} - V_{O} \le 15V$, $P_{D} \le P_{MAX}$	1.0	2.2		
		V_{I} - $V_{O} \leq 40V$, $P_{D} \leq P_{MAX}$		0.3	-	A
		$T_A = 25 ^{\circ}C$				
RMS Noise,% of V _{OUT}	eN	$T_A = +25^{o}C,\ 10Hz \leq f \leq 10KHz$	-	0.003	0.01	% / V _O
Ripple Rejection	RR	$V_O = 10V$, f = 120Hz without C_{ADJ}	66.0	60.0	-	dB
		$C_{ADJ} = 10 \mu F$ (Note 3)		75.0		uь
Long-Term Stability, T _J = T _{HIGH}	ST	$T_A = +25^{\circ}C$ for end point measurements, 1000HR	-	0.3	1.0	%
Thermal Resistance Junction to Case	$R_{\theta JC}$	-	-	5.0	-	°C / W

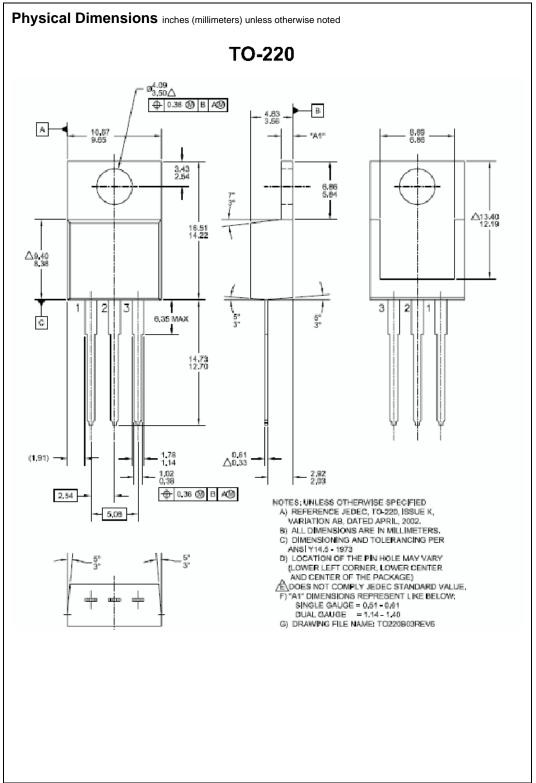
Inermal Resistance Junction to Case
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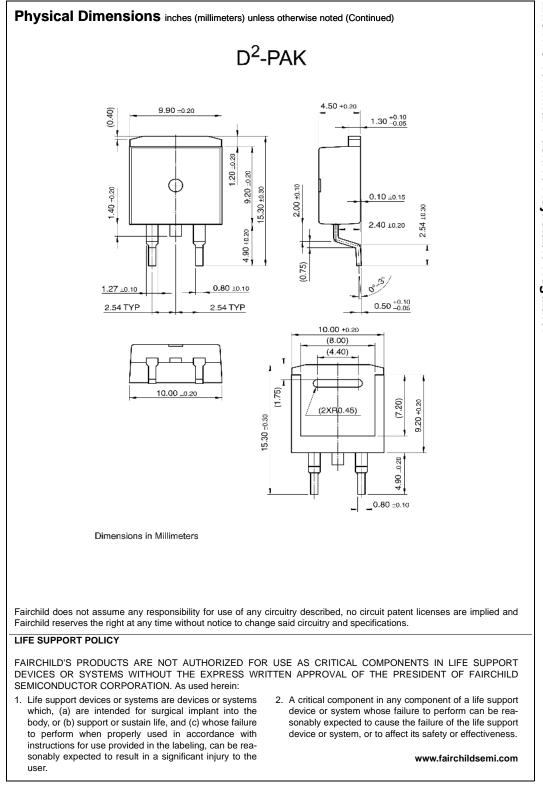
Note 3: CADJ, when used, is connected between the adjustment pin and ground.



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