

## 2A, 20V - 40V Schottky Barrier Surface Mount Rectifier

#### FEATURES

- AEC-Q101 qualified
- Very low profile typical height of 0.68mm
- Low power loss, high efficiency
- Ideal for automated placement
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

#### **APPLICATIONS**

- Low voltage, high freq. inverter
- DC/DC converter
- Freewheeling diodes
- Reverse battery protection
- Car lighting

### **MECHANICAL DATA**

- Case: Micro SMA
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Polarity: Indicated by cathode band
- Weight: 0.006g (approximately)

KEY PARAMETERS			
PARAMETER	VALUE	UNIT	
I <sub>F</sub>	2	А	
V <sub>RRM</sub>	20 - 40	V	
I <sub>FSM</sub>	25	А	
T <sub>J MAX</sub>	150	°C	
Package	Micro SMA		
Configuration	Single die		





ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25°C unless otherwise noted)					
PARAMETER	SYMBOL	SS22MH	SS23MH	SS24MH	UNIT
Marking code on the device		D	E	F	
Repetitive peak reverse voltage	V <sub>RRM</sub>	20	30	40	V
Reverse voltage, total rms value	V <sub>R(RMS)</sub>	14	21	28	V
Forward current	I <sub>F</sub>	2		Α	
Surge peak forward current 8.3ms single half sine wave superimposed on rated load	I <sub>FSM</sub>	25		А	
Junction temperature	TJ	-55 to +150		°C	
Storage temperature	T <sub>STG</sub>	-55 to +150 °		°C	



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THERMAL PERFORMANCE			
PARAMETER	SYMBOL	ТҮР	UNIT
Junction-to-lead thermal resistance	R <sub>ejl</sub>	15	°C/W
Junction-to-ambient thermal resistance	R <sub>eja</sub>	105	°C/W
Junction-to-case thermal resistance	R <sub>eJC</sub>	20	°C/W

ELECTRICAL SPECIFICATIONS (T <sub>A</sub> = 25°C unless otherwise noted)					
PARAMETER	CONDITIONS	SYMBOL	ТҮР	MAX	UNIT
Forward voltage <sup>(1)</sup>	$I_F = 2A, \ T_J = 25^\circ C$	V	-	0.60	V
	$I_F = 2A, T_J = 125^{\circ}C$	V <sub>F</sub>	-	0.55	V
Reverse current @ rated $V_R^{(2)}$	T <sub>J</sub> = 25°C	I <sub>R</sub>	-	150	μA
	T <sub>J</sub> = 125°C		-	15	mA
Junction capacitance	1MHz, V <sub>R</sub> = 4.0V	CJ	35	-	pF

#### Notes:

1. Pulse test with PW = 0.3ms

2. Pulse test with PW = 30ms

ORDERING INFORMATION		
ORDERING CODE <sup>(1)</sup>	PACKAGE	PACKING
SS2xMH	Micro SMA	12,000 / Tape & Reel

Notes:

1. "x" defines voltage from 20V(SS22MH) to 40V(SS24MH)



## **CHARACTERISTICS CURVES**

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$ 

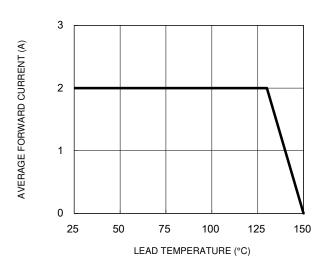
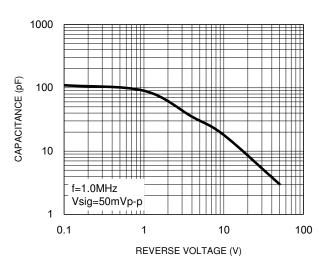
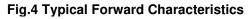


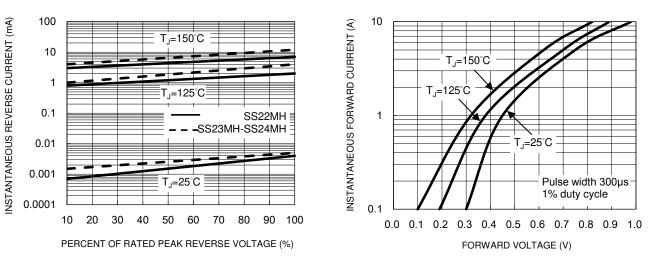
Fig.1 Forward Current Derating Curve

#### **Fig.3 Typical Reverse Characteristics**



#### **Fig.2 Typical Junction Capacitance**





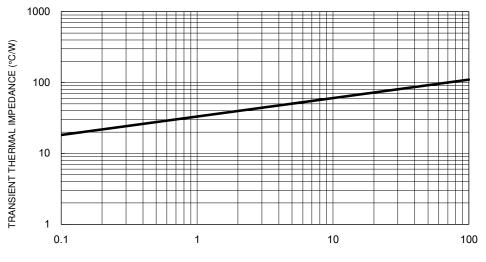
## 30 PEAK FORWARD SURGE CURRENT (A) 8.3ms single half sine wave 25 20 15 10 5 0 1 10 100 NUMBER OF CYCLES AT 60 Hz

#### Fig.5 Maximum Non-Repetitive Forward Surge Current



## **CHARACTERISTICS CURVES**

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$ 



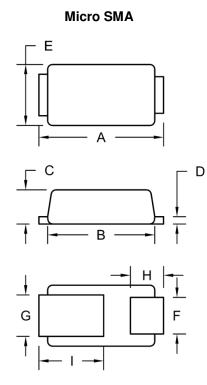
#### Fig.6 Typical Transient Thermal Impedance

PULSE DURATION (s)

# **SS22MH – SS24MH** Taiwan Semiconductor

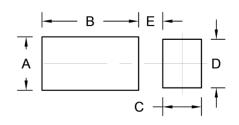


## **PACKAGE OUTLINE DIMENSIONS**



DIM.	Unit (mm)		Unit	(inch)	
DIN.	Min.	Max.	Min.	Max.	
А	2.30	2.70	0.091	0.106	
В	2.10	2.30	0.083	0.091	
С	0.63	0.73	0.025	0.029	
D	0.10	0.20	0.004	0.008	
E	1.15	1.35	0.045	0.053	
F	0.65	0.85	0.026	0.034	
G	0.75	0.95	0.030	0.037	
Н	0.55	0.75	0.022	0.030	
I	1.10	1.50	0.043	0.059	

## SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
A	1.10	0.043
В	2.00	0.079
С	0.80	0.031
D	1.00	0.039
E	0.50	0.020

## **MARKING DIAGRAM**



P/N	= Marking Code
YW	= Data Code



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